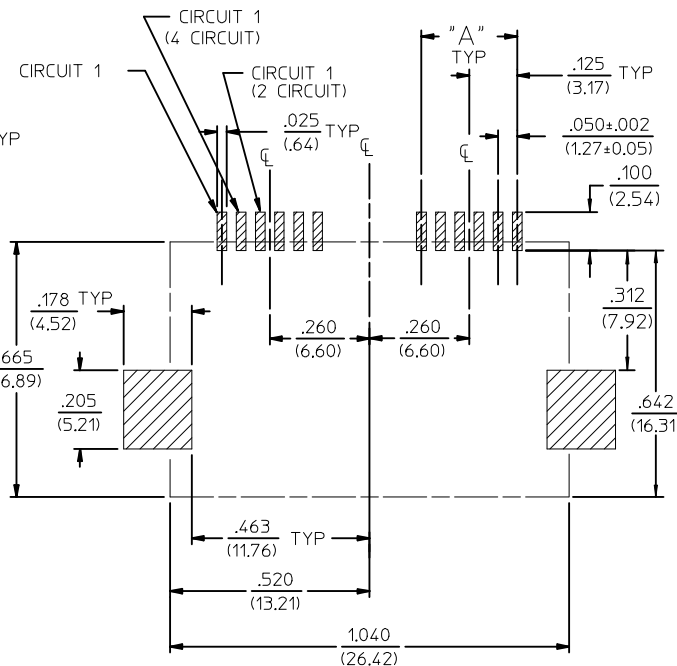
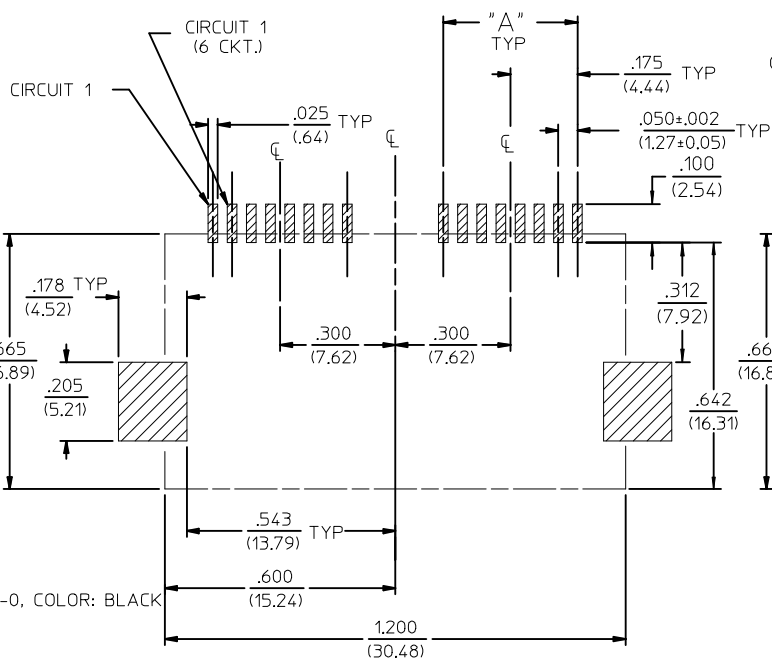
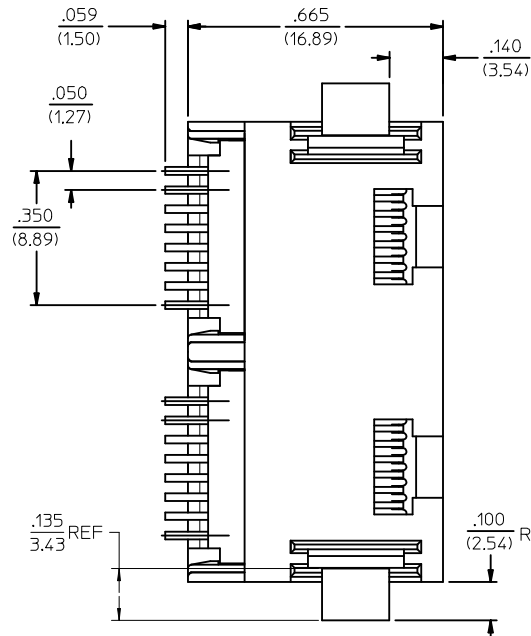
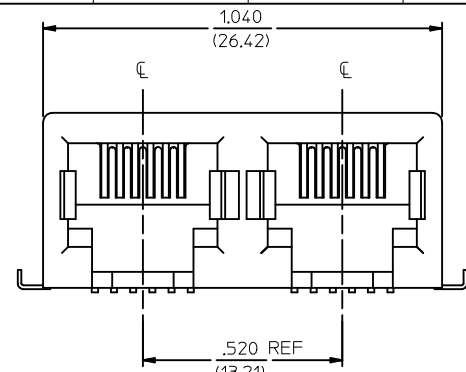
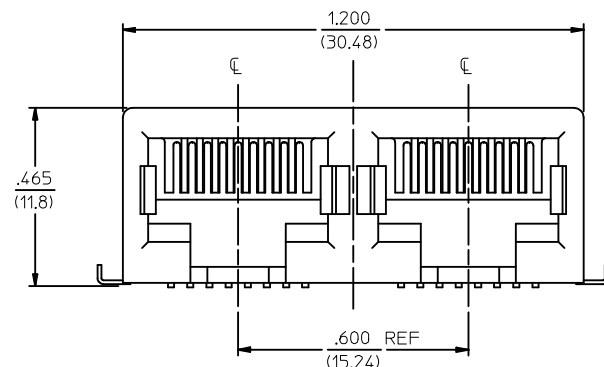
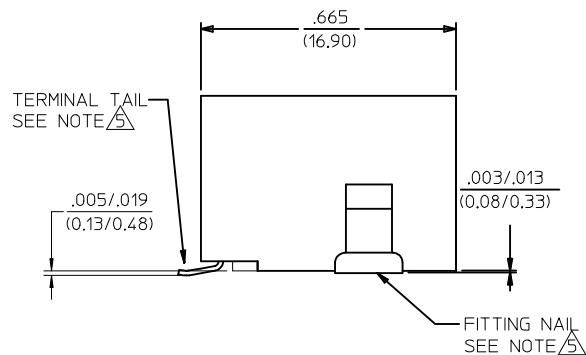




- NOTES:
- MATERIAL:
HOUSING & INSULATOR: NYLON(PA), GLASS FILLED, UL94V-0, COLOR: BLACK
TERMINALS: PHOSPHOR BRONZE, .012/(0.30) THICK
FITTING NAILS: BRASS, .010/(0.25) THICK
 - FINISH:
TERMINALS:
A = SELECT GOLD IN CONTACT AREA: .000050/(0.00127) MIN.,
SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MIN.,
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.
B = SELECT GOLD IN CONTACT AREA: .000030/(0.00076) MIN.,
SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MIN.,
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.
C = SELECT GOLD IN CONTACT AREA: .000015/(0.00038) MIN.,
SELECT MATTE TIN IN PC TAIL AREA: .000100/(0.00254) MIN.,
WITH OVERALL NICKEL UNDERPLATE: .000050/(0.00127) MIN.
 - FITTING NAILS:
100 MICROINCHES MATTE TIN OVER 50 MICROINCHES NICKEL UNDERPLATE
 - PRODUCT SPECIFICATION AND PROCESSING PARAMETERS: PS-43743-0001.
 - PACKAGING SPECIFICATION:
CONNECTOR ASSEMBLIES TO BE PACKAGED USING TAPE & REEL SYSTEM.
 - COPLANARITY SPECIFICATION:
WHEN AN ASSEMBLY IS PLACED UPON A LEVEL SURFACE, THE MAXIMUM GAP BETWEEN THE LEVEL SURFACE AND ALL TERMINAL TAILS AND BOTH FITTING NAILS IS .006/(0.15).
 - FOR ASSEMBLIES HAVING VOIDED CIRCUITS, THE TAIL PORTION IS CUT AND REMOVED, THE MATING PORTION OF THE TERMINAL REMAINS INTACT. VOIDED CIRCUITS MUST BE THE SAME FOR BOTH PARTS.
 - CONFORMS TO FCC REGULATION PART 68.5 FOR MODULAR JACKS.

PC BOARD LAYOUT FOR
8 POSITION HOUSING
(COMPONENT SIDE OF BOARD)

PC BOARD LAYOUT FOR
6 POSITION HOUSING
(COMPONENT SIDE OF BOARD)

CORRECT NOTE # EC NO: UCP2013-4879 DR: NGUYEN CHKD: JELL APP: FSMITH 2013/05/31 2013/06/07	QUALITY SYMBOLS ▽=0 ▽=0 ▽=0	GENERAL TOLERANCES (UNLESS SPECIFIED) mmINCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± .010 2 PLACES ± .025 ± .015 1 PLACE ± .38 ± --- 0 PLACE ± --- ± --- ANGULAR ±1/2° DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	DIMENSION STYLE IN/MM		SCALE 2:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION	
			DRAWN BY PEARSON	DATE 01/07/97	TITLE 4, 6 & 8 CIRCUIT DUAL PORT SMT MODJACK			
			CHECKED BY MUELLER	DATE 01/07/97				
			APPROVED BY FSMITH	DATE 2012/10/12				
			MATERIAL NO.		DOCUMENT NO.		SHEET NO.	
			SEE CHART		SD-43841-001		1 OF 2	
			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					

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