

<IGBT Modules>

## CM450DX-24T/CM450DXP-24T

## HIGH POWER SWITCHING USE

#### **INSULATED TYPE**



- Flat base type
- Copper base plate (Ni-plating)
- •RoHS Directive compliant
- •Tin plating pin terminals



Collector current Ic	4	5	0	Α
Collector-emitter voltage $V_{\text{CES}}$	2	0	0	V
Maximum junction temperature T <sub>vjmax</sub>	1	7	5	°C

- •Flat base type
- •Copper base plate (Ni-plating)
- •RoHS Directive compliant
- •Tin plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

#### **APPLICATION**

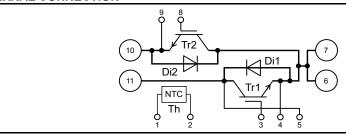
AC Motor Control, Motion/Servo Control, Power supply, etc.

dual switch (half-bridge)

#### **OPTION** (Below options are available.)

- ●PC-TIM (Phase Change Thermal Interface Material) pre-apply
- •V<sub>CEsat</sub> selection for parallel connection

#### **INTERNAL CONNECTION**



#### **TERMINAL CODE**

1. TH1 6. C2E1
2. TH2 7. C2E1
3. G1 8. G2
4. Es1 9. Es2
5. Cs1 10. E2
11. C1

OUTLINE DRAWING

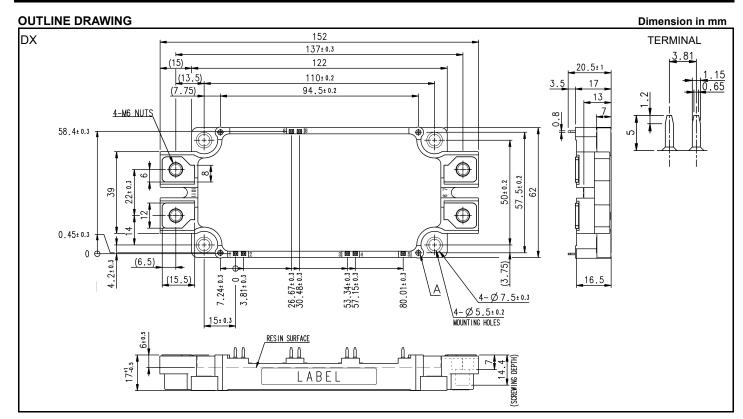
COM. (6.5) 97 MOUNTING HOLES SECTION A

Ø 2.6

Ø 2.32

HIGH POWER SWITCHING USE

INSULATED TYPE

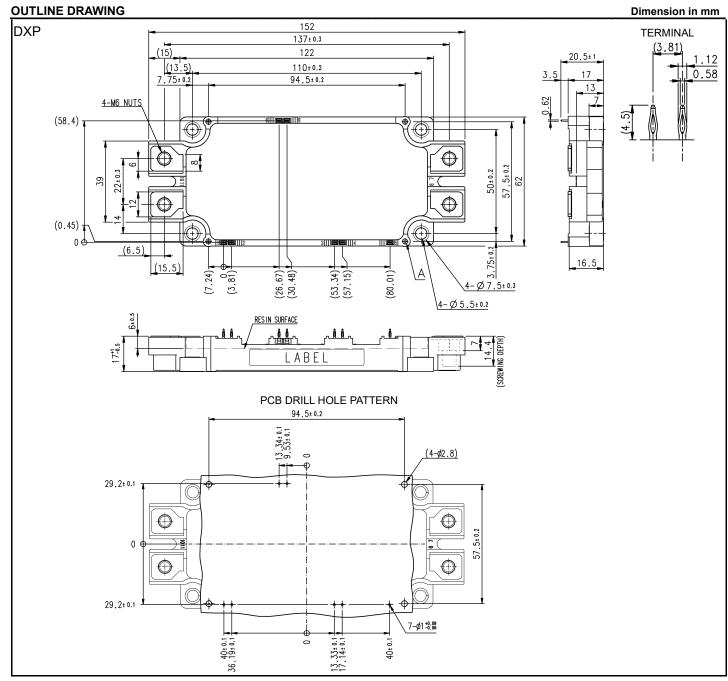


Tolerance otherwise specified

Divisio	n of l	Tolerance		
0.5		to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over	120	to 4	400	±1.2

HIGH POWER SWITCHING USE

**INSULATED TYPE** 



Tolerance otherwise specified

Divisio	n of l	Tolerance		
	0.5	to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 4	400	±1.2

HIGH POWER SWITCHING USE

**INSULATED TYPE** 

#### MAXIMUM RATINGS (T<sub>vj</sub>=25 °C, unless otherwise specified)

#### INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit	
V <sub>CES</sub>	Collector-emitter voltage	G-E short-circuited	1200	V	
$V_{GES}$	Gate-emitter voltage	C-E short-circuited	± 20	V	
Ic	Collector current	DC, T <sub>C</sub> =118 °C (Note2, 4)	450	Δ.	
I <sub>CRM</sub>	Collector current	Pulse, Repetitive (Note3)	900	A	
P <sub>tot</sub>	Total power dissipation	T <sub>C</sub> =25 °C (Note2, 4)	2500	W	
I <sub>E</sub> (Note1)	Emitter eurrent	DC (Note2)	450	Δ.	
I <sub>ERM</sub> (Note1)	Emitter current	Pulse, Repetitive (Note3)	900	Α	

#### MODULE

Symbol	Item	Conditions	Rating	Unit
V <sub>isol</sub>	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	2500	V
$T_{vjmax}$	Maximum junction temperature	Instantaneous event (overload) (Note9)	175	°C
T <sub>Cmax</sub>	Maximum case temperature	(Note4, 9)	125	
T <sub>vjop</sub>	Operating junction temperature	Continuous operation (under switching) (Note9)	-40 ~ +150	°C
T <sub>stq</sub>	Storage temperature	-	-40 ~ +125	

## ELECTRICAL CHARACTERISTICS (T $_{\nu j}$ =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Itama	Conditions		Limits		Unit	
Symbol	Item	Conditions			Тур.	Max.	Unit
I <sub>CES</sub>	Collector-emitter cut-off current	V <sub>CE</sub> =V <sub>CES</sub> , G-E short-circuited		-	-	1.0	mA
I <sub>GES</sub>	Gate-emitter leakage current	V <sub>GE</sub> =V <sub>GES</sub> , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I <sub>C</sub> =45 mA, V <sub>CE</sub> =10 V		5.4	6.0	6.6	V
		I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V,	T <sub>vj</sub> =25 °C	-	1.65	2.05	
V <sub>CEsat</sub>		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.85	-	V
(Terminal)	0.11	(Note5)	T <sub>vj</sub> =150 °C	-	1.90	-	
	Collector-emitter saturation voltage	I <sub>C</sub> =450 A,	T <sub>vj</sub> =25 °C	-	1.50	1.75	
V <sub>CEsat</sub>		V <sub>GE</sub> =15 V,	T <sub>vj</sub> =125 °C	-	1.70	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.75	-	
Cies	Input capacitance		1 9		-	109.1	
Coes	Output capacitance	V <sub>CE</sub> =10 V, G-E short-circuited		-	-	3.1	nF
Cres	Reverse transfer capacitance		·- · ·		-	1.4	
Q <sub>G</sub>	Gate charge	V <sub>CC</sub> =600 V, I <sub>C</sub> =450 A, V <sub>GE</sub> =15 V		-	3.4	-	μC
t <sub>d(on)</sub>	Turn-on delay time	Vcc=600 V, Ic=450 A, V <sub>GE</sub> =±15 V,		-	-	600	
t <sub>r</sub>	Rise time			-	-	200	1
t <sub>d(off)</sub>	Turn-off delay time			-	-	800	ns
t <sub>f</sub>	Fall time	R <sub>G</sub> =1.3 Ω, Inductive load		-	-	400	
		I <sub>E</sub> =450 A, G-E short-circuited,	T <sub>vj</sub> =25 °C	-	1.70	2.25	
V <sub>EC</sub> (Note1)		Refer to the figure of test circuit	T <sub>vj</sub> =125 °C	-	1.85	-	V
(Terminal)		(Note5)	T <sub>vj</sub> =150 °C	-	1.90	-	
	Emitter-collector voltage	I <sub>E</sub> =450 A,	T <sub>vj</sub> =25 °C	-	1.50	1.85	
V <sub>EC</sub> (Note1)		G-E short-circuited,	T <sub>vj</sub> =125 °C	-	1.50	-	V
(Chip)		(Note5)	T <sub>vj</sub> =150 °C	-	1.50	-	
t <sub>rr</sub> (Note1)	Reverse recovery time	V <sub>CC</sub> =600 V, I <sub>E</sub> =450 A, V <sub>GE</sub> =±15 V,		-	-	400	ns
Q <sub>rr</sub> (Note1)	Reverse recovery charge	$R_G$ =1.3 Ω, Inductive load		-	35.1	-	μC
Eon	Turn-on switching energy per pulse	V <sub>CC</sub> =600 V, I <sub>C</sub> =I <sub>E</sub> =450 A,		-	43.1	-	
E <sub>off</sub>	Turn-off switching energy per pulse	V <sub>GE</sub> =±15 V, R <sub>G</sub> =1.3 Ω, T <sub>Vi</sub> =150 °C,		-	45	-	mJ
E <sub>rr</sub> (Note1)	Reverse recovery energy per pulse	Inductive load		-	32.4	-	mJ
R <sub>CC'+EE'</sub>	Internal lead resistance	Main terminals-chip, per switch, T <sub>C</sub> =25	°C (Note4)	-	0.75	-	mΩ
r <sub>g</sub>	Internal gate resistance	Per switch		-	0.67	-	Ω

HIGH POWER SWITCHING USE

**INSULATED TYPE** 

#### ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

#### NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
		Conditions	Min.	Тур.	Max.	Offic
R <sub>25</sub>	Zero-power resistance	T <sub>C</sub> =25 °C (Note4)	4.85	5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R <sub>100</sub> =493 Ω, T <sub>C</sub> =100 °C (Note4)	-7.3	-	+7.8	%
B <sub>(25/50)</sub>	B-constant	Approximate by equation (Note6)	-	3375	-	K
P <sub>25</sub>	Power dissipation	T <sub>C</sub> =25 °C (Note4)	-	-	10	mW

#### THERMAL RESISTANCE CHARACTERISTICS

Symbol Item	Itam	Conditions		Unit		
	Conditions	Min.	Тур.	Max.	Unit	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	60	K/kW
$R_{th(j-c)D}$	Thermal resistance	Junction to case, per Inverter FWD (Note4)	-	-	87	r/KVV
R <sub>th(c-s)</sub>	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 7, 9)	-	11.5	-	K/kW

#### **MECHANICAL CHARACTERISTICS**

Comple al	lán m	Conditions		Limits			Unit	
Symbol	Item			Min.	Тур.	Max.	Unit	
M <sub>t</sub>	Mounting torque	Main terminals	Main terminals M 6 screw		4.0	4.5	N·m	
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m	
ds		Coldennin tune (DV)	Terminal to terminal	17	-	-		
	Creepage distance	Solder pin type (DX)	Terminal to base plate	16.4	-	-	mm	
		Pressfit pin type (DXP)	Terminal to terminal	17	-	-		
			Pressiit pin type (DXP)	Terminal to base plate	16.8	-	-	mm
		Solder pin type (DX)	Terminal to terminal	10	-	-		
			Terminal to base plate	16.2	-	-	mm	
d <sub>a</sub>	Clearance	Description (DVD)	Terminal to terminal	10	-	-		
		Pressfit pin type (DXP)  Terminal to base plate		16.2	-	-	mm	
ec	Flatness of base plate	On the centerline X, Y	lote8)	±0	-	+200	μm	
m	mass	-		-	300	-	g	

<sup>\*.</sup> This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

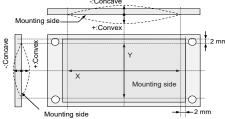
- 2. Junction temperature  $(T_{vj})$  should not increase beyond  $T_{vjmax}$  rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature  $(T_{vj})$  dose not exceed  $T_{vjmax}$  rating.
- 4. Case temperature  $(T_c)$  and heat sink temperature  $(T_s)$  are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6. 
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 $R_{25}\!\!:$  resistance at absolute temperature  $T_{25}$  [K];  $T_{25}\!\!=\!\!25$  [°C]+273.15=298.15 [K]

 $R_{50}$ : resistance at absolute temperature  $T_{50}$  [K];  $T_{50}\!=\!50$  [°C]+273.15=323.15 [K]

- 7. Reference value. Thermally conductive grease of thermal conductivity  $\lambda$ =0.9 W/(m·K) and thickness D<sub>(C-S)</sub>=50  $\mu$ m.
- 8. The base plate (mounting side) flatness measurement points (X, Y) are shown in the following figure.



Long term performance related to thermal conductive grease (including but not limited to aspects such as the increase of thermal resistance

9. due to pumping out, etc.) should be verified under user's specific application conditions. Each temperature condition (T<sub>vj max</sub>, T<sub>vj op</sub>, T<sub>C max</sub>) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

HIGH POWER SWITCHING USE

INSULATED TYPE

Note10. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness: t1.6

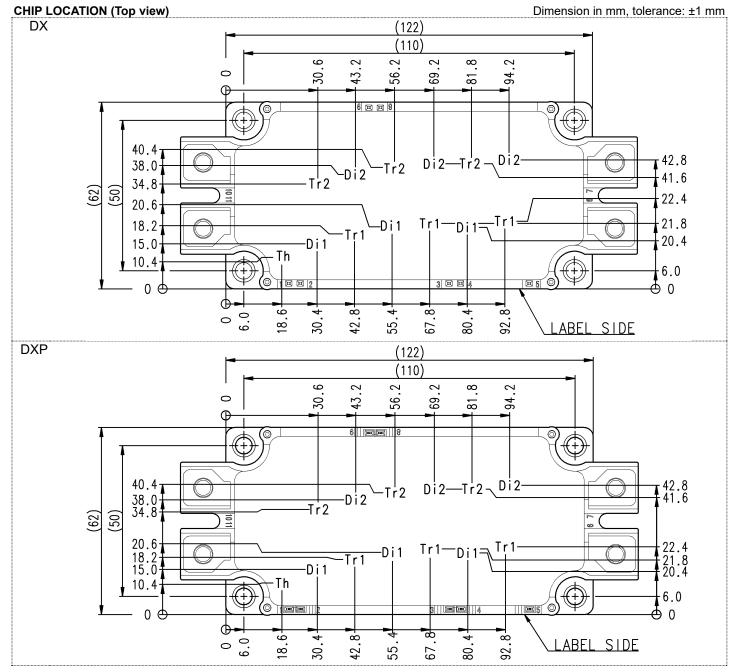
	Туре	Manufacturer	Size	Tightening torque (N•m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.75 ± 0.075 N•III	

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Item	Conditions		Unit		
	item Condition	Conditions	Min.	Тур.	Max.	Offic
V <sub>cc</sub>	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
$V_{GEon}$	Gate (-emitter drive) voltage	Applied across G1-E1s/G2-E2s terminals	13.5	15.0	16.5	V
$R_{G}$	External gate resistance	Per switch	1.3	-	10	Ω

HIGH POWER SWITCHING USE

**INSULATED TYPE** 

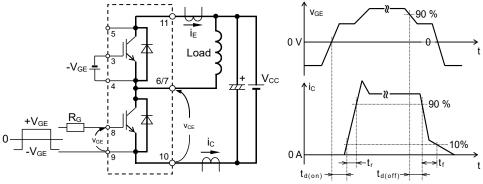


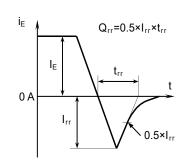
Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

HIGH POWER SWITCHING USE

INSULATED TYPE

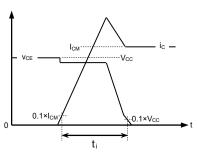
#### **TEST CIRCUIT AND WAVEFORMS**

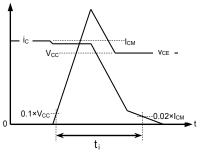


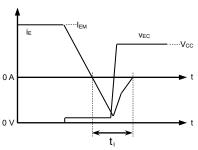


Switching characteristics test circuit and waveforms









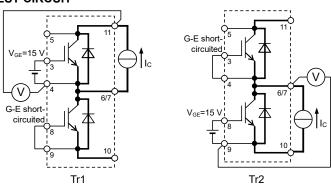
IGBT Turn-on switching energy

IGBT Turn-off switching energy

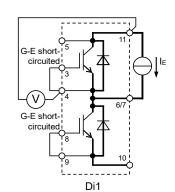
FWD Reverse recovery energy

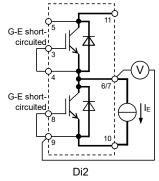
Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

#### **TEST CIRCUIT**









V<sub>EC</sub> characteristics test circuit

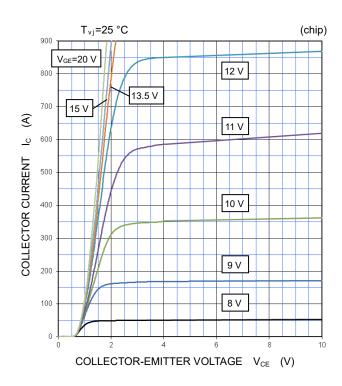
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

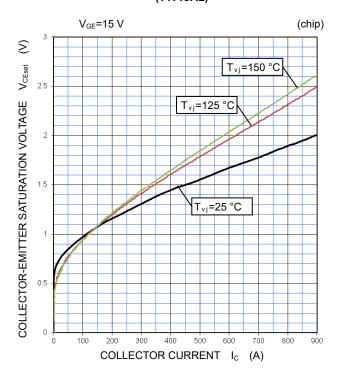
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

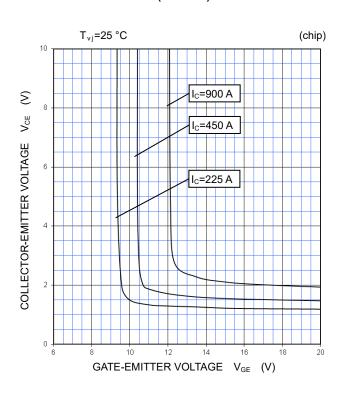
## OUTPUT CHARACTERISTICS (TYPICAL)



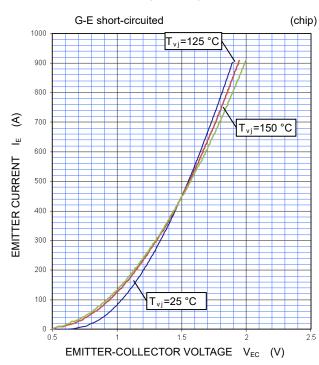
#### COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



## COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



#### FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



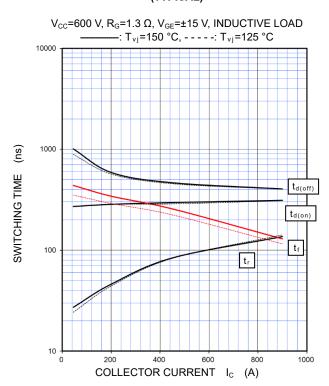
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

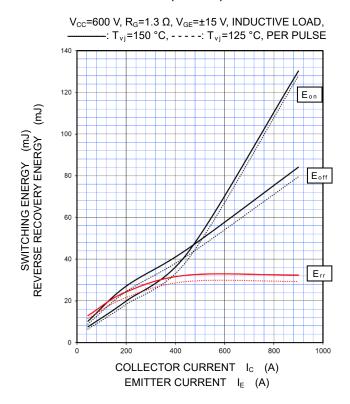
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

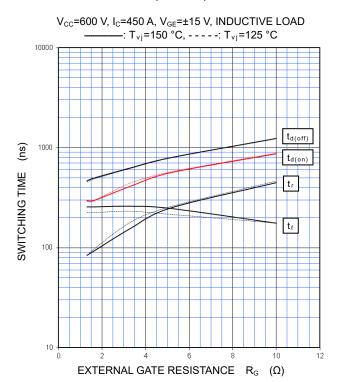
## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



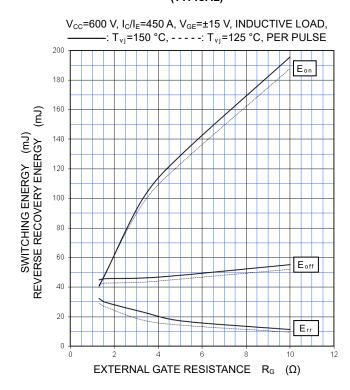
## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



## HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



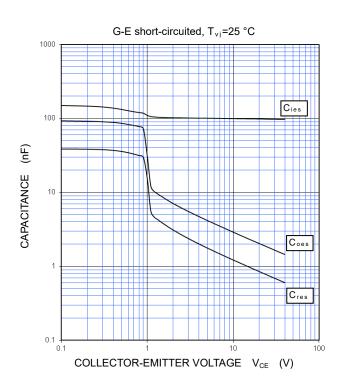
HIGH POWER SWITCHING USE

**INSULATED TYPE** 

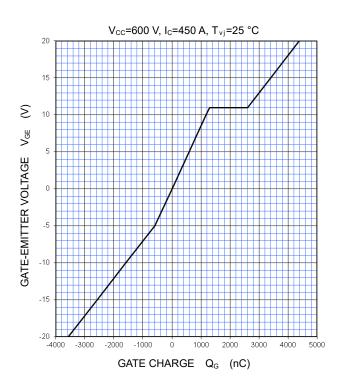
#### **PERFORMANCE CURVES**

#### **INVERTER PART**

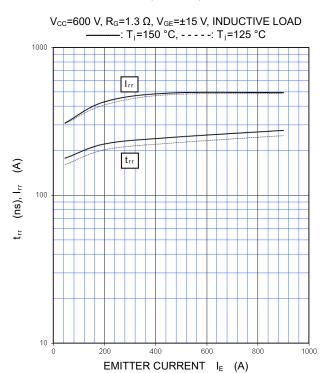
## CAPACITANCE CHARACTERISTICS (TYPICAL)



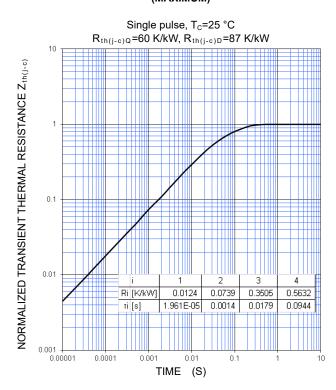
## GATE CHARGE CHARACTERISTICS (TYPICAL)



# FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



## TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



HIGH POWER SWITCHING USE

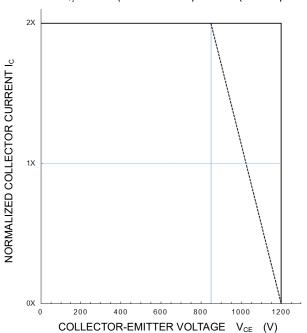
**INSULATED TYPE** 

#### **PERFORMANCE CURVES**

#### **INVERTER PART**

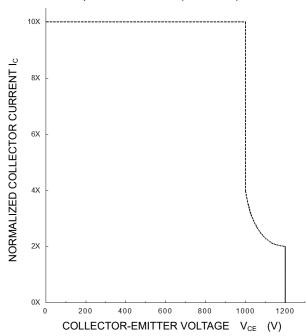
#### TURN-OFF SWITCHING SAFE OPERATING AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 $V_{\text{CC}} \le 850 \text{ V}$ ,  $R_{\text{G}} = 1.3 \sim 10 \Omega$ ,  $V_{\text{GE}} = \pm 15 \text{ V}$ , ....:  $T_{\text{v}_{\text{j}}} = 25 \sim 150 \text{ °C}$  (Normal load operations (Continuous) ...-:  $T_{\text{v}_{\text{j}}} = 175 \text{ °C}$  (Unusual load operations (Limited period)



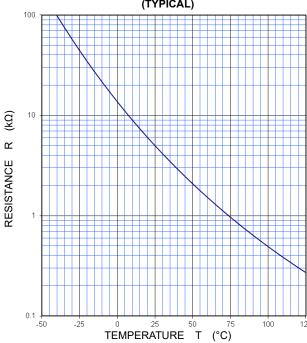
## SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 800 \text{ V}$ ,  $R_G = 1.3 \sim 10 \Omega$ ,  $V_{GE} = \pm 15 \text{ V}$ ,  $T_{vj} = 25 \sim 150 \text{ °C}$ ,  $t_W \le 8 \mu \text{s}$ , Non-Repetitive



#### NTC thermistor part





Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

### **Important Notice**

The information contained in this datasheet shall in no event be regarded as a guarantee of conditions or characteristics. This product has to be used within its specified maximum ratings, and is subject to customer's compliance with any applicable legal requirement, norms and standards.

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HIGH POWER SWITCHING USE INSULATED TYPE

### Keep safety first in your circuit designs!

Mitsubishi Electric Corporation puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of non-flammable material or (iii) prevention against any malfunction or mishap.

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