

## NFC 3 Click



PID: MIKROE-5538

**NFC 3 Click** is a compact add-on board that contains an NFC transceiver for contactless communication at 13.56MHz. This board features the [PN5180A0HN](#), a highly integrated high-performance full NFC Forum-compliant frontend from [NXP Semiconductors](#). The PN5180A0HN utilizes an outstanding modulation and demodulation concept for different contactless communication methods and protocols. It is fully compliant with many Reader/Writer standards (ISO 14443A/B, ISO 15693, ISO 18092, and more), alongside support for reading all NFC tag types (type 1, 2, 3, 4A, and 4B). Besides the SPI host interface, it also features high RF output power to drive an antenna etched on the PCB directly, besides its tuning circuit, at high efficiency. This Click board™ represents an ideal solution for rapidly integrating NFC technology into any custom application.

### How does it work?

NFC 3 Click is based on the PN5180A0HN, a high-performance multiprotocol full NFC frontend from NXP Semiconductors, for contactless communication at 13.56MHz. The PN5180A0HN implements the RF and all the low-level functionality, like an antenna driving and receiver circuitry, to realize an NFC Forum-compliant reader. It utilizes an outstanding modulation and demodulation concept for different contactless communication methods and protocols. The PN5180A0HN is fully compliant with many Reader/Writer standards like ISO 14443A/B up to 848 kBit/s, JIS X 6319-4 comparable with FeliCa scheme, ISO 15693, ISO 18092, and more. Alongside support for reading all NFC tag types (type 1, 2, 3, 4A, and 4B) and high RF output power, this Click board™ is ideally suited for industrial and consumer NFC applications like industrial, eGov readers, payment terminals, and more.

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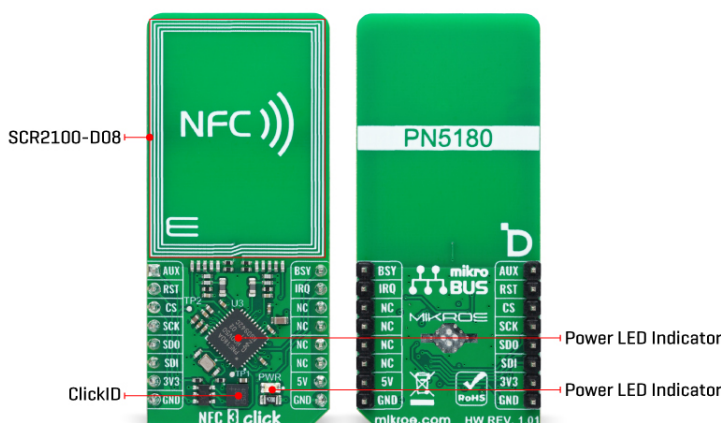
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This Click board™ connects to a host MCU with an SPI interface for configuration, NFC data exchange, and high-level NFC protocol implementation. It supports the most common SPI Mode 0 and operates with data rates up to 7 Mbit/s. The PN5180A0HN has two types of integrated memories: RAM and EEPROM. Internal registers of the PN5180A0HN store configuration data, while the RF configuration for dedicated RF protocols is defined by EEPROM data, copied by a command issued from the host MCU. This allows users to achieve maximum RF performance from a given antenna design.

In addition to the SPI interface signals, this board uses several other signals from the mikroBUS™ socket. The reset pin routed on the RST pin of the mikroBUS™ socket provides the general reset ability, while the IRQ pin of the mikroBUS™ socket represents an interrupt request to inform the host controller of various events.

The PN5180A0HN also has the possibility of updating the implemented firmware. In Secure Firmware update mode, the PN5180A0HN requires dedicated physical handling of the SPI interface lines and the BSY line of the mikroBUS™ socket. The BSY signal is used to indicate that the PN5180A0HN is not able to send or receive data over the SPI interface. The secure firmware download mode is entered by setting the AUX pin to a high logic state during the Start-Up sequence of the device. The AUX pin is allowed for any other functionality after Start-Up (as test signals provided by test points on the board); the level of this pin has no impact on the download functionality after Start-Up during standard NFC operation.

This Click board™ can only be operated from a 3.3V logic voltage level. Therefore, the board must perform appropriate logic voltage level conversion before using MCUs with different logic levels. However, the Click board™ comes equipped with a library containing functions and an example code that can be used as a reference for further development.

## Specifications

Type	RFID/NFC
Applications	Can be used for industrial and consumer NFC applications like industrial, eGov readers, payment terminals, and more
On-board modules	PN5180A0HN - multiprotocol NFC frontend from NXP Semiconductors
Key Features	High performance, full NFC Forum-compliant

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	frontend, contactless communication at 13.56MHz, fully compliant with many Reader/Writer standards, reading of all NFC tag types, SPI interface, high RF power, possibility of updating the implemented firmware, and more
Interface	SPI
Feature	ClickID
Compatibility	mikroBUS™
Click board size	L (57.15 x 25.4 mm)
Input Voltage	3.3V,5V

## Pinout diagram

This table shows how the pinout on NFC 3 Click corresponds to the pinout on the mikroBUS™ socket (the latter shown in the two middle columns).

Notes	Pin					Pin	Notes
FW Download Request	<b>AUX</b>	1	AN	PWM	16	<b>BSY</b>	BUSY Indicator
Reset / ID SEL	<b>RST</b>	2	RST	INT	15	<b>IRQ</b>	Interrupt
SPI Select / ID COMM	<b>CS</b>	3	CS	RX	14	NC	
SPI Clock	<b>SCK</b>	4	SCK	TX	13	NC	
SPI Data OUT	<b>SDO</b>	5	MISO	SCL	12	NC	
SPI Data IN	<b>SDI</b>	6	MOSI	SDA	11	NC	
Power Supply	<b>3.3V</b>	7	3.3V	5V	10	<b>5V</b>	Power Supply
Ground	<b>GND</b>	8	GND	GND	9	<b>GND</b>	Ground

## Onboard settings and indicators

Label	Name	Default	Description
LD1	PWR	-	Power LED Indicator

## NFC 3 Click electrical specifications

Description	Min	Typ	Max	Unit
Supply Voltage	-	3.3	-	V
Operating Frequency	-	13.56	-	MHz

## Software Support

We provide a library for the NFC 3 Click as well as a demo application (example), developed using Mikroe [compilers](#). The demo can run on all the main Mikroe [development boards](#).

Package can be downloaded/installed directly from NECTO Studio Package Manager(recommended), downloaded from our [LibStock™](#) or found on [Mikroe github account](#).

## Library Description

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This library contains API for NFC 3 Click driver.

Key functions

- nfc3\_read\_card\_uid NFC 3 read card UID function.
- nfc3\_read\_firmware\_version NFC 3 reading firmware version function.
- nfc3\_read\_eeprom\_version NFC 3 reading EEPROM version function.

## Example Description

This example demonstrates the use of NFC 3 Click board by reading MIFARE ISO/IEC 14443 type A tag UID.

The full application code, and ready to use projects can be installed directly from NECTO Studio Package Manager(recommended), downloaded from our [LibStock™](#) or found on [Mikroe github account](#).

Other Mikroe Libraries used in the example:

- MikroSDK.Board
- MikroSDK.Log
- Click.NFC3

## Additional notes and informations

Depending on the development board you are using, you may need [USB UART click](#), [USB UART 2 Click](#) or [RS232 Click](#) to connect to your PC, for development systems with no UART to USB interface available on the board. UART terminal is available in all Mikroe [compilers](#).

## mikroSDK

This Click board™ is supported with [mikroSDK](#) - Mikroe Software Development Kit. To ensure proper operation of mikroSDK compliant Click board™ demo applications, mikroSDK should be downloaded from the [LibStock](#) and installed for the compiler you are using.

For more information about mikroSDK, visit the [official page](#).

## Resources

[mikroBUS™](#)

[mikroSDK](#)

[Click board™ Catalog](#)

[Click Boards™](#)

[ClickID](#)

## Downloads

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[NFC 3 click 2D and 3D files v101](#)

[PN5180A0HN datasheet](#)

[NFC 3 click example on Libstock](#)

[NFC 3 click schematic v101](#)

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