

TFT Module Specification

MODEL: AWD-360360T18N01

This module uses ROHS material

CUSTOMER
APPROVED BY
DATE:

Approved by	Checked by	Made by
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Revision Record

Rev No.	Rev Date	Contents	Note
А	2024/05/02	New issue.	



Product Specification

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AWD-360360T18N01

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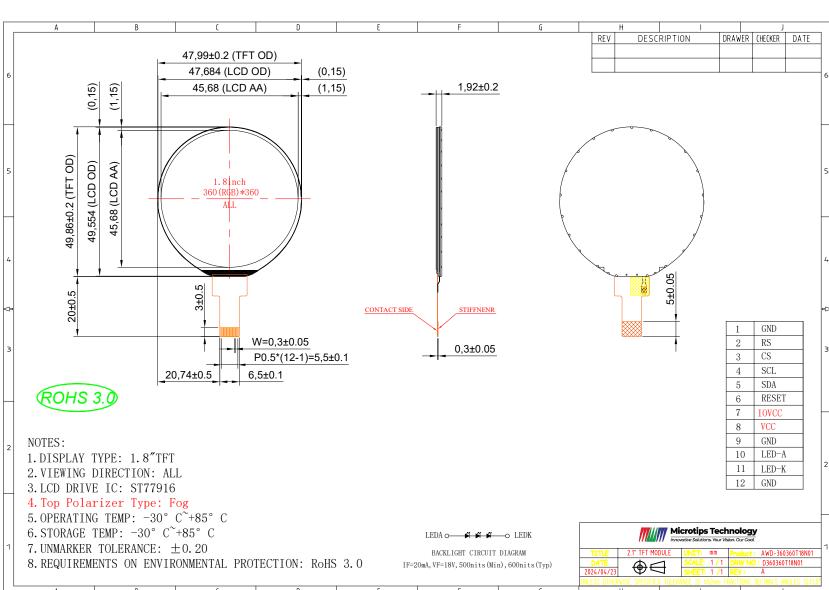
1. Scope

This specification defines general provisions as well as inspection standards for TFT module supplied by Micotips Technology. If the event of unforeseen problem or unspecified items may occur, naturally shall negotiate and agree to solution

1. 2. General Information

Item	Standard Values	Units
LCD type	1.8"TFT	
Dot arrangement	360 (RGB)×360	dots
Color filter array	RGB vertical stripe	
Display mode	IPS / Normally Black	
Eyes Viewing Direction	ALL	
Driver IC	ST77916	
Module size	47.99(W)×49.86(H)×1.92(T)	mm
Active area	45.68(W)×45.68(H)	mm
Dot pitch	0.0423(W)×0.1269(H)	mm
Interface	4-line serial	
Matched connector	FH33-12S-0.5SH or Other Alternative	
Operating temperature	ating temperature -30 ~ +85	
Storage temperature	-30 ~ +85	
Back Light	3 White LEDS	

	. Page. 2 5/22	_		A			В			(TFT OD)) <u>-</u>	(0.1
	Issued Date. 2024/05/02		6	Ŧ	(0,15)	(1,15)	<u>'</u>	-	47,684 (L			(0,1
	Rev. No.		5	49,86±0.2 (TFT OD)	49,554 (LCD OD)	45,68 (LCD AA)	_		1.85 360 (R0	nch B)*360 L		-
Microtips Technology Innovative Solutions. Your Vision. Our Goal.	AWD-360360T18N01		4 🖟 🛪	BOH3		, -	,	20,7	9.04±0.5		,3±0.05 *(12-1) .1	
Mic	Model:	us	2	NOTES: 1. DISPLAY 2. VIEWING 3. LCD DR	G DI	IREC IC:	CTION: ST77	ALL '916				
	Product Specification	External Dimensions	1	4. Top Po. 5. OPERAT 6. STORAGI 7. UNMARKI 8. REQUIRI	ENG E TI ER 1	TEN EMP: TOLE	IP: -3 -30° ERANCE	60° C^ C^+8 C: ±0	+85° C 5° C . 20	PROTEC	ΓΙΟΝ:	RoHS



MICROTIPS TECHNOLOGY USA

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4. Interface Description

Pin No.	Pin Name	Description
1	GND	Ground.
2	RS	Command/Data select
3	CS	Chip select input pin ("Low" enable) in SPI I/F.
4	SCL	This pin is used serial interface clock in SPI.
5	SDA	Serial input/output data bus
6	Reset	Reset signal input terminal, active at 'L'
7	IOVCC	Supply Voltage (Logic)
8	VCC	Supply Voltage
9	GND	Ground.
10	LED-A	LED backlight (Anode).
11	LED-K	LED backlight (Cathode).
12	GND	Ground.



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5. Absolute Maximum Ratings

Item	Symbol	Min.	Max.	Unit
Supply Voltage (Logic)	IOVCC	-0.3	4.6	V
Supply Voltage	VCC	-0.3	4.6	V
Input Voltage	Vin	0.5	IOVCC +0.5	V
Operating Temperature	ТОР	-30	85	°C
Storage Temperature	TST	-30	85	°C
Storage Humidity	HD	20	90	%RH

6. DC Characteristics

Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Supply Voltage (Logic)	IOVCC	1.65	1.8	3.3	V	-
Supply Voltage	VCC	2.65	2.8	3.3	V	-
Power Supply Current	IVCC	TBD	TBD	TBD	mA	-
Input High Voltage	$ m V_{IH}$	0.7IOVCC	-	IOVCC	V	-
Input Low Voltage	V_{IL}	GND	-	0.3 IOVCC	V	-
Output High Voltage	V _{OH}	0.8IOVCC	-	IOVCC	V	-
Output Low Voltage	V_{OL}	GND	-	0.2IOVCC	V	-
I/O Leak Current	ILI	-1	-	-	uA	-



7. Timing Characteristics

7.1 Power ON/OFF Sequence

VDDI and VDD can be applied in any order.

VDD and VDDI can be power down in any order.

During power off, if LCD is in the Sleep Out mode, VDD and VDDI must be powered down minimum 120msec after RESX has been released.

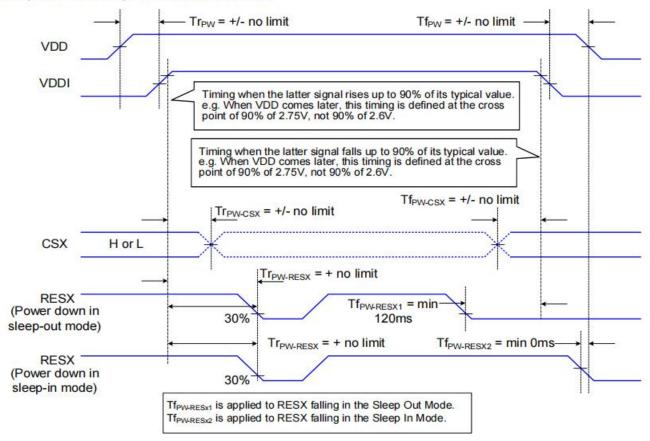
During power off, if LCD is in the Sleep In mode, VDDI or VCI can be powered down minimum 0msec after RESX has been released.

CSX can be applied at any timing or can be permanently grounded. RESX has priority over CSX.

- Note 1: There will be no damage to the display module if the power sequences are not met.
- Note 2: There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.
- Note 3: There will be no abnormal visible effects on the display between end of Power On Sequence and before receiving Sleep Out command. Also between receiving Sleep In command and Power Off Sequence.

Note 4: If RESX line is not held stable by host during Power On Sequence as defined in the sequence below, then it will be necessary to apply a Hardware Reset (RESX) after Host Power On Sequence is complete to ensure correct operation. Otherwise function is not guaranteed.

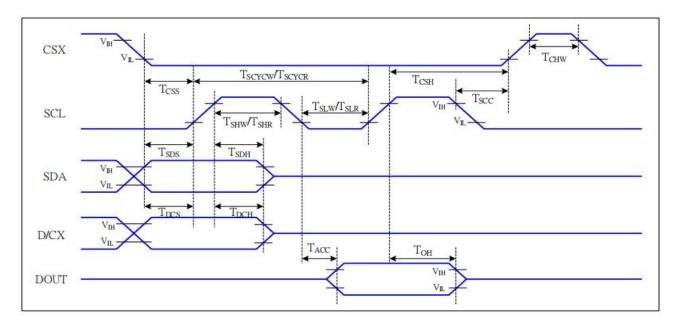
The power on/off sequence is illustrated below





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7.2 Serial Interface Characteristics (4-line serial)



VDDI=1.65 to 3.3V, VDD=2.65 to 3.3V, GND=RGND=0V, Ta=25℃

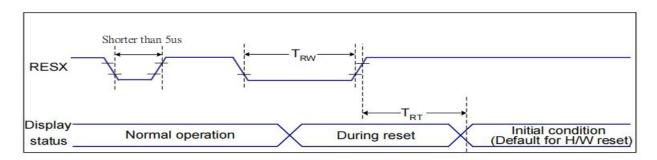
Signal	Symbol	mbol Parameter		MAX	Unit	Description
	T _{CSS}	Chip select setup time (write)	15		ns	
	T _{CSH}	Chip select hold time (write)	15		ns	
CSX	T _{CSS}	Chip select setup time (read)	60		ns	
	T _{SCC}	Chip select hold time (read)	65		ns	
	Tchw	Chip select "H" pulse width	40		ns	
	T _{SCYCW}	Serial clock cycle (Write)	16		ns	
	Tshw	SCL "H" pulse width (Write)	7	7		-write command & data
001	T _{SLW}	SCL "L" pulse width (Write)	7		ns	ram
SCL	TSCYCR	Serial clock cycle (Read)	150		ns	
	T _{SHR}	SCL "H" pulse width (Read)	60	60 ns		-read command & data
	T _{SLR}	SCL "L" pulse width (Read)	60		ns	ram
DIOY	T _{DCS}	D/CX setup time	7		ns	
D/CX	T _{DCH}	D/CX hold time	7		ns	
SDA	T _{SDS}	Data setup time	10		ns	
(DIN)	T _{SDH}	Data hold time	10		ns	
DOUT	TACC	Access time	10	50	ns	For maximum CL=30pF
DOUT	Тон	Output disable time	15	50	ns	For minimum CL=8pF

4-line serial Interface Characteristics



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7.3 Reset Timing



VDDI=1.8V, VDD=2.8V, GND=RGND=0V, Ta=25℃

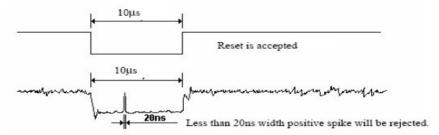
Related Pins	Symbol	Parameter	MIN	MAX	Unit
	TRW	Reset pulse duration	10	150	us
RESX	TRT Reset cancel		-	5 (Note 1, 5)	ms
			27	120 (Note 1, 6, 7)	ms

Notes:

- The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
- 2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action		
Shorter than 5us	Reset Rejected		
Longer than 9us	Reset		
Between 5us and 9us	Reset starts		

- 3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition for Hardware Reset.
- 4. Spike Rejection also applies during a valid reset pulse as shown below:



- 5. When Reset applied during Sleep In Mode.
- 6. When Reset applied during Sleep Out Mode.
- 7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.



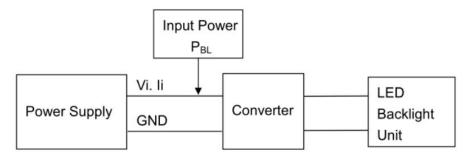
8. Backlight Characteristic



Item	Symbol	MIN	TYP	MAX	UNIT	Remark
Supply Voltage	Vf	16.5	18	19.5	V	Note 1
Supply Current	If	-	20	-	mA	-
Life Time	-	-	30000	-	Hr	Note 2
Backlight Color			,	White		

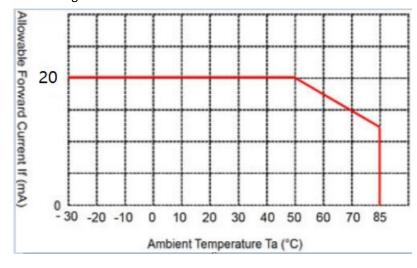
Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and If =20mA.

Note 2: LED current is measured by utilizing a high frequency current meter as shown below:



Note 3: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 °C and If =20mA. The LED lifetime could be decreased if operating If is larger than 20mA.

Note 4: LED light bar circuit:



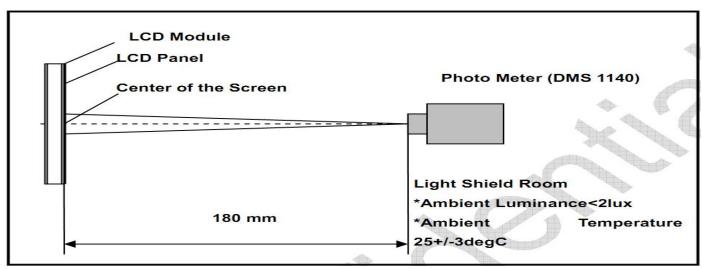


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9. Optical Characteristics

Item	Conditions		Min.	Тур.	Max.	Unit	Note
	Harizantal	θL	80	85	-		
Viewing Angle	Horizontal	θR	80	85	-	40000	(1) (2) (6)
(CR>10)	Vertical	θТ	80	85	-	degree	(1),(2),(6)
	Vertical	θВ	80	85	-		
Luminous Intensity for LCM	-		500	600	-	cd/m2	If=20mA
Uniformity for LCM	-		75	80	-	%	If=20mA
Contrast Ratio	Center		800	1200	-	-	(1),(3),(6)
Response Time	Rising +Falling		-	25	35	ms	(1),(4),(6)
	White x		TBD	TBD	TBD	-	
	White y		TBD	TBD	TBD	-	
	Red x		TBD	TBD	TBD	-	
CF Color	Red y		TBD	TBD	TBD	-	(4) (5)
Chromaticity (CIE1931)	Green x		TBD	TBD	TBD	-	(1), (6)
	Green y		TBD	TBD	TBD	-	
	Blue x		TBD	TBD	TBD	-	
	Blue y		TBD	TBD	TBD	-	

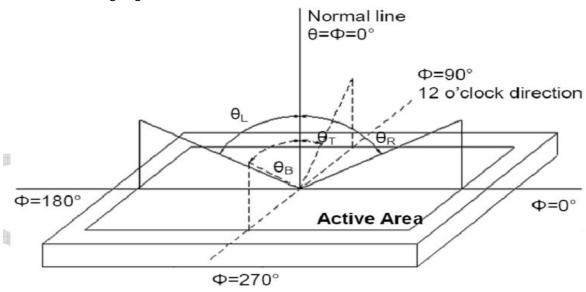
Note (1) Measurement Setup: The LCD module should be stabilized at given temp. 25°C for 15 minutes to avoid abrupt temperature change during measuring. In order to stabilize the luminance, the measurement should be executed after lighting backlight for 15 minutes in a windless room.





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Note (2) Definition of Viewing Angle



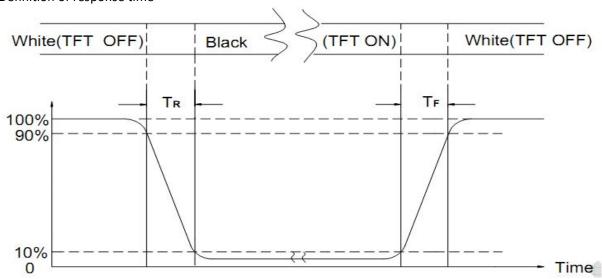
Note (3) Definition of Contrast Ratio (CR)

The contrast ratio can be calculated by the following expression

Contrast Ratio (CR) = L255 / L0

L255: Luminance of gray level 255, L0: Luminance of gray level 0

Note (4) Definition of response time



Note (5) Definition of Transmittance (Module is without signal input)

Transmittance = Center Luminance of LCD / Center Luminance of Back Light x 100%

Note (6) Definition of color chromaticity (CIE1931)

Color coordinates measured at the center point of LCD



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10. Reliability Test Conditions and Methods

1	High Temperature		
	Storage	85°C±2°C×96Hours	
2	Low Temperature Storage	-30°C±2°C×96Hours	
3	High Temperature Operating	85°C±2°C×96Hours	
4	Low Temperature Operating	-30°C±2°C×96Hours	
5	Temperature Cycle(Storage)	-20°C 25°C 60°C (30min) 1cycle Total 10cycle	Inspection after 2~4hours
6	Damp Proof Test (Storage)	50°C±5°C×90%RH×96Hours	storage at room temperature, the samples should be free from
7	Vibration Test	Frequency:10Hz~55Hz~10Hz Amplitude:1.5mm X,Y,Z direction for total 3hours (packing condition test will be tested by a carton)	defects: 1, Air bubble in the LCD. 2, Seal leak. 3, Non-display. 4, Missing segments.
8	Drooping Test	Drop to the ground from 1M height one time every side of carton. (packing condition test will be tested by a carton)	5, Glass crack.6, Current IDD is twice higher than initial value.7, The surface shall be free from damage.
9	ESD Test	Voltage:±8KV,R:330Ω,C:150PF,Air Mode,10times	8, The electric characteristic requirements shall be satisfied.
10	Image Sticking Test	25 ± 2°C Operation with test pattern sustained for 2 hrs, then change to gray pattern immediately. After 5 mins, the mura must be disappeared completely	

REMARK:

- 1, The Test samples should be applied to only one test item.
- 2, Sample side for each test item is $5\sim10$ pcs.
- 3, For Damp Proof Test, Pure water(Resistance>10M Ω) should be used.
- 4,In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
- 5, EL evaluation should be accepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and Fluorescence EL has.
- 6, Failure Judgment Criterion: Basic Specification Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.



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11. Inspection Standard

11.1 Scope

Specifications contain

11.1.1 Display Quality Evaluation

11.1.2 Mechanics Specification

11.2 Sampling Plan

Unless there is other agreement, the sampling plan for incoming inspection shall follow MIL-STD-105E.

11.2.1 Lot size: Quantity per shipment as one lot (different model as different lot).

11.2.2 Sampling type: Normal inspection, single sampling.

11.2.3 Sampling level: Level II.

11.2.4 AQL: Acceptable Quality Level

Major defect: AQL=0.65 Minor defect: AQL=1.5

11.3 Panel Inspection Condition

11.3.1 Environment:

Room Temperature: 25±5°C.

Humidity: 65±5% RH.

Illumination: 300 ~ 700 Lux.

11.3.2 Inspection Distance:

35±5 cm

11.3.3 Inspection Angle:

The vision of inspector should be perpendicular to the surface of the Module.

11.3.4 Inspection time:

Perceptibility Test Time: 20 seconds max.



11.4 Inspection Plan

Class	Item	Judgment	Class
	Outside and inside package.	"MODEL NO.", "LOT NO." and "QUANTITY" should indicate on the package.	Minor
Packing & Indicate	2. Model mixed and quantity.	Other model mixed Quantity short or over	Major
	3. Product indication.	"MODEL NO." should indicate on the product.	Major
	4. Dimension, LCD glass scratch and scribe defect.	According to specification or drawing.	Major
	5. Viewing area.	Polarizer edge or LCD's sealing line is visible in the viewing areaRejected.	Minor
	6. Blemish, black spot, white spot in the LCD and LCD glass cracks.	According to standard of visual inspection.(inside viewing area)	Minor
	7. Blemish, black spot, white spot and scratch on the polarizer.	According to standard of visual inspection.(inside viewing area)	Minor
Appearance	8. Bubble in polarizer.	According to standard of visual inspection.(inside viewing area)	Minor
	9. LCD's rainbow color.	Strong deviation color (or newton ring) of LCDRejected. Or according to limited sample.(if needed, and inside viewing area)	Minor
	-	According to specification or drawing.(inside viewing area)	Major
	11. Missing line.	Missing dot line character	Major
Electrical	12.Short circuit. Wrong pattern display.	No display, wrong pattern display, current consumption. Out of specification	Major
	13. Dot defect.(for color and TFT)	According to standard of visual Inspection.	Minor



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11.5 Standard Of Visual Inspection

No.	Class	Item	Judgment			
			(A) Round type: Unit: mm			
			Diameter (mm.) Acceptable Q'ty			
			Φ≦0.2 Disregard			
		Black and white spot.	$0.2 < \Phi \le 0.25$ 3(Distance>5mm)			
		Foreign materiel.	0.25 < Ф 0			
11.5.1	Minor	Dust.	Note: $\Phi = (length+width)/2$			
		Blemish.	(B) Linear type: Unit: mm			
		Scratch.	Length Width (mm.) Acceptable Q'ty			
			W≤0.03 Disregard			
			$L \le 5.0$ $0.03 < W \le 0.05$ $2(Distance > 5mm)$			
			0.05 < W FOLLOW ROUND TYPE			
			Unit: mm.			
			Diameter Acceptable Q'ty			
11.5.2	Minor	Dent on polarizer.	$\Phi \le 0.2$ Disregard			
		$0.2 < \Phi \leq 0.5$ 2(Distance>5mm)				
			0.5 < Φ 0			
			Unit: mm.			
		Bubble in polarizer.	Diameter Acceptable Q'ty			
11.5.3	Minor		$\Phi \le 0.2$ Disregard			
			$0.2 < \Phi \le 0.5$ 2(Distance>5mm)			
			0.5 < Φ 0			
			Items Acceptable Q'ty			
			Bright dot N ≤1			
			Dark dot N ≤1			
			Total dot $N \leq 2$			
11.5.4	Minor	Dot defect	Pixel define: Pixel Dot Dot Dot Note1: The definition of dot: The size of a defective dot over 1/2 of whole dot is regarded as one defective dot. Note 2: Bright dot: Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. Note 3: The bright dot defect must be visible through 2% ND filter Note 4: Dark dot: Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue pattern.			
11.5.5	Minor	Mura	ND 5% (In 50% gray screen)			



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No.	Class	Item	Judgment
11.5.6	Minor	LCD glass chipping.	Y>S Reject
11.5.7	Minor	LCD glass chipping.	X or Y>S Reject
11.5.8	Major	LCD glass crack.	T Y>(1/2) T Reject
11.5.9	Major	LCD glass scribe defect.	1. a>L/3, A>1.5mm Reject 2. B : According to dimension
11.5.10	Minor	LCD glass chipping. (on the terminal area)	$\Phi = (x+y)/2 > 2.5 \text{mm}$ Reject
11.5.11	Minor	LCD glass chipping. (on the terminal surface)	Y>(1/3)T Reject
11.5.12	Minor	LCD glass chipping.	T Y>T Reject



12. Handling Precautions

12.1 Mounting Method

The LCD panel of MT USA TFT module consists of two thin glass plates with polarizes which easily be damaged.

And since the module in so constructed as to be fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be needed when handling the LCD modules.

12.2 Caution of LCD Handling And Cleaning

When cleaning the display surface, Use soft cloth with solvent

[Recommended below] and wipe lightly

- Isopropyl alcohol
- Ethyl alcohol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvent:

- Water
- Aromatics

Do not wipe ITO pad area with the dry or hard materials that will damage the ITO patterns

Do not use the following solvent on the pad or prevent it from being contaminated:

- Soldering flux
- Chlorine (CI), Sulfur (S)

If goods were sent without being silicon coated on the pad, ITO patterns could be damaged due to the corrosion as time goes on.

If ITO corrosion happen by miss-handling or using some materials such as Chlorine (CI), Sulfur (S) from customer, Responsibility is on customer.

12.3 Caution Against Static Charge

The LCD module use C-MOS LSI drivers, so we recommended that you:

Connect any unused input terminal to power or ground, do not input any signals before power is turned on, and ground your body, work/assembly areas, and assembly equipment to protect against static electricity.

12.4 Packing

- Module employs LCD elements and must be treated as such.
- Avoid intense shock and falls from a height.
- To prevent modules from degradation, do not operate or store them exposed direct to sunshine or high temperature/humidity

12.5 Caution for operation

It is an indispensable condition to drive LCD's within the specified voltage limit since the higher



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- voltage then the limit cause the shorter LCD life.
- An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- Response time will be extremely delayed at lower temperature then the operating temperature range and on the
 other hand at higher temperature LCD's how dark color in them. However those phenomena do not mean
 malfunction or out of order with LCD's, which will come back in the specified operation temperature.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- Slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%Rh or less is required.

12.6 Storing

In the case of storing for a long period of time for instance, for years for the purpose or replacement use, the following ways are recommended.

- Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it. And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light's keeping the storage temperature range.
- Storing with no touch on polarizer surface by the anything else.
 [It is recommended to store them as they have been contained in the inner container at the time of delivery from us

12.7 Safety

- It is recommendable to crash damaged or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- When any liquid leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water



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13. Precaution for Use

13.1

A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity. Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.

13.2

On the following occasions, the handing of problem should be decided through discussion and agreement between responsible of the both parties.

- When a question is arisen in this specification
- When a new problem is arisen which is not specified in this specifications
- When an inspection specifications change or operating condition change in customer is reported to MICROTIPS TFT, and some problem is arisen in this specification due to the change
- When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.



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14. Packing

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