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## LIQUID CRYSTAL DISPLAY MODULE MODEL: NMTC-S16205DFGHSAY-07 Customer's No.:

Acceptance			

Microtips Technology Inc. 12F. No.31 Lane 169, Kang Ning St., His-Chih, Taipei Hsien, Taiwan, R.O.C. FAX: 886-2-26958625

Approved and Checked by

Approved by	Check	Made by	
微端	微端	微端	微端
2017/08/22	2017/08/22	2017/08/22	2017/08/22
連俊傑	幸有乾	洪振益	許瓊窈



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## **Revise Records**

Rev.	Date	Contents	Written	Approved
A.	2017/08/22	See Note 1.	Jill Hsu	Danny Lien
	_		_	_
			_	_

## Special Notes

Note 1.	The LCD module is compliant with RoHS.
Note 2.	
Note 3.	
Note 4.	
Note 5.	



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# The Microtips Customized LCD module, model: NMTC-S16205DFGHSAY-07 is compliant with RoHS

## 1. General Specifications

Operating Temperature. : Min.  $-20^{\circ}$ C  $\sim$  Max.  $70^{\circ}$ C

Storage Temperature. : Min.  $-30^{\circ}$ C  $\sim$  Max.  $80^{\circ}$ C

Display Format : 16 characters x 2 lines

Display Fonts :  $5 \times 7 \text{ dots} + \text{cursor} (1 \text{ character})$ 

Viewing Area : 69.0 (W) x 16.4 (H) mm

Outline Dimensions : 80.0 (W) x 36.0 (H) x 12.9 max. (D) mm

Weight : N/A

LCD Type : STN / Positive, Gray mode / Transflective

Viewing Direction : 6:00

Backlight : Array type LED (Yellow Green)

LCD LSI : ST 7066

Drawings : As attached drawings



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## **Electrical Specifications**

## 2.1 Absolute Maximum Ratings

 $V_{SS} = 0V$ 

Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage (Logic)	$V_{\rm DD} - V_{\rm SS}$		- 0.3	7.0	V
Supply Voltage (LCD Drive)	$V_{LCD}$		V <sub>DD</sub> -15.0	$V_{DD} + 0.3$	V
Input Voltage	$V_{\rm I}$		- 0.3	$V_{DD} + 0.3$	V

#### 2.2 DC Characteristics

 $Ta = 25^{\circ}C, V_{SS} = 0V$ 

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage (Logic)	$V_{DD} - V_{SS} \\$		4.5		5.5	V
Supply Voltage (LCD Drive)	$V_{DD} - V_{O} \\$	Shown in 3.1			V	
High Level (Input Voltage)	$ m V_{IH}$	$V_{DD} = 5.0V$	2.2		$V_{DD}$	V
Low Level (Input Voltage 0	$V_{\mathrm{IL}}$	$V_{\rm DD} = 5.0 \mathrm{V}$	-0.3		0.6	V
High Level (Output Voltage)	$V_{\mathrm{OH}}$	$I_{OH} = -0.205 \text{mA}$	2.4		$V_{DD}$	V
Low Level (Output Voltage)	$V_{ m OL}$	$I_{OL} = 1.2 \text{mA}$	0		0.4	V
Supply Current	$I_{DD}$	$V_{DD} - V_{SS} = 5.0V$		1.5	5.0	mA

 $Ta = 25^{\circ}C, V_{SS} = 0V$ 

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage (Logic)	$V_{DD} - V_{SS}$		2.7		4.5	V
Supply Voltage (LCD Drive)	$V_{DD} - V_{O}$		Shown in 3	.1		V
High Level (Input Voltage)	$ m V_{IH}$	$V_{DD} = 3.0V$	$0.7~\mathrm{V_{DD}}$		$V_{ m DD}$	V
Low Level (Input Voltage)	V <sub>IL</sub>	$V_{DD} = 3.0V$	-0.3		0.55	V
High Level (Output Voltage)	$V_{\mathrm{OH}}$	$I_{OH} = -0.1 \text{mA}$	$0.75~\mathrm{V_{DD}}$		$V_{DD}$	V
Low Level (Output Voltage)	$V_{OL}$	$I_{OL} = 0.1 \text{mA}$	0		$0.2~\mathrm{V_{DD}}$	V
Supply Current	$I_{DD}$	$V_{DD} - V_{SS} = 5.0V$		1.5	5.0	mA



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## 2.3 AC Characteristics

 $V_{DD} = 4.5 V \sim 5.5 V$ 

Parameter	Symbol	Conditions	Min.	Max.	Units
Enable Cycle Time	$t_{\mathrm{CYC}}$	Fig.1, 2	500		ns
Enable Pulse Width	$PW_{EH}$	Fig.1, 2	230		ns
Enable Rise / Fall Time	$t_{\rm Er},t_{\rm Ef}$	Fig.1, 2		20	ns
Address Setup Time	$t_{AS}$	Fig.1, 2	40		ns
Address Hold Time	$t_{ m AH}$	Fig.1, 2	10		ns
Write Data Setup Time	$t_{ m DSW}$	Fig.1	80		ns
Write Data Hold Time	$t_{ m DHW}$	Fig.1	10		ns
Read Data Delay Time	$t_{\mathrm{DDR}}$	Fig.2		120	ns
Read Data Hold Time	$t_{ m DHR}$	Fig.2	5		ns

 $V_{\rm DD}=2.7V\sim4.5V$ 

Parameter	Symbol	Conditions	Min.	Max.	Units
Enable Cycle Time	$t_{ m CYC}$	Fig.1, 2	1000		ns
Enable Pulse Width	$PW_{EH}$	Fig.1, 2	450		ns
Enable Rise / Fall Time	$t_{\rm Er},t_{\rm Ef}$	Fig.1, 2		25	ns
Address Setup Time	$t_{AS}$	Fig.1, 2	60		ns
Address Hold Time	$t_{AH}$	Fig.1, 2	20		ns
Write Data Setup Time	$t_{ m DSW}$	Fig.1	195		ns
Write Data Hold Time	$t_{ m DHW}$	Fig.1	10		ns
Read Data Delay Time	$t_{ m DDR}$	Fig.2		360	ns
Read Data Hold Time	$t_{\mathrm{DHR}}$	Fig.2	5		ns



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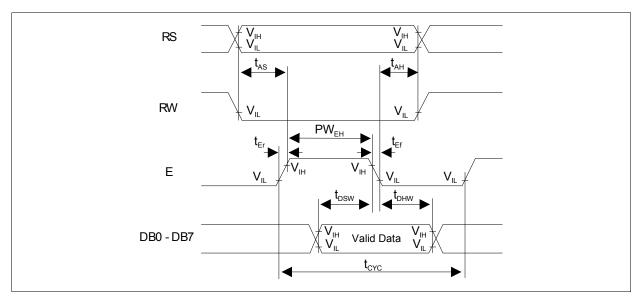


Fig.1 Write Operation Timing

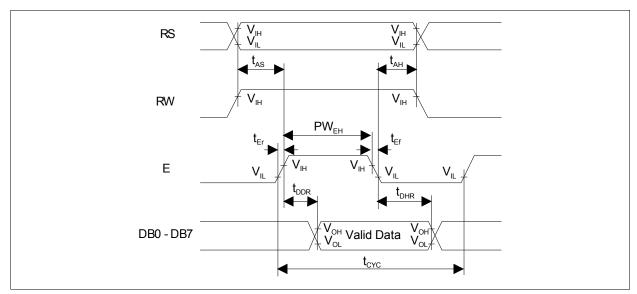


Fig.2 Read Operation Timing



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## 2.4 Lighting Specifications

## 2.4.1 Absolute Maximum Ratings

Ta = 25°C

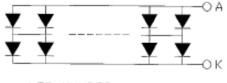
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Forward Current	$I_F$	$V_F = 4.8V$			240	mA
Reverse Voltage	$V_R$				10	V
LED Power Dissipation	$P_D$				1.152	W

## 2.4.2 Operating Characteristics

Ta = 25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Forward Voltage	$V_{F}$	$I_F = 120 \text{mA}$		4.0	4.8	V
Luminance of Backlight Surface*	L	$I_F = 120 \text{mA}$	130	185		cd/m <sup>2</sup>
Spectrum radiation bandwidth	$\Delta_{\lambda}$	I <sub>F</sub> = 20mAa / per chip		30		nm
Peak emission wavelength	$\lambda_{ m P}$	$I_F = 20 \text{mA} / \text{per chip}$	567	570	573	nm
AVG, X of 1931 C.I.E	X	$I_F = 120 \text{mA}$	0.40	0.43	0.46	
AVG, Y of 1931 C.I.E	Y	$I_F = 120 \text{mA}$	0.51	0.53	0.56	

<sup>\*</sup>Measured from the surface of backlight.



LED NUMBERS:2X12=24 A:ANDDE/K:CATHODE



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#### 3. Optical Specifications

3.1 LCD Driving Voltage

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Recommended LCD Driving Voltage Note 1		Ta = -20 °C		4.8	4.95	V
	$V_{DD} - V_{O}$	Ta = 25 °C		4.5		V
Driving voltage Note 1		Ta = 70 °C	4.05	4.2		V

Note 1: Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

#### 3.2 Optical Characteristics

Ta = 25 °C, 1/16 Duty, 1/5 Bias,  $V_{DD}$  = 5.0V (Note 4),  $\theta$  = 0°,  $\phi$  = 270°

Par	amete	r	Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast	Ratio	Note 1	CR	$\theta = 0^{\circ},  \phi = 0^{\circ}$		5		
		Hor.	$\theta_{R}$	$\theta_{R}$		35		deg.
Viewing Ar	ngle	пог.	$\theta_{ m L}$	Cr≧2		30		deg.
(Shown in 3	3.3)	Van	$\theta_{\mathrm{B}}$	CI ≤ 2		40		deg.
	Ver.		$\theta_{\mathrm{F}}$			45		deg.
Response	Ris	se Note 2 T <sub>ON</sub>				250	750	ms
Time	Dec	ay Note 3	$T_{OFF}$			300	900	ms

Note 1: Contrast ratio is defined as follows.  $CR = L_C$ 

 $CR = L_{OFF} / L_{ON}$ 

 $L_{\text{ON}}\!\!:$  Luminance of the ON segments,  $L_{\text{OFF}}\!\!:$  Luminance of the OFF segments

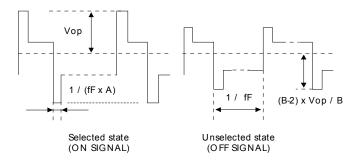
Note 2: The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.

Note 3: The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is applied.

Note 4: Definition of Driving Voltage  $V_D$ . Assuming that the typical driving waveforms shown below are applied to the LCD Panel at / A Duty – 1/B Bias (A: Duty Number, B: Bias Number). Driving voltage  $V_D$  is defined s follows:  $V_D = (Vth1 + Vth2)/2$ 

Vth1: The voltage VO – P that should provide 50% of the saturation level in the luminance at the segment which the ON signal is applied to.

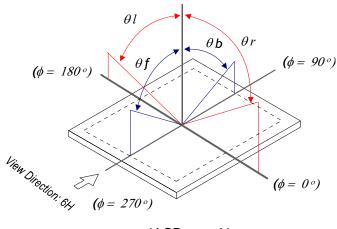
Vth2: The voltage VO – P that should provide 50% of the saturation level in the luminance at the segment which the OFF signal is applied to.





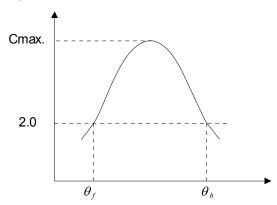
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#### 3.3 Definition of Viewing Angle and Optimum Viewing Area



(LCD panel)

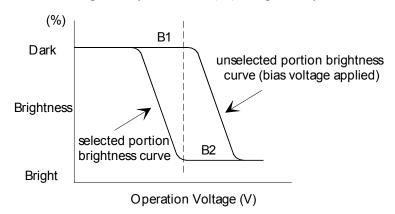
## 3.4 Definition of Viewing Angle $\theta_f$ and $\theta_b$



Viewing angles  $\theta$  (  $\phi$  fixed)

Optimum viewing angle with the naked eye and viewing angle  $\theta$  at Cmax. Above are not always the same.

#### 3.5 Definition of Contrast C, C= Brightness of selected dot (B1) / Brightness of unselected dot (B2)





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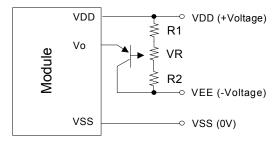
## I/O Terminal

#### 4.1 Pin Assignment

No.	Symbol	Level	Function
1.	VSS		Power Supply (0V, GND)
2.	VDD		Power Supply for Logic
3.	VEE (Vo)		Power Supply for LCD Drive
4.	RS	H/L	Register Select Signal
5.	R/W	H/L	Read / Write Select Signal H: Read L: Write
6.	Е	H/L	Enable Signal (No pull – up Resister)
7.	DB0	H/L	Data Bus Line / Non – connection at 4 – bit operation
8.	DB1	H/L	Data Bus Line / Non – connection at 4 – bit operation
9.	DB2	H/L	Data Bus Line / Non – connection at 4 – bit operation
10.	DB3	H/L	Data Bus Line / Non – connection at 4 – bit operation
11.	DB4	H/L	Data Bus Lin
12.	DB5	H/L	Data Bus Line
13.	DB6	H/L	Data Bus Line
14.	DB7	H/L	Data Bus Line
15.	LEDA		Power Supply for Backlight / LED Anode (+)
16.	LEDK		Power Supply for Backlight / LED Cathode (-)

## 4.2 Example of Power Supply

It is recommended to apply a potentiometer for the contrast adjust due to the tolerance of the driving voltage and its temperature dependence.

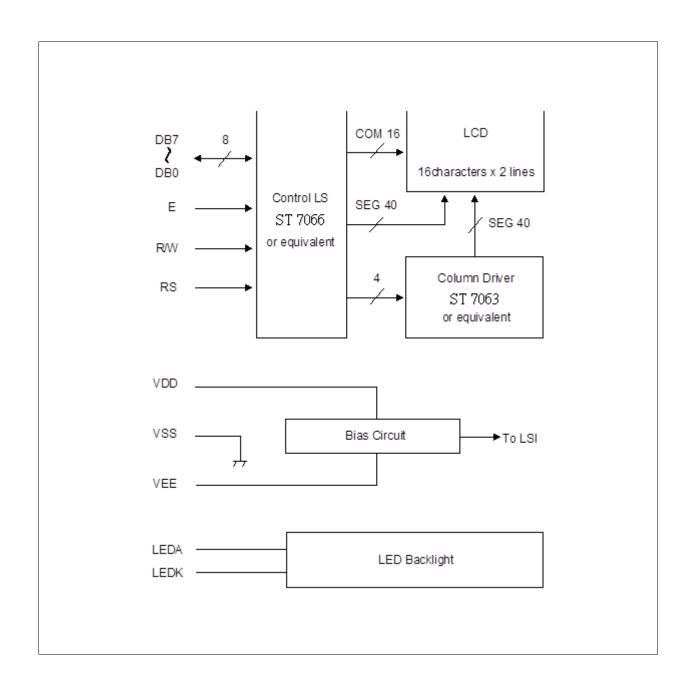


R1+R2+VR=10  $\sim$  20K $\Omega$ Tr=2SA1202 or equivalent



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## 4.3 Block Diagram





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#### 5. Reliability Test

#### 5.1 Test Item

No change on display and in operation under the following test condition.

No.	Test Item	Description	Condition	Note	
1.	High Temperature (Operation)	Durability test under long time high temperature with electrical stress (voltage, current) $70^{\circ}\text{C} \pm 2^{\circ}\text{C}$ , 96hrs			
2.	High Temperature (Storage)	Durability test under long time high temperature storage	80°C ± 2°C, 96hrs	4	
3.	Low Temperature (Operation)	Durability test under long time low temperature with electrical stress (voltage, current)	-20°C ± 2°C, 96hrs	3	
4.	Low Temperature (Storage)	Durability test under long time low temperature storage	-30°C ± 2°C, 96hrs	3, 4	
5.	Damp Proof Test	Durability test under long time high temperature and high humidity $40^{\circ}\text{C} \pm 2^{\circ}\text{C}, 90 \sim 95\%$ RH 96hrs			
6.	Vibration Test	Total fixed amplitude: 1.5mm Vibration frequency: 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes		5	
7.	Drop Test	To be measured after dropping from 60cm high in packing state.  Dropping method A corner: of Edge dropping B, C, D edge Face dropping E, F, G face Concrete Surface	od corner dropping nce e: once		

Note 1: Unless otherwise specified, tests will be conducted under the following condition,

Temperature :  $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Humidity :  $65\% \pm 5\%$ 

Note 2: Unless otherwise specified, tests will be not conducted under functioning state.

Note 3: No dew condensation to be observed.

Note 4: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5: Vibration test will be conducted to the product itself without putting it in a container.



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## 5.2 Judgment Standard

Failure Mode	Test Item				m			Judgment Standard	
3 413 413 413	1	2	3	4	5	6	7		
Orientation	*	*	*	*	*			No remarkable degradation of appearance under bias / non – bias condition	
Current Value (IAC)	*	*	*	*	*			No remarkable increase	
Contrast	*		*	*	*			No remarkable poor contrast	
Domain	*	*	*	*	*			Less than 20% of all dots have reverse tilt of more than on third of one dot area.	
Bubble (Inside Cell)	*	*	*	*	*	*		As per "Appearance Standard" (Note. In – cluding one which disappear after 25°C 2H)	
Polarizer	*				*	*		As per "Appearance Standard" no remarkable appearance change	
Glass Damage							*	As per "Appearance Standard"	

Note. 1. \* is strong linkage between Failure Mode and Test Item.

- 2. Number of Test Item should be referred to former page.
- 3. Judgment and Standard value should be fixed by other inspection standard and criteria samples.

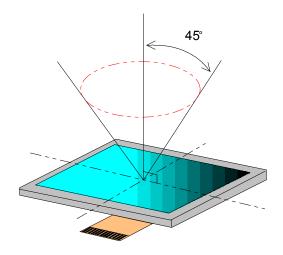


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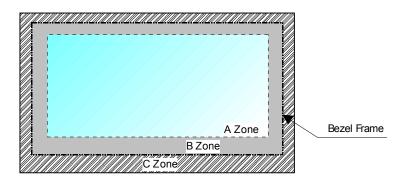
#### **Appearance Standards 6.**

#### 6.1 Inspection Conditions

The LCD shall be inspected under 40W white fluorescent light. The distance between the eyes and the sample shall be more than 30cm. All directions for inspecting the sample should be within 45° against perpendicular line.



## 6.2 Definition of Applicable Zones



A Zone: Active display area

B Zone: Area from outside of "A Zone" to validity viewing area

C Zone: Rest parts

A Zone + B Zone = Validity viewing area



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## 6.3 Standards

No.	Parameter		Criteria						
		(1) Round Shape							
		Zone	Acc	ceptable Nui	mber				
		Dimension (mm)	A	В	C				
		D ≤ 0.1	*	*	*				
		$0.1 < D \le 0.2$	3	5	*				
		$0.2 < D \le 0.25$	2	3	*				
		$0.25 < D \le 0.3$	0	1	*				
		0.3 < D	0	0	*				
1.	Black and White	D = (Long + Short) / 2 *: Disre	egard						
	Spots, Foreign Substances	(2) I : GI							
		(2) Line Shape  Zone  Zone	Δ c	ceptable Nu	mher				
		X (mm) Y (mm)	A	В	С				
		0.03 ≥ W	*	*	*				
		$2.0 \geq L  0.05 \geq W$	3	3	*				
		1.0 ≥ L 0.1 ≥ W	3	3	*				
		0.1 < W	In t	he same wa	y (1)				
		X: Length Y: Width *: Disre	egard						
		Total defects shall not exceed 5	•						
		7	Α -	nontohla N	mah an				
		Zone Dimension (mm)	Acc	ceptable Nui B	mber C				
	Air Bubbles (between glass & polarizer)	Differsion (film) $D \leq 0.3$	*	*	*				
2.		$0.3 < D \le 0.4$	3	*	*				
-		$0.4 < D \le 0.4$	2	3	*				
		0.4	0	0	*				
		*: Disregard	<u> </u>	0					
		Total defects shall not exceed 3							

To be continued.....



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No.	Parameter	Criteria
3.	The Shape of Dot	(1) Dot Shape (with Dent)  O.15≥ → As per the sketch of left hand.  (2) Dot Shape (with Projection)  Should not be connected to next dot.  (3) Pin Hole  (X + Y) /2 ≤ 0.2mm (Less than 0.1mm is no counted.)  (4) Deformation  (X + Y) /2 ≤ 0.2mm  (X + Y) /2 ≤ 0.2mm
4.	Polarizer Scratches	Not to be conspicuous defects.
5.	Polarizer Dirt's	I f the stains are removed easily from LCDP surface, the module is not defective.
6.	Complex Foreign Substance Defects	Black spots, line shaped foreign substance or air bubbles between glass & polarizer should be 5pcs maximum in total.
7.	Distance between different Foreign Substance defects	$D \le 0.2$ : 20mm or more $0.2 < D$ : 40mm or more



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## 7. Handling and Precautions

The Following precautions will guide you in handling our product correctly.

- 1 Liquid crystal display devices
  - 1.1 The liquid crystal display device panel used in the liquid crystal display module is made of plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care.
  - 1.2 The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.
- 2 Care of the liquid crystal display module against static electricity discharge.
  - 2.1 When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect work tables against the hazards of electrical shock.
  - 2.2 Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
  - 2.3 Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.
- When the LCD module alone must be stored for long periods of time:
  - 3.1 Protect the modules from high temperature and humidity.
  - 3.2 Keep the modules out of direct sunlight or direct exposure to ultra-violet rays.
  - 3.3 Protect the modules from excessive external forces.
- 4 Use the module with a power supply that is equipped with an over current protector circuit, since the module is not provided with this protective feature.
- Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.
- Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.



Messrs.								
Product Specification	Model	NMTC-S16205DFGHSAY-07	Rev. No.	Issued Date.	Page.			
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#### 8. Warranty

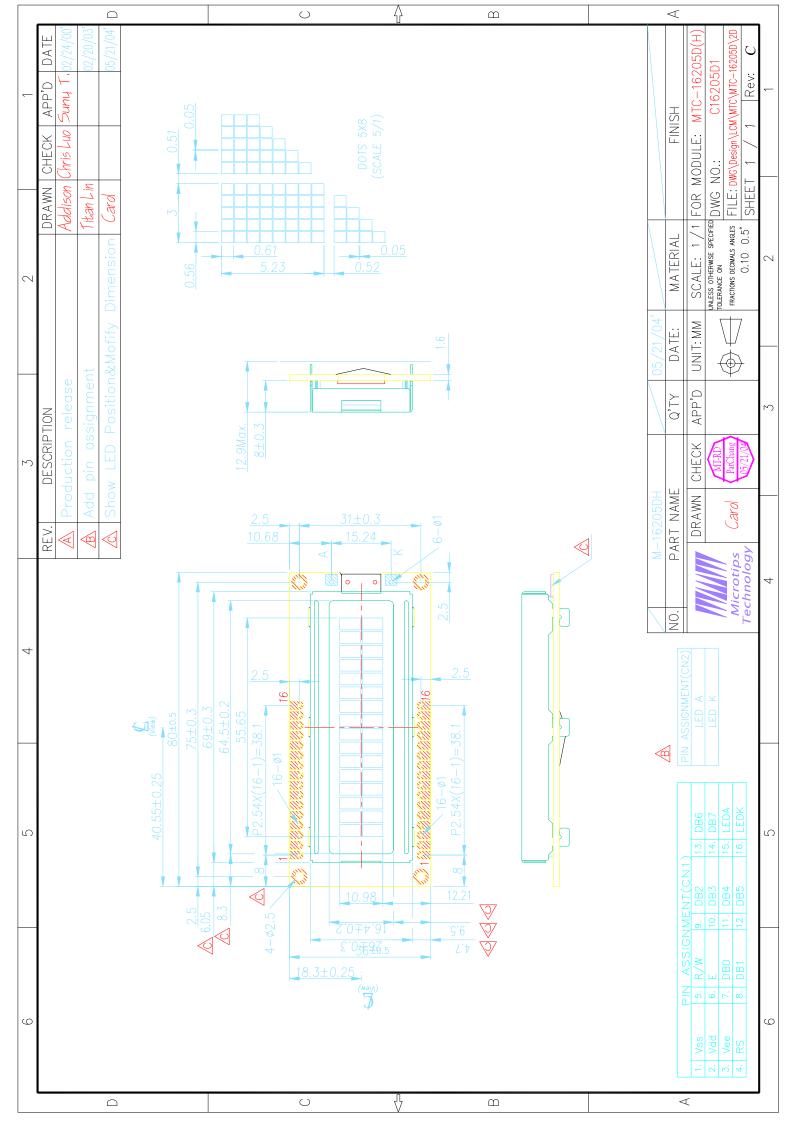
This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1 13-month guarantee starts from the date code.
- We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product. Microtips-origin longer than one year from Microtips production.

## 9. <u>Dimensional Outlines</u>

• See the next page......





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**Authorized Distributor** 

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Microtips Technology: NMTC-S16205DFGHSAY-07