



# Microtips

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## TECHNOLOGY

Model No: MTD0210ADP04RF-1

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## Record of Revision

Date	Revision No.	Summary
2020-04-23	1.0	Rev 1.0 was issued

## 1. Scope

This data sheet is to introduce the specification of MTD0210ADP04RF-1, active matrix TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC and a backlight unit. The 2.1" display area contains 480(RGB) x 480 pixels.

As to basic specification of the driver IC, refer to the IC specification and datasheet.

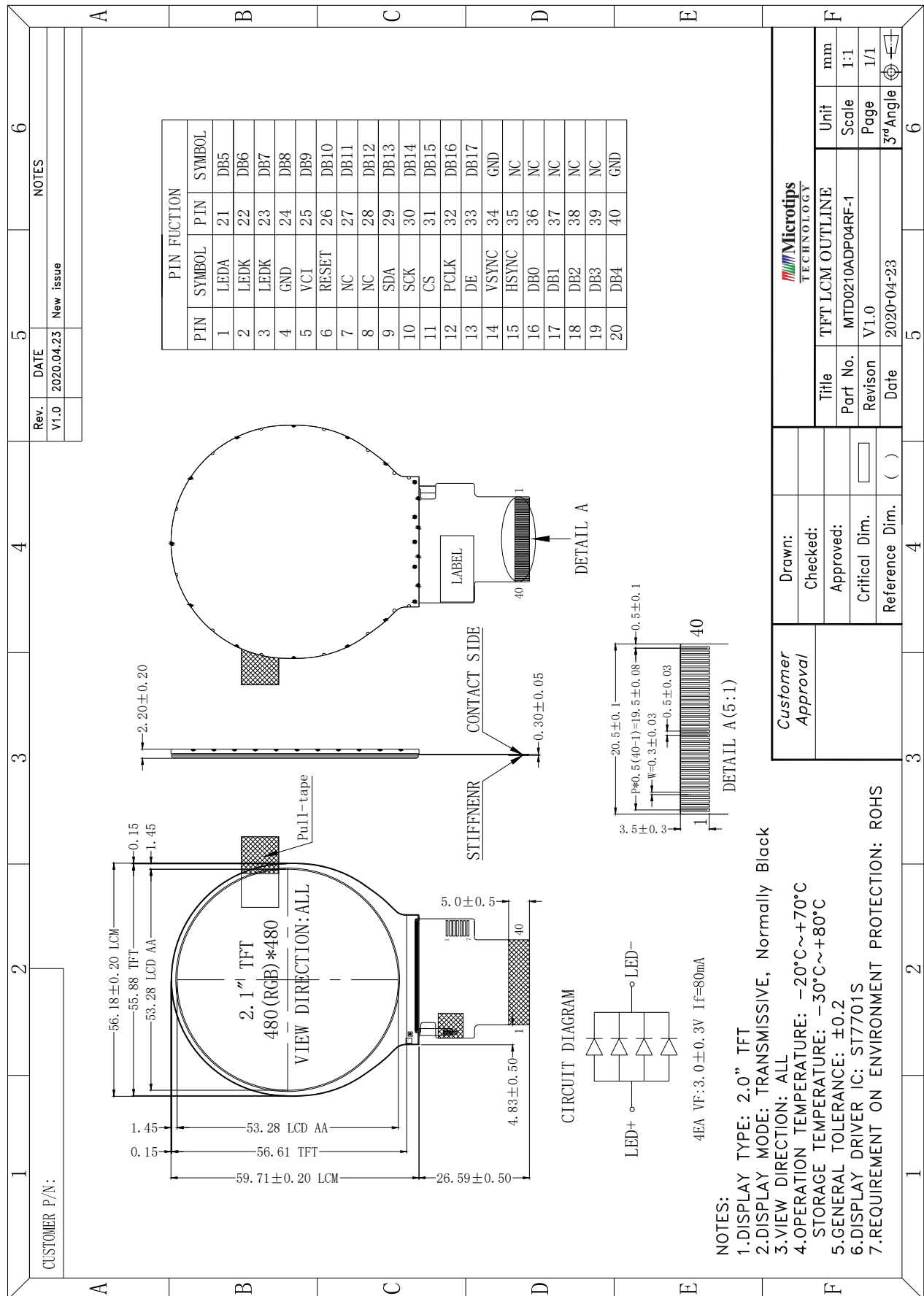
## 2. Application

Digital equipments which need color display, mobile navigator/video systems.

## 3. General Information

Item	Contents	Unit
Size	2.1	inch
Resolution	480(RGB) x 480	/
Interface	SPI/RGB	/
Technology type	IPS TFT	/
Pixel Configuration	R.G.B. Stripe	
Pixel Pitch	0.111 x 0.111	
Outline Dimension (W x H x D)	56.18 x 59.71 x 2.20	mm
Active Area	53.28 x 53.28	mm
Backlight Type	LED	/
Display Controller/ driver	ST7701S	/
Viewing Direction	ALL	

#### 4. Outline Drawing



## 5. Interface signals

### 5.1 LCM Pin Definition

No	Symbol	Description	Remark
1	LEDA	LED Backlight (Anode)	
2~3	LEDK	LED Backlight (Cathode)	
4	GND	No connection	
5	VCI	Power supply	
6	RESET	Reset pin, active "low"	
7~8	NC	No connection	
9	SDA	Serial input data bus	
10	SCK	Serial clock as serial interface	
11	CS	Chip select signal	
12	PCLK	Data clock	
13	DE	Data enable pin	
14	VSYNC	Vertical sync input in RGB mode	
15	HSYNC	Horizontal sync input in RGB mode	
16~33	DB0 – DB17	RGB data bus	
34	GND	Power ground	
35~39	NC	No connection	
40	GND	Power ground	

## 6. Absolute maximum Ratings

### 6.1. Electrical Absolute max. ratings

Parameter	Symbol	MIN	MAX	Unit	Remark
Power Supply Voltage	VCI	-0.3	3.6	V	

#### Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged.  
Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. VCI >VSS must be maintained.
3. Please be sure users are grounded when handing LCD Module.

### 6.2. Environment Conditions

Item	Symbol	MIN	MAX	Unit	Remark
Operating Temperature	TOPR	-20	70	°C	
Storage Temperature	TSTG	-30	80	°C	

Notes:

- The response time will become lower when operated at low temperature.
- Background color changes slightly depending on ambient temperature.  
The phenomenon is reversible.
- Ta≤40°C:85%RH MAX.  
Ta>40°C:Absolute humidity must be lower than the humidity of 85%RH at 40°C.

## 7. Electrical Specifications

### 7.1 Electrical characteristics

GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Power Supply	VCI	2.5	2.8	3.6	V	
Input voltage	H	V <sub>IH</sub>	0.7*VCI	-	V	
	L	V <sub>IL</sub>	0	-	V	

### 7.2 LED Backlight

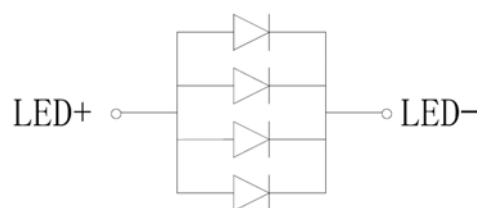
Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	IL	-	80	-	mA	
Forward Voltage	VL	2.7	3.0	3.3	V	Note1
LED life time	-	-	20,000	-	Hrs	Note2

Notes:

- The LED Supply Voltage is defined by the number of LED at Ta=25°C and IL =80mA.
- The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL =80mA. The LED lifetime could be decreased if operating IL is larger than 80mA.
- LED Backlight Circuit Diagram as follow:

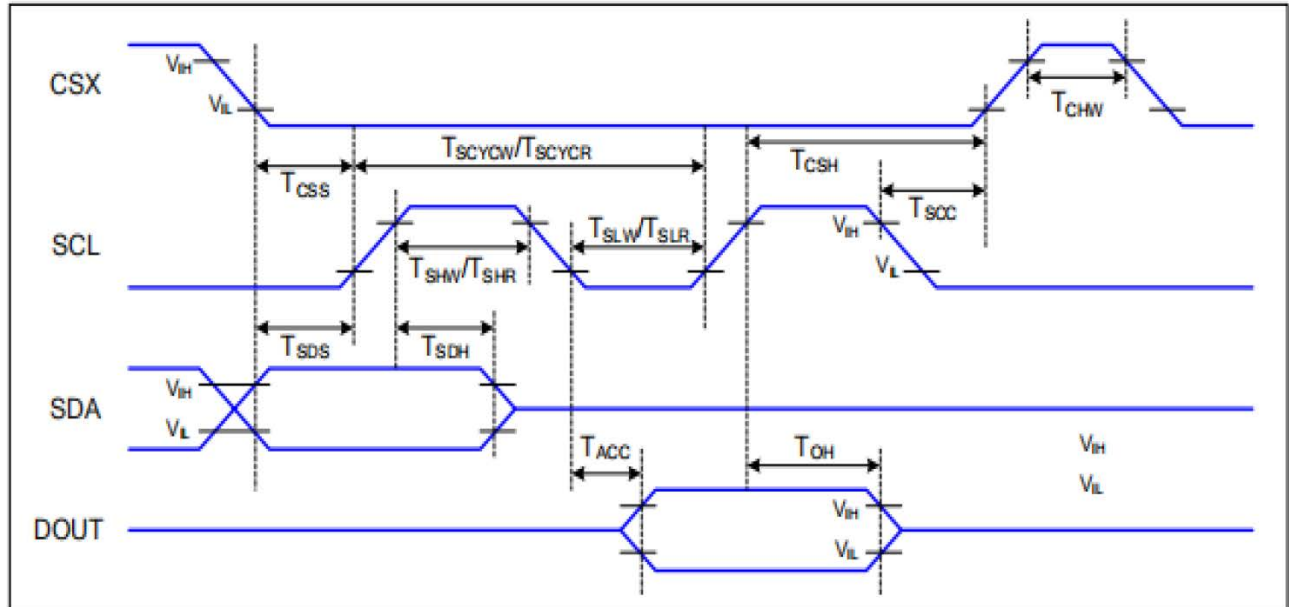
#### CIRCUIT DIAGRAM



4EA VF:3.0±0.3V If=80mA

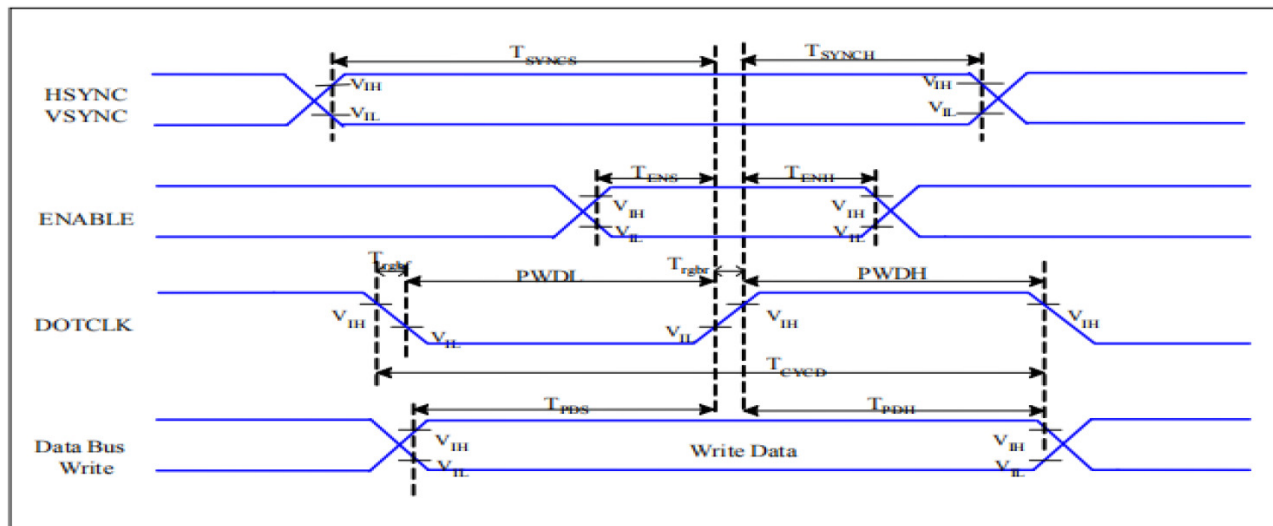
## 8. Command/AC Timing

### 8.1 Serial Interface Characteristics (3-line serial)



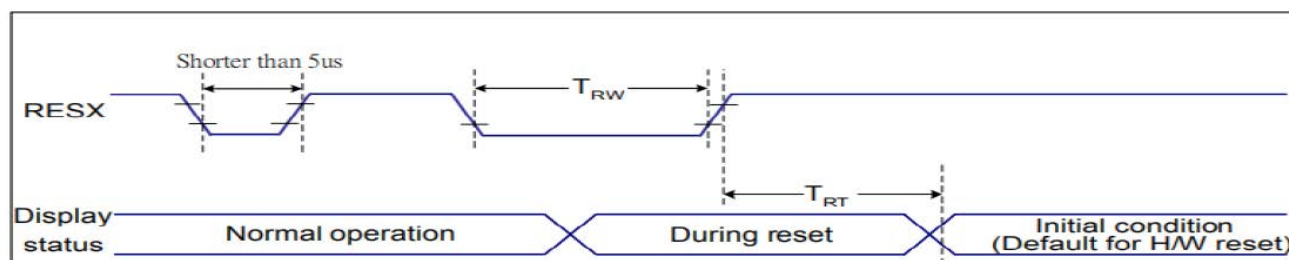
Signal	Symbol	Parameter	Min.	Max.	Unit	Description
CSX	tcss	Chip select setup time (write)	15	-	ns	
	tcsH	Chip select hold time (write)	15	-	ns	
	tcSS	Chip select setup time (read)	60	-	ns	
	tscC	Chip select hold time (read)	60	-	ns	
	tchW	Chip select "H" pulse width	40	-	ns	
SCL	tscYCW	Serial clock cycle (write)	66	-	ns	
	tshW	SCL "H" pulse width (write)	15	-	ns	
	tsLW	SCL "L" pulse width (write)	15	-	ns	
	tscYCR	Serial clock cycle (read)	150	-	ns	
	tshR	SCL "H" pulse width (read)	60	-	ns	
	tsLR	SCL "L" pulse width (read)	60	-	ns	
SDA (DIN)	tsDS	Data setup time	10	-	ns	
	tsDH	Data hold time	10	-	ns	

## 8.2 RGB Interface Characteristics



Signal	Symbol	Parameter	Min.	Max.	Unit	Description
HSYNC VSYNC	$t_{SYNCS}$	VSYNC, HSYNC setup time	5	-	ns	
ENABLE	$t_{ENS}$	Enable setup time	5	-	ns	
	$t_{ENH}$	Enable hold time	5	-	ns	
DOTCLK	$PWDH$	DOTCLK high-level pulse width	15	-	ns	
	$PWDL$	DOTCLK low-level pulse width	15	-	ns	
	$t_{CYCD}$	DOTCLK cycle time	33	-	ns	
	$t_{trghr}$ , $t_{trghf}$	DOTCLK rise/fall time	-	15	ns	
DB	$t_{PDS}$	PD data setup time	5	-	ns	
	$t_{PDH}$	PD data hold time	5	-	ns	

## 8.3 Reset timing



Related Pins	Symbol	Parameter	Min.	Max.	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5	ms
			-	120	ms



## 9. Optical Specification

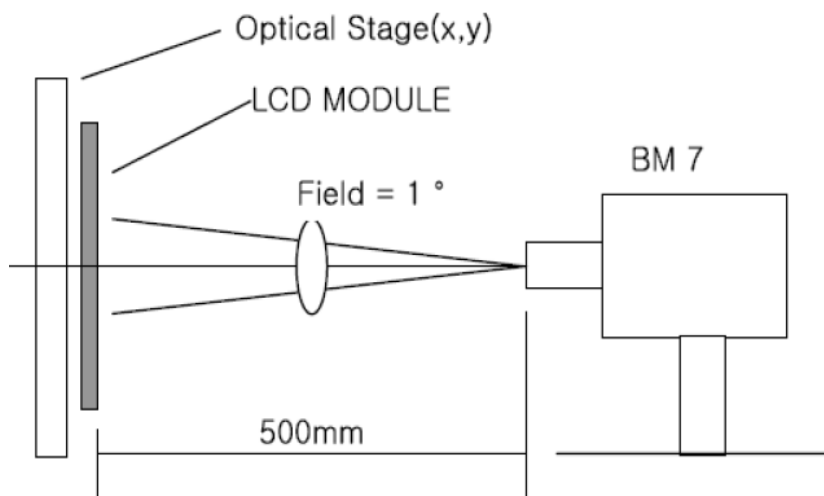
Ta=25°C

Item		Symbol	Condition	Min	Typ.	Max.	Unit	Remark
Contrast Ratio		CR	$\theta=0^{\circ}$	800	1000	-		Note1 Note2
Response Time		Tr+Tf	25℃	-	30	35	ms	Note1 Note3
View Angles		ΘT	CR≧10	-	80	-	Degree	Note 4
		ΘB		-	80	-		
		ΘL		-	80	-		
		ΘR		-	80	-		
Chromaticity	White	x	Brightness is on	Typ-0.05	TBD	Typ+0.05		Note5, Note1
		y			TBD			
	Red	x			TBD			
		y			TBD			
	Green	x			TBD			
		y			TBD			
	Blue	x			TBD			
		y			TBD			
Luminance		L		-	250	-	cd/m2	Note1 Note6
Uniformity		U		75	80	-	%	Note1 Note7

Note 1: Definition of optical measurement system.

Temperature = 25°C(±3°C)

LED back-light: ON, Environment brightness < 150 lx

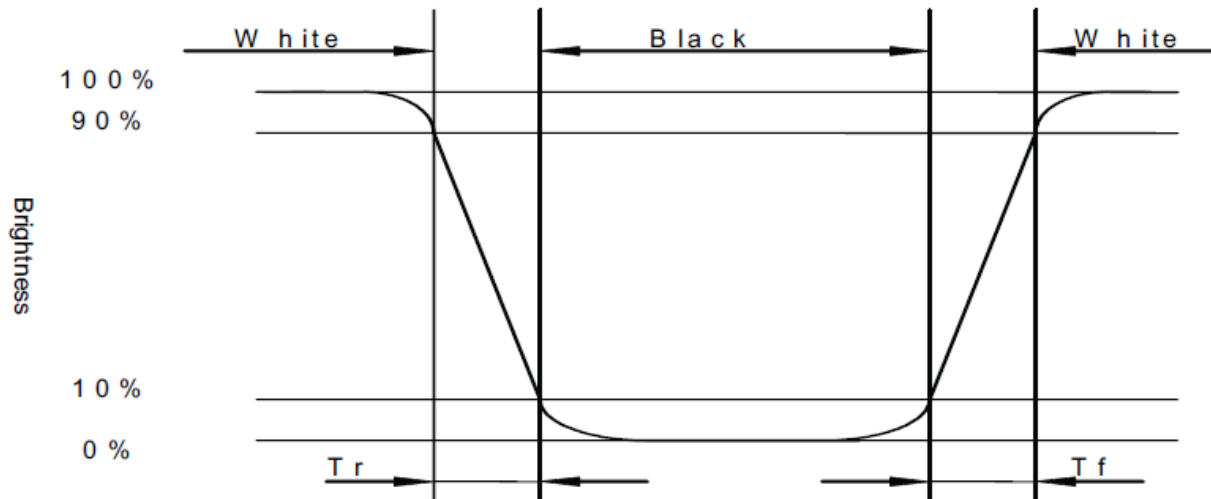


Note 2: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

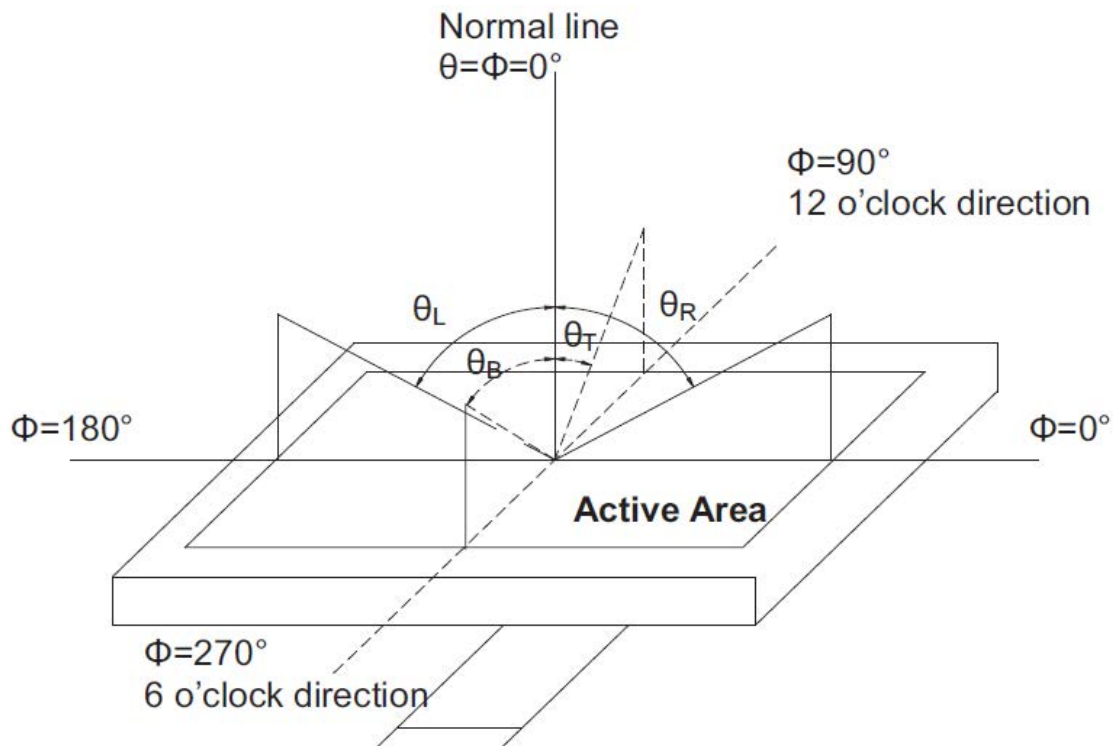
Note 3: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time,  $T_r$ ) and from white to black (Decay Time,  $T_f$ ).



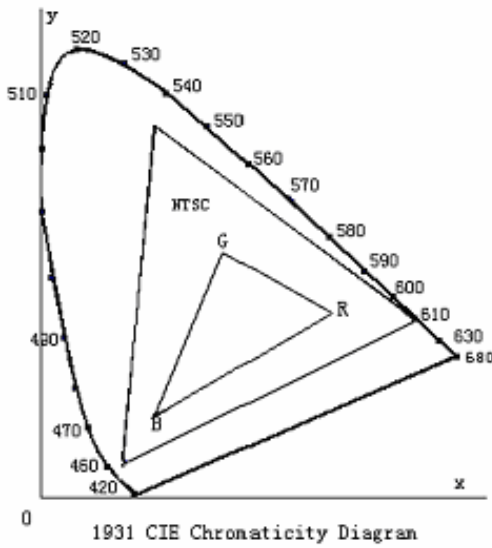
Note 4: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.



Note 5: Color chromaticity is defined as follow: (CIE1931)

Color coordinates measured at center point of LCD.



$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 6: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels "White" at the center of display area on optimum contrast.

Note 7: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity (U)} = \frac{\text{Minimum Luminance( brightness ) in 9 points}}{\text{Maximum Luminance( brightness ) in 9 points}}$$

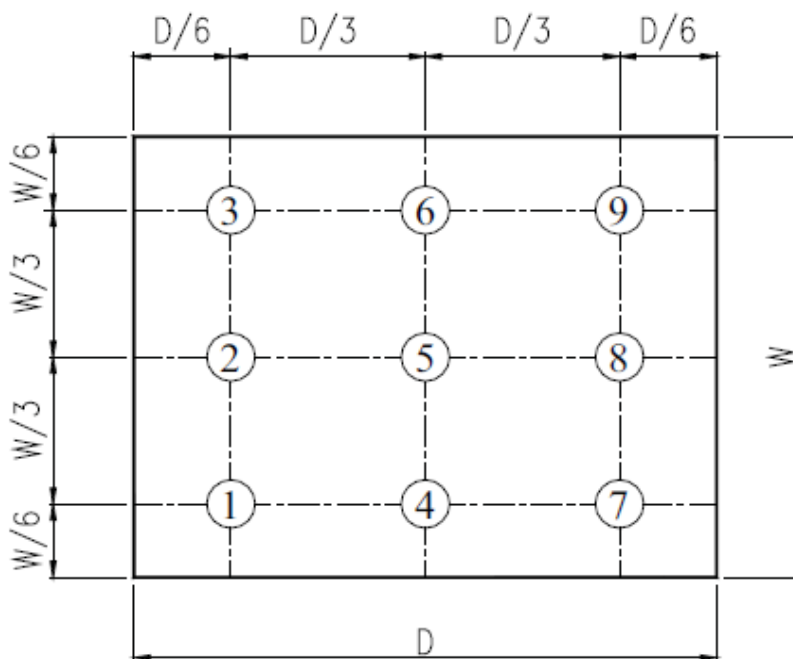


Fig. 2 Definition of uniformity

## 10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment criteria
1	High Temp Operation	Ta= +70℃, 96hrs	Per table in below
2	Low Temp Operation	Ta= -20℃, 96hrs	Per table in below
3	High Temp Storage	Ta= +80℃, 96hrs	Per table in below
4	Low Temp Storage	Ta= -30℃, 96hrs	Per table in below
5	High Temp & High Humidity Storage	Ta= +60℃, 90% RH, 96 hours	Per table in below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-30℃ 30 min~+80℃ 30 min, Change time:5min, 5Cycles	Per table in below
7	ESD (Operation)	Air discharge:+/-8KV, Contact discharge:4KV	Per table in below
8	Vibration (Non-operation)	10Hz~150Hz, 100m/s <sup>2</sup> , 120min	Per table in below
9	Shock (Non-operation)	Half- sine wave,300m/s <sup>2</sup> ,11ms	Per table in below
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	Per table in below

INSPECTION	CRITERION(after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel No other Defects of Alignment in Active area
Electrical current	Within device specifications
Function / Display	No Broken Circuit, No Short Circuit or No Black line No Other Defects of Display

## 11. Precautions for Use of LCD Modules

### 11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

### 11.2 Handling

- A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- B. Do not handle the product by holding the flexible pattern portion in order to assure the reliability
- C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.
- D. Provide a space so that the panel does not come into contact with other components.
- E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.
- F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.
- G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.
- H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

### 11.3 Static Electricity

- A. Ground soldering iron tips, tools and testers when they are in operation.
- B. Ground your body when handling the products.
- C. Power on the LCD module before applying the voltage to the input terminals.
- D. Do not apply voltage which exceeds the absolute maximum rating.
- E. Store the products in an anti-electrostatic bag or container.

### 11.4 Storage

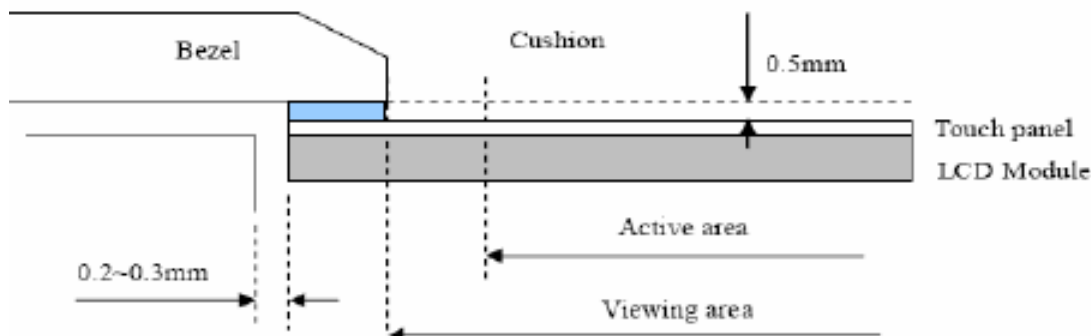
- A. Store the products in a dark place at  $+25^{\circ}\text{C} \pm 10^{\circ}\text{C}$  with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.
- B. Storage in a clean environment, free from dust, active gas, and solvent.

### 11.5 Cleaning

- A. Do not wipe the touch panel with dry cloth, as it may cause scratch.
- B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

### 11.6 Cautions for installing and assembling

- A. Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.
- B. In order to make the display assembly stable and firm, MT recommends to design some supporting at the display backside, especially for the display with tape-attached touch panel, such supporting is important and essential, or else, the display may drop-off from front after some period of time.
- C. Do not display the fixed pattern for a long time because it may develop image sticking due to the LCD structure. If the screen is displayed with fixed pattern, use a screen saver.



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