



The OX-174 is a low phase noise, high-stability ovenized crystal oscillator in a 28 x 38 mm package. The oscillator has a noise floor of -175 dBc/Hz, and typical ADEV below 2E-12 for t=0.1 to 100 s. The OX-174 is a member of the OX-17 oscillator series. Other oscillators in the series include the OX-170 standard oscillator, OX-171 high stability oscillator, OX-172 optimized for 1588 solutions, and the OX-175 low phase noise, high frequency oscillator. The Vectron design team will also help develop custom solutions where performance optimization is required for specific applications. Please contact the factory for customization options.

#### **Features**

- Reflow Process Compatible
- Temperature Stability to 5 ppb
- Frequency Range 5 to 20 MHz
- Standard Frequency:10 MHz
- CMOS and Sinewave Options Available

### **Applications**

- Military Radar
- · Instrumentation and Test Equipment
- Synthesizers
- Military Communication Equipment
- DRO references
- Satellite Communications

## **Performance Specifications**

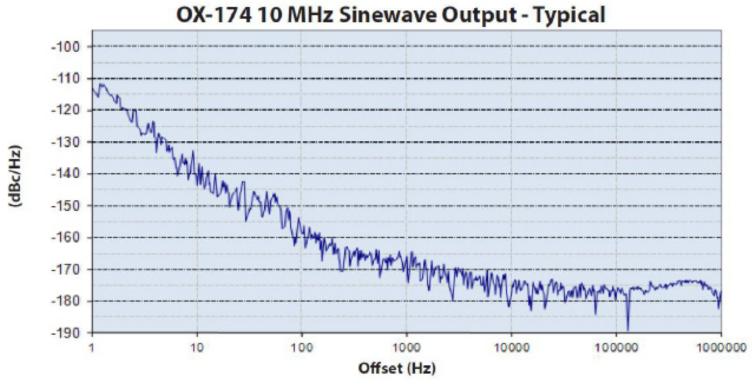
Phase Noise Ordering Codes at 10 MHz , Sinewave Output							
Frequency Offset (Hz)	А	В	C (12V only	) D (5V on	nly) Unit	Condition	
1	-95	-100	-105	-105	dBc/Hz	Maximum values	
10	-125	-130	-135	-135	dBc/Hz	All EFC settings	
100	-150	-155	-157	-157	dBc/Hz		
1000	-160	-165	-167	-167	dBc/Hz		
10,000	-170	-170	-175	-172	dBc/Hz		
100,000	-170	-170	-175	-173	dBc/Hz		
Phase Noise Ordering Codes at 10 MHz, CMOS Output							
Frequency Offset (Hz)	А	В	С	Unit	Condition		
1	-95	-100	-105	dBc/Hz	Maximum values All EFC settings (5V only)		
10	-125	-130	-135	dBc/Hz			
100	-150	-155	-157	dBc/Hz			
1000	-160	-160	-160	dBc/Hz			
10,000	-160	-165	-165	dBc/Hz			
100,000	-160	-165	-165	dBc/Hz			

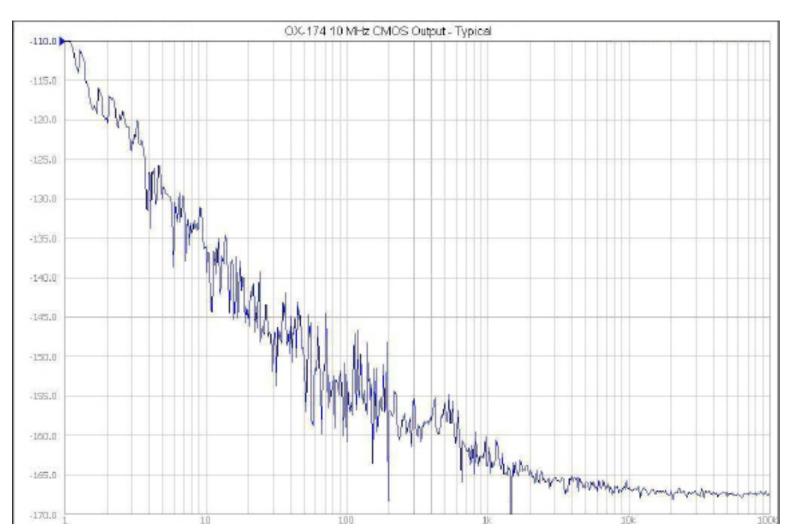
# **Performance Specifications**

Parameter	Min	Typical	Max	Units	Condition	
Allan Deviation		2 1 1.5 1.8 2	5 3 4 5 5	E-12 E-12 E-12 E-12 E-12	0.1s tau 1 s tau 10 s tau 100 s tau 1000 s tau	@ 10MHz
For oscillators with TDEV and MTIE requirements please review the OX-172 datasheet.						

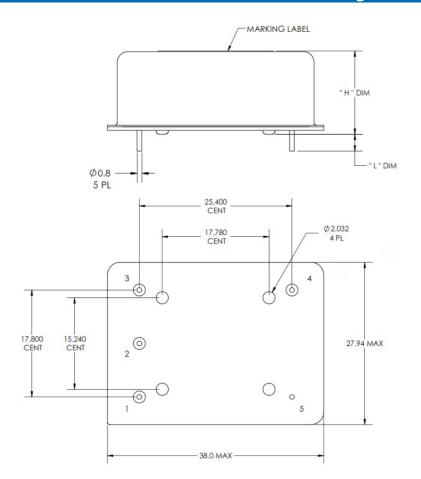
Frequency Stabilities¹ (Stabilities listed for 10 MHz. For stabilities above 10 MHz values may degrade. Please contact factory)					
Parameter	Min	Typical	Max	Units	Condition
vs. Operating Temperature Range (referenced to +25°C)	-5 -10		+5 -10	ppb ppb	-20 to +70°C -40 to +85°C
	For better stability please review the OX-171 datasheet.				ew the OX-171 datasheet.
Initial Tolerance vs. Supply Voltage Change vs. Load Change vs. Aging / Day vs. Aging / 1st Year vs. Aging / Year (following years)	-50 -2 -2 -0.5 -100 -30		+50 +2 +2 +0.5 +100 +30	ppb ppb ppb ppb ppb ppb	at time of shipment, nominal EFC  V <sub>s</sub> ±5%  Load ±5%  after 7 days operation  after 7days operation
Retrace <sup>2</sup>	-10		+10	ppb	
Warm-up Time			5	minutes	to ±10ppb of final frequency (1 hour reading) @ +25°C
		Sup	oply Voltag	e (Vs)	
Parameter	Min	Typical	Max	Units	Condition
C 1 1/1 (1/1)	4.75	5.0	5.25	VDC	ordering option D
Supply Voltage (Vs)	11.4	12.0	12.6	VDC	ordering option B
Down			3.5	Watts	during warm-up, all temperatures
			1.8	Watts	steady state @ +25°C
Power Consumption		3.3		Watts	steady state @ -40°C
		0.5		Watts	steady state @ +85°C
			RF Outpu	t	
Start Time		1		S	time required to achieve 90% of amplitude
Signal		HCI	MOS		5 Volt only
Load		15		pF	
Signal Level (Vol)			0.5		
Signal Level (Voh)	3.5				
Duty Cycle	45		55	%	@ (Voh-Vol)/2
Rise and Fall Time (Tr/Tf)			10	ns	
Signal		Sine	Wave		
Load		50		Ω	
Output Power	+7	+8	+10	dBm	Vs=12VDC
·	+4	+5	+7	dBm	Vs=5VDC
Harmonics			-30	dBc	
Spurious			-80	dBc	

Frequency Tuning (EFC)						
Parameter	Min	Typical	Max	Units	Condition	
Tuning range	±400		±800	ppb	(fixed frequency option available)	
Linearity		15		%		
Tuning Slope		Pos	itive			
Input Impedance		100		kOhm		
Bandwidth Modulation	150			Hz		
C I IV II D	0.0		4.35	VDC	with Vs=5.0 VDC	
Control Voltage Range	0.0		10	VDC	with Vs=12.0 VDC	
		Referenc	e Voltage Ou	tput (Vref)		
	4.26	4.35	4.44	VDC	with Vs = 5.0 VDC	
Reference Voltage (Vref)	9.8	10	10.2	VDC	with Vs =12 VDC	
The OX-174 series can be configur	red without a v	oltage refere	nce. Please c	ontact the fact	ory for ordering information.	
		Addi	tional Paraı	neters		
Parameter	Min	Typical	Max	Units	Condition	
g-sensitivity		- 1		1.5	ppb/g	
g-sensitivity of 0.5 ppb/g available	e in this packag	e size. Pleas	e contact fac	tory for orderin		
For g-sensitivity <0.5 ppb/g pleas						
Weight			25	g		
		Absolu	te Maximur	n Ratings		
			6.5	VDC	with Vs= 5.0 VDC	
Supply Voltage (Vs)			15.0	VDC	with Vs= 12 VDC	
Output Load	25		open	Ω	Sinewave	
Output Load			50	pF	CMOS	
Operable Temperature Range	-55		+95	°C	Operable temperature range implies the device will continue to operate with no long-term damage to unit; however, it will not be specification compliant outside the operating temperature range.	
	En	vironmenta	l and Produ	uct Classificati	on	
Shock (Endurance)	MIL-STD-202,	Method 213	, Condition J,	.30g 11 ms		
Sine Vibration (Endurance)	MIL-STD-202, Method 201 and 204, Condition A, except 5g to 500 Hz, 1 sweep each axis					
Random Vibration (Endurance)	MIL-STD-202, Method 214, Condition I-D					
Humidity	MIL-STD-202, Method 103, Condition B, 100% rh					
Seal	MIL-STD-202, Method 112, Condition D, hermetic, washable					
Altitude	MIL-STD-202, Method 105, sea level to space					
Resistance to Soldering Heat	MIL-STD-202, Method 210, Condition A,B,C					
Terminal Strength	MIL-STD-202, Method 211, Condition C (5 bends at 45°, 2 lbs)					
Moisture Sensitive Level	1					
RoHS	6 (fully compliant)					
Storage Temperature Range	-55		+125	°C		
				=	<u> </u>	





# **Outline Drawing / Enclosure**



Through hole Package configuration A					
	Height "H"	Pin Length "L"			
0	18.2 max	4.5 mm min			

Pin Connections					
1	Electronic Frequency Control Input (EFC) No Connect for Fixed frequency Oscillators				
2	Reference Voltage (Vref)				
3	Supply Voltage Input (Vs)				
4	RF Output				
5	Ground (Case)				

Dimensions in mm

Surface mount
Package configuration B

Pin Length "L"

n/a

			Height
"H"		1	20.3 ma
<u></u>			
SEATING PLANE			
37.0±0.2			
10 11 12 13			
1.7			Pin Co
	4,5,6,7		
2.5 -	11,12,1		
2.5 -   -   -   -     -	<b>1</b>	l El	lectronic I

3

5

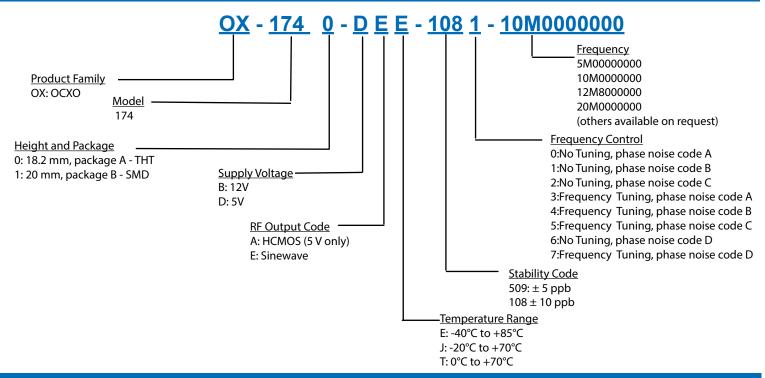
17.8±0.1

-27.9±0.1

8

Pin Connections					
4,5,6,7, 11,12,13	No Connect				
1	Electronic Frequency Control Input (EFC) No Connect for Fixed frequency Oscillators				
2	Reference Voltage (Vref)				
3	Supply Voltage Input (Vs)				
8	RF Output				
9,10	Ground (Case)				

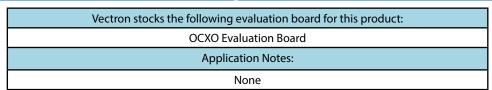
### Ordering Information<sup>3</sup>



### **Additional Ordering Options**

Additional ordering options available include custom aging rates, custom temperature ranges, custom temperature stabilities, custom phase noise requirements, improved g-sensitivity, and oscillators with no voltage reference output on pin 2. These modifications require a custom dash number - please contact the factory for additional information.

### **Design Tools**



#### Notes:

- 1. Unless otherwise stated, all values are valid after warm-up time and refer to typical conditions for supply voltage, frequency control voltage, load, and temperature (25°C).
- 2. Retrace is defined as the frequency difference between the end of two 24 hour on power periods with a 24 hour off period in between while at a constant temperature.
- 3. Not all options and codes available at all frequencies.

#### **Contact Information**

#### USA:

100 Watts Street Mt Holly Springs, PA 17065 Tel: 1.717.486.3411 Fax: 1.717.486.5920 Europe:

Landstrasse 74924 Neckarbischofsheim Germany Tel: +49 (0) 7268.801.0

Fax: +49 (0) 7268.801.281



Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your reasonability to ensure that your application meets with your specifications. MICROCHIP MAKES NO REPRESENTATION OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION INCLUDING, BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE. Microchip disclaims all liability arising from this information and its use. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly, or otherwise, under any Microchip intellectual property rights unless otherwise stated.

#### Trademarks

The Microchip and Vectron names and logos are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

Page 6 of 6 Rev: 5-6-2019 jar

## **Mouser Electronics**

**Authorized Distributor** 

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

## Microchip:

OX-1740-BEJ-5092-10M0000000 OX-1740-BEJ-5095-10M0000000