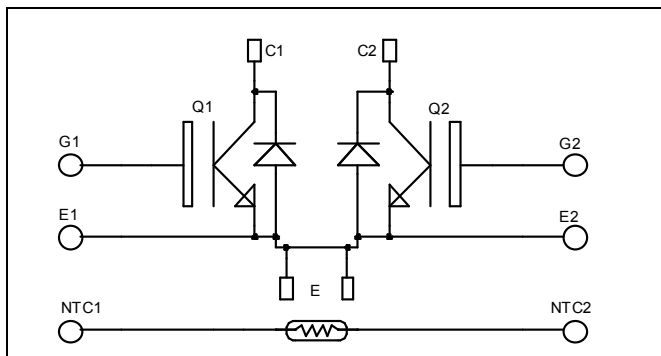


***Dual common source  
Fast Trench + Field Stop IGBT3  
Power Module***

**$V_{CES} = 1200V$   
 $I_C = 100A @ T_c = 80^\circ C$**

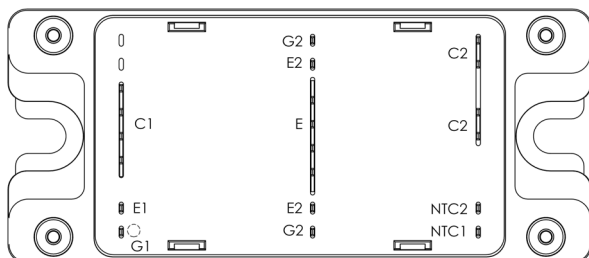


## Application

- AC Switches
- Switched Mode Power Supplies
- Uninterruptible Power Supplies

## Features

- Fast Trench + Field Stop IGBT3 Technology
  - Low voltage drop
  - Low tail current
  - Switching frequency up to 20 kHz
  - Soft recovery parallel diodes
  - Low diode VF
  - Low leakage current
  - RBSOA and SCSOA rated
- Kelvin emitter for easy drive
- Very low stray inductance
  - Symmetrical design
  - Lead frames for power connections
- High level of integration
- Internal thermistor for temperature monitoring



## Benefits

- Stable temperature behavior
- Very rugged
- Solderable terminals for easy PCB mounting
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Easy paralleling due to positive TC of  $V_{CEsat}$
- Low profile
- RoHS Compliant

## Absolute maximum ratings

Symbol	Parameter	Max ratings	Unit
$V_{CES}$	Collector - Emitter Breakdown Voltage	1200	V
$I_C$	Continuous Collector Current	$T_C = 25^\circ C$	A
		$T_C = 80^\circ C$	
$I_{CM}$	Pulsed Collector Current	$T_C = 25^\circ C$	200
$V_{GE}$	Gate - Emitter Voltage	$\pm 20$	V
$P_D$	Maximum Power Dissipation	$T_C = 25^\circ C$	480
RBSOA	Reverse Bias Safe Operating Area	$T_j = 125^\circ C$	200A @ 1100V



**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

**All ratings @  $T_j = 25^\circ\text{C}$  unless otherwise specified**

**Electrical Characteristics**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$I_{CES}$	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} = 1200V$			250	$\mu A$
$V_{CE(sat)}$	Collector Emitter Saturation Voltage	$V_{GE} = 15V$ $I_C = 100A$	$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	1.4 2.0	2.1	V
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 2\text{ mA}$	5.0	5.8	6.5	V
$I_{GES}$	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE} = 0V$			400	nA

**Dynamic Characteristics**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$C_{ies}$	Input Capacitance	$V_{GE} = 0V$		7200		pF
$C_{oes}$	Output Capacitance	$V_{CE} = 25V$		400		
$C_{res}$	Reverse Transfer Capacitance	$f = 1\text{ MHz}$		300		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching ( $25^\circ\text{C}$ ) $V_{GE} = \pm 15V$ $V_{Bus} = 600V$ $I_C = 100A$ $R_G = 3.9\Omega$		260		ns
$T_r$	Rise Time			30		
$T_{d(off)}$	Turn-off Delay Time			420		
$T_f$	Fall Time			70		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching ( $125^\circ\text{C}$ ) $V_{GE} = \pm 15V$ $V_{Bus} = 600V$ $I_C = 100A$ $R_G = 3.9\Omega$		290		ns
$T_r$	Rise Time			50		
$T_{d(off)}$	Turn-off Delay Time			520		
$T_f$	Fall Time			90		
$E_{on}$	Turn on Energy	$V_{GE} = \pm 15V$ $V_{Bus} = 600V$ $I_C = 100A$	$T_j = 125^\circ\text{C}$	10		mJ
$E_{off}$	Turn off Energy	$R_G = 3.9\Omega$	$T_j = 125^\circ\text{C}$	10		

**Reverse diode ratings and characteristics**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$V_{RRM}$	Maximum Peak Repetitive Reverse Voltage		1200			V
$I_{RM}$	Maximum Reverse Leakage Current	$V_R = 1200V$	$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$		250 500	$\mu A$
$I_F$	DC Forward Current		$T_c = 80^\circ\text{C}$	100		A
$V_F$	Diode Forward Voltage	$I_F = 100A$ $V_{GE} = 0V$	$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	1.6 1.6	2.1	V
$t_{rr}$	Reverse Recovery Time	$I_F = 100A$ $V_R = 600V$ $di/dt = 2000A/\mu s$	$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	170 280		ns
$Q_{rr}$	Reverse Recovery Charge		$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	9 18		
$E_r$	Reverse Recovery Energy		$T_j = 25^\circ\text{C}$ $T_j = 125^\circ\text{C}$	5 9		mJ



<i>Symbol</i>	<i>Characteristic</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
B <sub>25/85</sub>	T <sub>25</sub> = 298.15 K		3952		K

$$R_T = \frac{R_{25}}{\exp \left[ B_{25/85} \left( \frac{1}{T_{25}} - \frac{1}{T} \right) \right]}$$

<i>Symbol</i>	<i>Characteristic</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>	
R <sub>thJC</sub>	Junction to Case Thermal Resistance	IGBT			0.26	°C/W	
		Diode			0.48		
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz		4000			V	
T <sub>J</sub>	Operating junction temperature range		-40		150	°C	
T <sub>STG</sub>	Storage Temperature Range		-40		125		
T <sub>C</sub>	Operating Case Temperature		-40		100		
Torque	Mounting torque	To Heatsink	M5	2.5		4.7	N.m
Wt	Package Weight					160	g

Technical drawing of a mechanical part, showing three views: top, front, and side.

**Top View Dimensions:**

- Overall width:  $93 \pm 0,50$
- Overall height:  $80 \pm 0,20$
- Distance from left edge to first hole center:  $11,6^*$
- Distance between first and second hole centers:  $30,50^*$
- Distance between second and third hole centers:  $26,40^*$
- Distance from third hole center to right edge:  $11,6^*$
- Hole diameters:  $\varnothing 6,1 \pm 0,2$  and  $\varnothing 2,50 \pm 0,10$
- Other dimensions:  $15,24^*$ ,  $11,43^*$ ,  $5,72^*$ ,  $3,81^*$ ,  $1,91^*$ ,  $7,62^*$ ,  $0$

**Front View Dimensions:**

- Overall width:  $78,50 \pm 0,25$
- Overall height:  $17,20 \pm 0,30$
- Distance from left edge to first hole center:  $0,50 \pm 0,05$
- Distance from right edge to first hole center:  $1 \pm 0,10$
- Distance from right edge to second hole center:  $7 \pm 0,20$

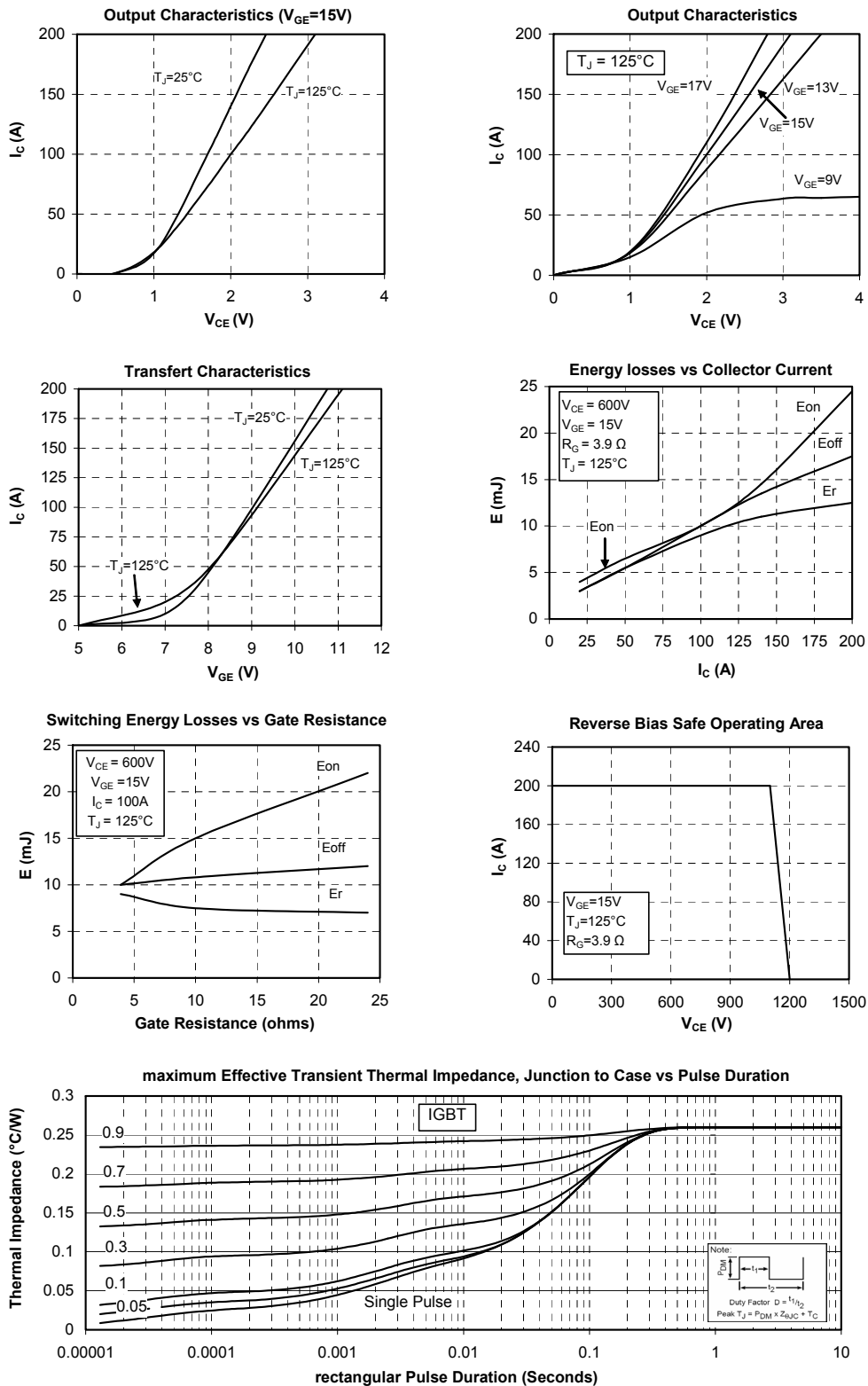
**Side View Dimensions:**

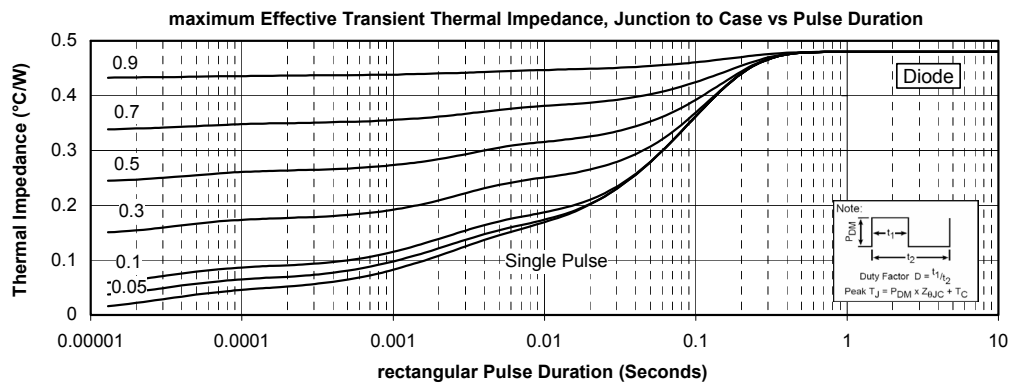
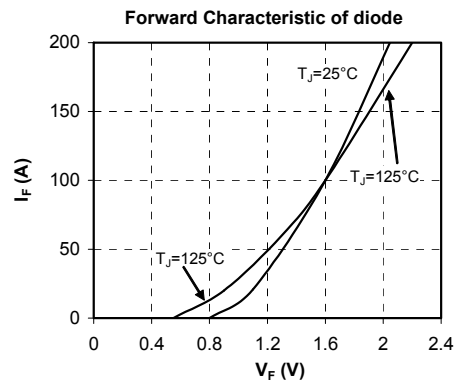
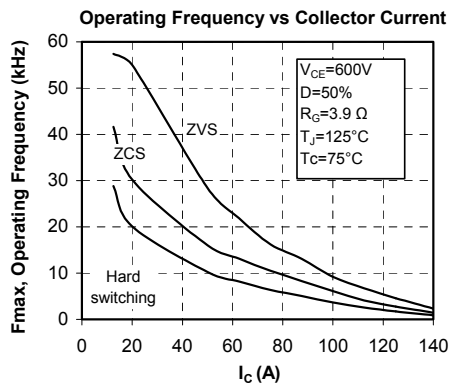
- Overall width:  $40,40 \pm 0,50$
- Overall height:  $21,2 \pm 1$
- Distance from left edge to first hole center:  $3 \pm 0,20$
- Distance from left edge to second hole center:  $5 \pm 0,50$
- Distance from right edge to first hole center:  $11 \pm 0,5$
- Distance from right edge to second hole center:  $5,50 - 0,05$
- Distance from right edge to third hole center:  $+0,20$

ALL DIMENSIONS MARKED "H" ARE TOLERANCED AS :  $\begin{array}{|c|c|} \hline \oplus \\ \hline \end{array} \begin{array}{|c|c|} \hline \phi \\ \hline \end{array} 1$

See application note APT0501 - Mounting Instructions for SP4 Power Modules on [www.microsemi.com](http://www.microsemi.com)

## Typical Performance Curve





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