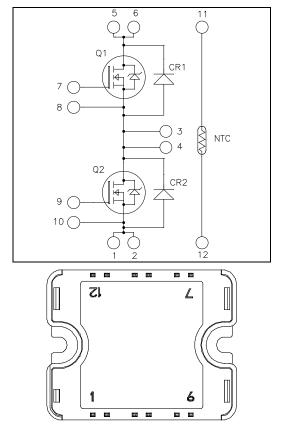


## Phase leg SiC MOSFET Power Module



Pins 1/2; 3/4; 5/6 must be shorted together

## $V_{DSS} = 1200V$ $R_{DSon} = 17m\Omega \text{ max} @ \text{Tj} = 25^{\circ}\text{C}$ $I_D = 143\text{A} @ \text{Tc} = 25^{\circ}\text{C}$

#### Application

- Welding converters
  - Switched Mode Power Supplies
  - Uninterruptible Power Supplies
  - Motor control

#### Features

#### • SiC Power MOSFET

- Low R<sub>DS(on)</sub>
- High temperature performance

#### • SiC Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Very low stray inductance
- Internal thermistor for temperature monitoring
- High level of integration
- AlN substrate for improved thermal performance

#### Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- RoHS Compliant

### All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

### 1. SiC MOSFET characteristics (Per MOSFET)

Symbol	Parameter		Max ratings	Unit
V <sub>DSS</sub>	Drain - Source Breakdown Voltage		1200	V
т	Continuous Drain Current	$T_c = 25^{\circ}C$	143	
I <sub>D</sub>	Continuous Drain Current	$T_c = 80^{\circ}C$	108	Α
I <sub>DM</sub>	Pulsed Drain current		280	
V <sub>GS</sub>	Gate - Source Voltage		-10/+25	V
R <sub>DSon</sub>	Drain - Source ON Resistance		17	mΩ
P <sub>D</sub>	Maximum Power Dissipation	$T_c = 25^{\circ}C$	600	W

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com APTMC120AM20CT1AG - Rev 1 June, 2013

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**Electrical Characteristics** 

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0V$ , $V_{DS} = 1200V$			20	200	μΑ
D	Durin Country on Desistance	$V_{GS} = 20V$ $T_i = 25^{\circ}C$		12.5	17		
R <sub>DS(on)</sub>	Drain – Source on Resistance	$I_{\rm D} = 100 {\rm A}$	$T_{j} = 150^{\circ}C$		22	32	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 2mA$		1.9	2.3		V
I <sub>GSS</sub>	Gate – Source Leakage Current	$V_{GS} = 20 V, V_{DS} = 0V$				1	μA

## **Dynamic Characteristics**

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
C <sub>iss</sub>	Input Capacitance	$V_{GS} = 0V$			5960		
C <sub>oss</sub>	Output Capacitance	$V_{\rm DS} = 1000 V$			440		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1 MHz			46		
Qg	Total gate Charge	$V_{GS} = -2/+20V$			360		
Q <sub>gs</sub>	Gate – Source Charge	$V_{Bus} = 800V$			64		nC
$Q_{gd}$	Gate – Drain Charge	$I_D = 100A$			126		
T <sub>d(on)</sub>	Turn-on Delay Time	$V_{GS} = -2/+20V$ $V_{Bus} = 800V$			21		
$T_{\rm r}$	Rise Time				19		
T <sub>d(off)</sub>	Turn-off Delay Time	$I_{\rm D} = 100 {\rm A}$			50		ns
$T_{\rm f}$	Fall Time	$R_L = 8\Omega$ ; $R_G = 10\Omega$			30		
Eon	Turn on Energy	Inductive Switching $V_{GS} = -5/+20V$ $V_{Bus} = 600V$	$T_j = 150^{\circ}C$		2.2		mJ
E <sub>off</sub>	Turn off Energy	$I_{\rm D} = 100 \text{A}$ $R_{\rm G} = 10 \Omega$	$T_j = 150^{\circ}C$		1.2		1113
R <sub>thJC</sub>	Junction to Case Thermal Resistance	2				0.21	°C/W

## 2. SiC diode characteristics (Per SiC diode)

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit		
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Voltage			1200			V	
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> =1200V	$T_j = 25^{\circ}C$		70	400	μA	
IRM	Waxiniani Keverse Deakage Current	• R-1200 • T	$V_{\rm R} = 1200 V$ $T_{\rm j} = 175^{\circ} {\rm C}$	$T_{j} = 175^{\circ}C$		130	800	μΑ
I <sub>F</sub>	DC Forward Current		Tc = 125°C		40		Α	
V <sub>F</sub>	Diode Forward Voltage	$I_{\rm F} = 40A$ $T_{\rm i} = 25$	$T_i = 25^{\circ}C$		1.5	1.8	V	
• F	Diode Forward Voltage	$I_{\rm F} = 40 A$	$T_{i} = 175^{\circ}C$		2.2	3		
Q <sub>C</sub>	Total Capacitive Charge	$I_F = 40A, V_R = 1200V$ di/dt = 1000A/µs			260		nC	
С	$f = 1 MHz, V_R = 200 V$		= 200V		186		pF	
U	Total Capacitance	$f = 1 MHz, V_R = 400 V$			134		μг	
R <sub>thJC</sub>	Junction to Case Thermal Resistance				0.7	°C/W		



## 3. Thermal and package characteristics

## **Package characteristics**

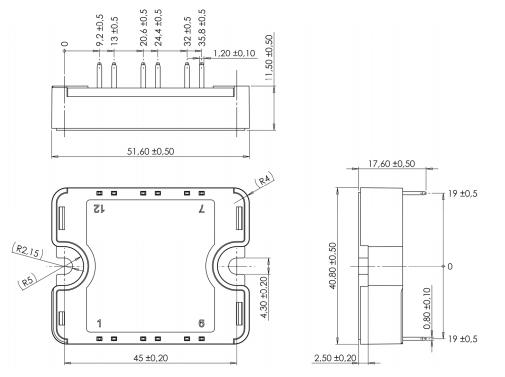
Symbol	Characteristic			Min	Тур	Max	Unit	
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V	
TJ	Operating junction temperature range		SiC MO	SFET	-40		150	
Тj	Operating Junction temperature range	SiC diode		-40		175		
T <sub>JOP</sub>	Recommended junction temperature under switching conditions			-40		T <sub>J</sub> max	°C	
T <sub>STG</sub>	Storage Temperature Range				-40		-25 125	
T <sub>C</sub>	Operating Case Temperature				-40		125	
Torque	Mounting torque	To he	atsink	M4	2		3	N.m
Wt	Package Weight				80	g		

### Temperature sensor NTC (see application note APT0406 on www.microsemi.com).

Symbol	Characteristic			Тур	Max	Unit
R <sub>25</sub>	Resistance @ 25°C	C		50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B <sub>25/85</sub>	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta B/B$		T <sub>C</sub> =100°C		4		%

$$R_{T} = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$
 T: Thermistor temperature  
R<sub>T</sub>: Thermistor value at T

### SP1 Package outline (dimensions in mm)



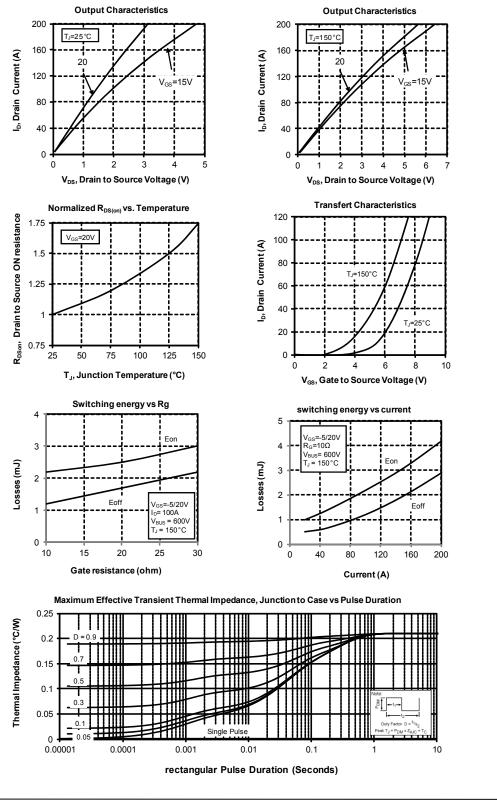
See application note 1904 - Mounting Instructions for SP1 Power Modules on www.microsemi.com

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3 - 6



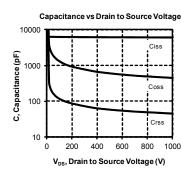
### 4. Typical Performance Curves SiC MOSFET

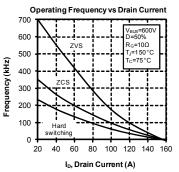


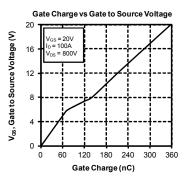
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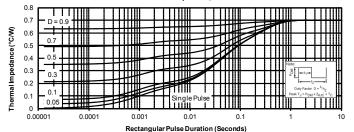


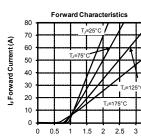




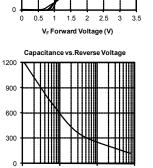
#### SiC diode

Maximum Effective Transient Thermal Impedance, Junction to Case vs Pulse Duration





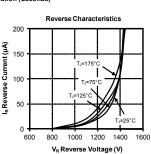
C, Capacitance (pF)



100

V<sub>R</sub> Reverse Voltage

10





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1000



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