NOTES

Allow clearance for finger squeeze access from the two sides shown.

No tracks or vias in the shaded keepout area.

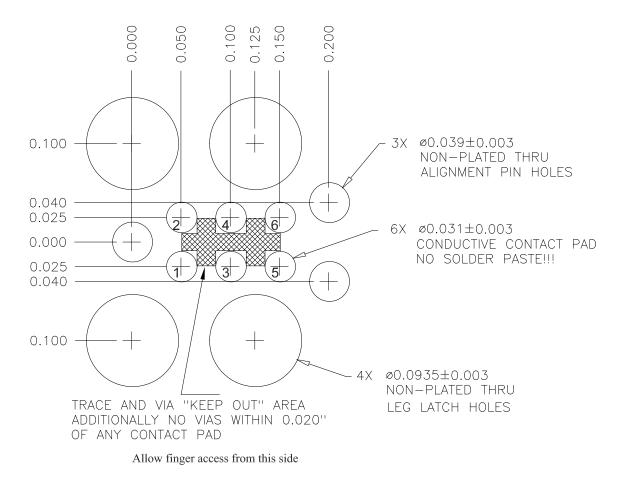
No other track or signal within 0.020" of any contact pad.

Do not allow solder paste on the contact pads. It is essential to eliminate any hole in the solder paste-mask (solder stencil) layer. If this is not possible make the contact pads thru-hole with finished hole size of 0.008" or less.

Leg and alignment holes may be plated and used as vias but ensure minimum finished hole size meets specified tolerances.

To prevent purchasing confusion, please specify DNL in your BOM!

Allow finger access from this side



UNLESS OTHERWISE NOTED DIMENSIONS ARE IN INCHES

.XX±

.01

THIRD ANGLE PROJECTION

 \oplus

ANGLE ±

1.5°

DRAWN

CHECKE

PM

.XXX±

.005

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AUTHORIZATION FROM TAG-CONNECT, SAN BRUNO, CA

FILE:

Microchip ICD / ICSP SIGNALS
Pin 1: -MCLR/Vpp
Pin 2: Vdd
Pin 3: Ground
Pin 4: PGD (ICSPDAT)
Pin 5: PGC (ICSPCLK)
Pin 6: Unusèd or LVP

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SCALE: 10:	TITLE TC2030-MCP-FP FOOTPRINT FOR TC2030-MCP						
/ NS D	date 4/5/10 date	size A	SHEET 1	0F 1	DWG. NO.		rev.

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Microchip: TC2030-MCP