



Powermite High Efficiency 2 Amp Ultrafast Rectifier

DESCRIPTION

The Microsemi UPR20e3, UPR30e3, and UPR40e3 Powermite high efficiency rectifiers are RoHS compliant and offers optimized forward voltage characteristics with reverse blocking capabilities up to 400 Volts. They are ideal for surface mount applications that operate at high frequencies.

In addition to its size advantages, Powermite package features include a full metallic bottom that eliminates possibility of solder flux entrapment during assembly and a unique locking tab acts as an efficient heat path from die to mounting plane for external heat sinking with very low thermal resistance junction to case (bottom). Its innovative design makes this device ideal for use with automatic insertion equipment.

Important: For the latest information, visit our website http://www.microsemi.com.

FEATURES

- Low thermal resistance DO-216 package for higher current operation
- Utrafast recovery time of 50 ns
- Efficient heat path with Integral locking bottom metal tab
- Low forward voltage
- Full metallic bottom eliminates flux entrapment
- Compatible with automatic insertion equipment
- Low profile-maximum height of 1 mm
- RoHS compliant

APPLICATIONS / BENEFITS

- Switching and regulating power supplies
- Charge pump circuits
- Reduces reverse recovery loss with low I_{RM}
- Small 8.45 mm² foot print (See mounting pad details)

MAXIMUM RATINGS @ 25 °C unless otherwise specified

Parameters/Test Conditions		Symbol	Value	Unit
Junction and Storage Temperature		T_J and T_{STG}	-55 to +150	°C
Thermal Resistance Junction-to-Tab			30	°C/W
Thermal Resistance Junction-to-Bottom			10	°C/W
Non-Repetitive Peak Forward Surge Current		I _{FSM}	20	Α
(At 8.3 ms Single half-sine wave)				
Working Peak Reverse Voltage	UPR20e3	V _{RWM}	200	V
	UPR30e3		300	
	UPR40e3		400	
Average Rectified Output Current (1)		Ιo	2.0	Α
(At rated V_{RWM} , $T_C = 75^{\circ}C$)				
Solder Temperature @ 10 s			260	°C

Notes: 1. When mounted on FR-4PC board using 2 oz copper with recommended minimum foot print.



DO-216 Package

MSC – Lawrence

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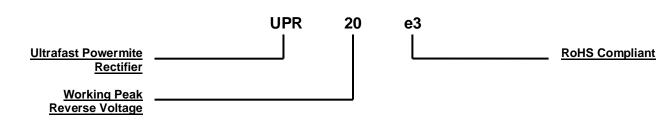
www.microsemi.com



MECHANICAL and PACKAGING

- CASE: Void-free transfer molded thermosetting epoxy compound meeting UL94V-0
- TERMINALS: Annealed matte-tin plating over copper and readily solderable per MIL-STD-750, method 2026. (Consult factor for tin-lead plating.)
- MARKING: R20• for UPR20e3, R30• for UPR30e3, and R40• for UPR40e3 (dot indicates "e3" designation)
- POLARITY: Cathode designated by TAB 1
- TAPE & REEL option: 12 mm tape per standard EIA-481-B. Consult factory for quantities.
- WEIGHT: Approximately 0.016 gram
- See Package Dimensions on last page.

PART NOMENCLATURE



	SYMBOLS & DEFINITIONS		
Symbol	Definition		
CT	Total Capacitance: The total small signal capacitance between the diode terminals of a complete device.		
f	Frequency		
IF	Forward Current: The dc current flowing from the external circuit into the anode terminal		
I _{FSM}	Surge Peak Forward Current: The forward current including all nonrepetitive transient currents but excluding all repetitive transients (ref JESD282-B)		
Ι _ο	Average Rectified Output Current: The Output Current averaged over a full cycle with a 50 Hz or 60 Hz sine-wave input and a 180 degree conduction angle.		
I _R	Reverse Current: The dc current flowing from the external circuit into the cathode terminal at the specified voltage V_R		
I _{REC}	Recovery Current:		
V _R	Reverse Voltage: A positive dc cathode-anode voltage below the breakdown region		
V _{RWM}	Working Peak Reverse Voltage: The peak voltage excluding all transient voltages (ref JESD282-B). Also sometimes known historically as PIV.		

ELECTRICAL CHARACTERISTICS @ $T_A = +25 \ ^{\circ}C$ unless otherwise noted

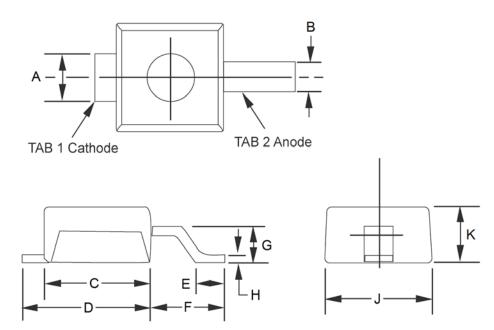
Parameter	Symbol	Conditions	Min	Max	Units
Forward Voltage (Note 1)	VF	I _F = 1.0 Amps		1.25	V
Forward Voltage (Note 1)	V _F	I _F = 1.0 Amps, T _J = 100 °C		1.15	V
Reverse Current	I _R	$V_R = V_{RWM}, T_J = 25 \ ^{\circ}C$		10	μA
Reverse Current	I _R	$V_R = V_{RWM}, T_J = 100 \ ^{\circ}C$		100	μA
Reverse Recovery Time	t _{rr}	$I_F = 0.5 \text{ A}; I_R = 1.0 \text{ A}: I_{REC} = 0.25 \text{ A}$		50	ns

Note 1: Short duration test pulse used to minimize self – heating effect.



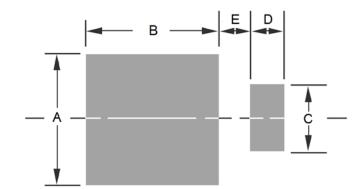


PACKAGE DIMENSIONS



	Dimensions			
Ltr	In	ch	Millimeters	
	Min	Max	Min	Max
Α	0.029	0.039	0.73	0.99
В	0.016	0.026	0.40	0.66
С	0.070	0.080	1.77	2.03
D	0.087	0.097	2.21	2.46
Е	0.020	0.030	0.50	0.76
F	0.051	0.061	1.29	1.54
G	0.021	0.031	0.53	0.78
Н	0.004	0.008	0.10	0.20
J	0.070	0.080	1.77	2.03
Κ	0.035	0.045	0.89	1.14

MOUNTING PAD DIMENSIONS



	Dimensions		
Ltr	Inch	Millimeters	
Α	0.100	2.54	
В	0.105	2.67	
С	0.050	1.27	
D	0.030	0.76	
E	0.025	0.64	

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Microchip: UPR40e3/TR7