MSCSM120TAM16CTPAG
Datasheet
Triple Phase Leg SiC MOSFET Power Module

January 2020

Microsemi

a Microchip company

Contents

1 Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

1.1 Revision 1.0

Revision 1.0 was published in January 2020. It is the first publication of this document.

2 Product Overview

The MSCSM120TAM16CTPAG device is a 3 phase leg 1200 V/171 A full Silicon Carbide (SiC) power module.

Figure 1 • MSCSM120TAM16CTPAG Electrical Schematic

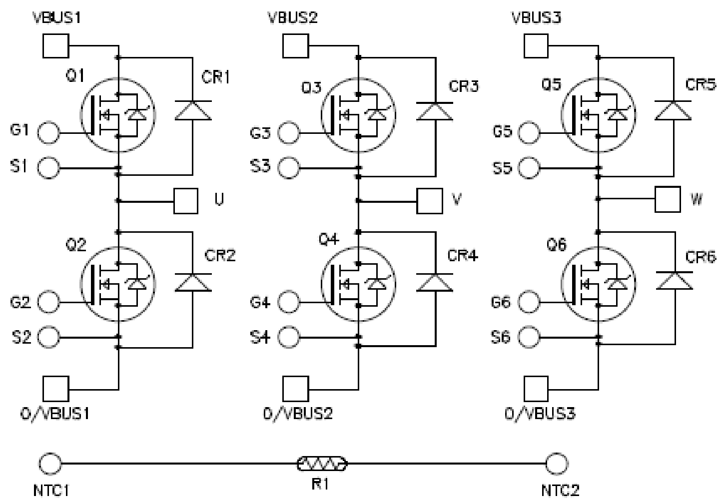
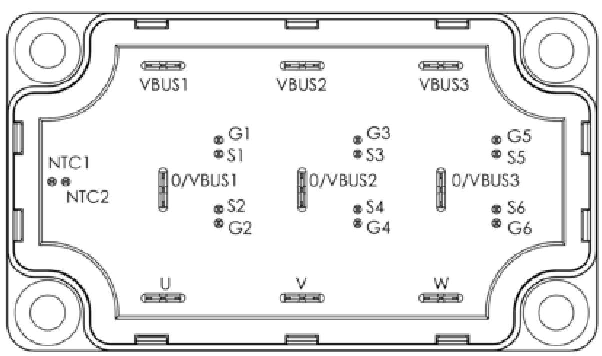


Figure 2 • MSCSM120TAM16CTPAG Pinout Location



All ratings at T_J = 25 °C, unless otherwise specified.

Caution: These devices are sensitive to electrostatic discharge. Proper handling procedures should be followed.

2.1 Features

The following are key features of the MSCSM120TAM16CTPAG device:

- SiC Power MOSFET
  - High temperature performance
  - Low $R_{DS(on)}$
- SiC Schottky Diode
  - Zero reverse recovery
  - Zero forward recovery
  - Temperature Independent switching behavior
  - Positive temperature coefficient on VF
- Very low stray inductance
- Internal thermistor for temperature monitoring
- Aluminum nitride (AlN) substrate for improved thermal performance

2.2 Benefits

The following are benefits of the MSCSM120TAM16CTPAG device:

- High power and efficient converters and inverters
- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction-to-case thermal resistance
- Solderable terminals for power and signal, for easy PCB mounting
- Low profile
- RoHS compliant

2.3 Applications

The MSCSM120TAM16CTPAG device is designed for the following applications:

- Uninterruptible power supplies
- Switched mode power supplies
- EV motor and traction drive
- Welding converters

3 Electrical Specifications

This section shows the electrical specifications of the MSCSM120TAM16CTPAG device.

3.1 SiC MOSFET Characteristics (Per MOSFET)

The following table shows the absolute maximum ratings per MOSFET of the MSCSM120TAM16CTPAG device.

Table 1 • Absolute Maximum Ratings

Symbol	Parameter		Max Ratings	Unit
V_{DSS}	Drain-source voltage		1200	V
I_D	Continuous drain current	T_C = 25 °C	171[1]	A
		T_C = 80 °C	136[1]	
I_{DM}	Pulsed drain current		350	
V_{GS}	Gate-source voltage		–10/25	V
R_{DSon}	Drain-source ON resistance		16	mΩ
P_D	Power dissipation	T_C = 25 °C	728	W

Note:

1. Specification of SiC MOSFET device, but output current must be limited due to size of power connectors.

The following table shows the electrical characteristics per MOSFET of the MSCSM120TAM16CTPAG device.

Table 2 • Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_{DSS}	Zero gate voltage drain current	V_{GS} = 0 V; V_{DS} = 1200 V			20	200	µA
$R_{DS(on)}$	Drain-source on resistance	V_{GS} = 20 V I_D = 80 A	T_J = 25 °C		12.5	16	mΩ
			T_J = 175 °C		20		
$V_{GS(th)}$	Gate threshold voltage	V_{GS} = V_{DS}, I_D = 2 mA		1.8	2.8		V
I_{GSS}	Gate-source leakage current	V_{GS} = 20 V, V_{DS} = 0 V				200	nA

The following table shows the dynamic characteristics per MOSFET of the MSCSM120TAM16CTPAG device.

Table 3 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C_{iss}	Input capacitance	V_{GS} = 0 V V_{DS} = 1000 V f = 1 MHz			6040		pF
C_{oss}	Output capacitance				540		
C_{rss}	Reverse transfer capacitance				50		
Q_g	Total gate charge	V_{GS} = –5 V/20 V V_{Bus} = 800 V I_D = 80 A			464		nC
Q_{gs}	Gate-source charge				82		
Q_{gd}	Gate-drain charge				100		
$T_{d(on)}$	Turn-on delay time	V_{GS} = –5 V/20 V V_{Bus} = 600 V I_D = 100 A R_{Gon} = 4 Ω; R_{Goff} = 2.4 Ω			30		ns
T_r	Rise time				30		
$T_{d(off)}$	Turn-off delay time				50		
T_f	Fall time				25		
E_{on}	Turn on energy	Inductive switching V_{GS} = –5 V/20 V V_{Bus} = 600 V I_D = 100 A R_{Gon} = 4 Ω R_{Goff} = 2.4 Ω	T_J = 150 °C		1.98		mJ
E_{off}	Turn off energy		T_J = 150 °C		1.3		mJ
R_{Gint}	Internal gate resistance				2.94		Ω
R_{thJC}	Junction-to-case thermal resistance					0.206	°C/W

The following table shows the body diode ratings and characteristics per MOSFET of the MSCSM120TAM16CTPAG device.

Table 4 • Body Diode Ratings and Characteristics

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
V_{SD}	Diode forward voltage	V_{GS} = 0 V; I_{SD} = 80 A		4.0		V
		V_{GS} = –5V ; I_{SD} = 80 A		4.2		
t_{rr}	Reverse recovery time	I_{SD} = 80 A; V_{GS} = –5 V V_R = 800 V; d_{iF}/dt = 2000 A/µs		90		ns
Q_{rr}	Reverse recovery charge			1100		nC
I_{rr}	Reverse recovery current			27		A

3.2 Reverse SiC Diode Ratings and Characteristics (Per SiC Diode)

The following table shows the reverse SiC diode ratings and characteristics of the MSCSM120TAM16CTPAG device.

Table 5 • Reverse SiC Diode Ratings and Characteristics (Per SiC Diode)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Peak repetitive reverse voltage					1200	V
I_{RM}	Reverse leakage current	V_R = 1200 V	T_J = 25 °C		15	400	µA
			T_J = 175 °C		250		
I_F	DC forward current		T_C = 100 °C		50		A
V_F	Diode forward voltage	I_F = 50 A	T_J= 25 °C		1.5	1.8	V
			T_J = 175 °C		2.1		
Qc	Total capacitive charge	V_R = 600 V			224		nC
C	Total capacitance	f = 1 MHz, V_R = 400 V			246		pF
		f = 1 MHz, V_R = 800 V			182		
R_{thJC}	Junction-to-case thermal resistance					0.573	°C/W

3.3 Thermal and Package Characteristics

The following table shows the package characteristics of the MSCSM120TAM16CTPAG device.

Table 6 • Package Characteristics

Symbol	Characteristic			Min	Max	Unit
V_{ISOL}	RMS isolation voltage, any terminal to case t = 1 min, 50 Hz/60 Hz			4000		V
T_J	Operating junction temperature range			–40	175	°C
T_{JOP}	Recommended junction temperature under switching conditions			–40	T_{Jmax} –25	
T_{STG}	Storage temperature range			–40	125	
T_C	Operating case temperature			–40	125	
Torque	Mounting torque	To heatsink	M6	3	5	N.m
Wt	Package weight				250	g

The following table shows the temperature sensor NTC (see application note *APT0406* on www.microsemi.com) of the MSCSM120TAM16CTPAG device.

Table 7 • Temperature Sensor NTC

Symbol	Characteristic		Min	Typ	Max	Unit
R_{25}	Resistance at 25 °C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	T_{25} = 298.15 K			3952		K
ΔB/B		T_C = 100 °C		4		%

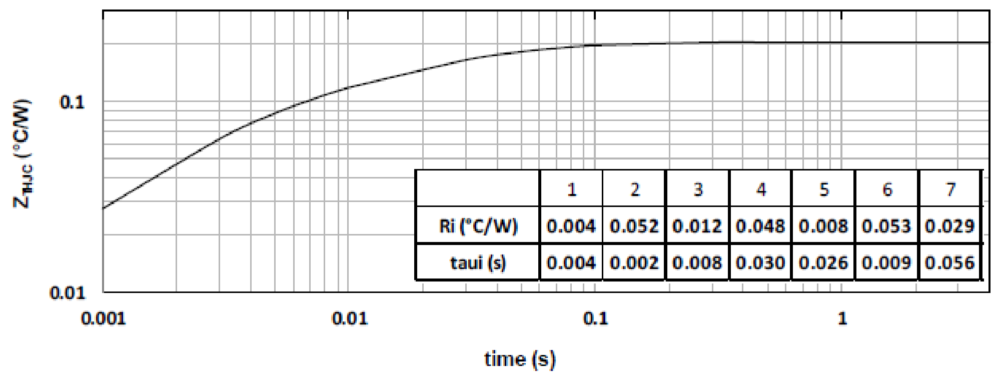
$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature
R_T: Thermistor value at T

3.4 Typical SiC MOSFET Performance Curves

This section shows the typical SiC MOSFET performance curves of the MSCSM120TAM16CTPAG device.

Figure 3 • Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.004	0.052	0.012	0.048	0.008	0.053	0.029
taui (s)	0.004	0.002	0.008	0.030	0.026	0.009	0.056

Figure 4 • Output Characteristics, T_J = 25 °C

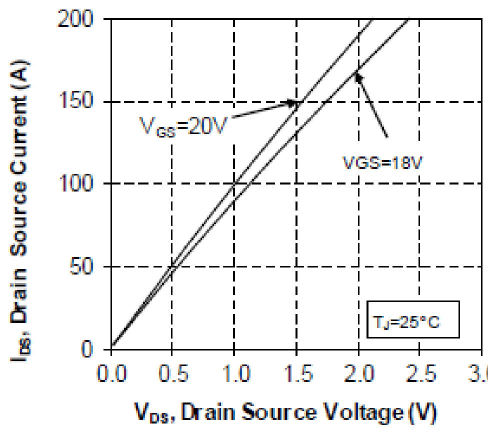


Figure 5 • Output Characteristics, T_J = 175 °C

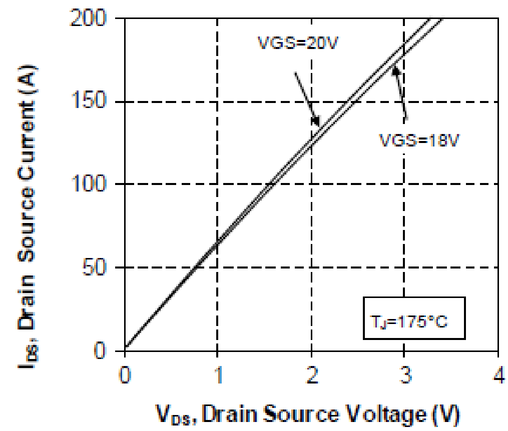


Figure 6 • Normalized $R_{DS(on)}$ vs. Temperature

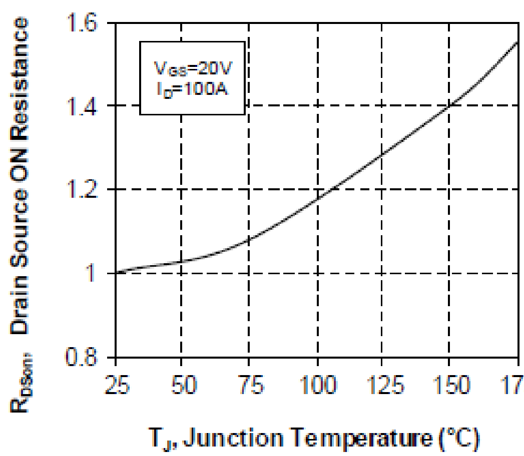


Figure 7 • Transfer Characteristics

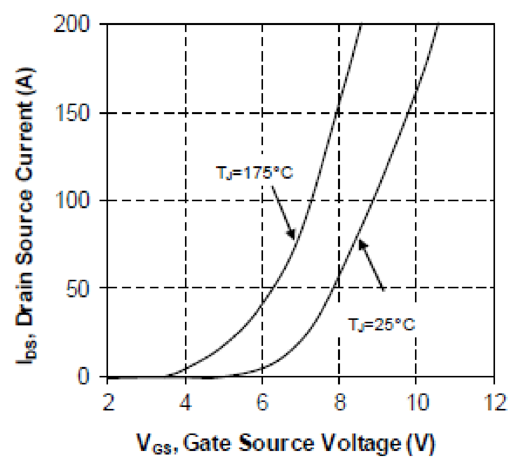


Figure 8 • Switching Energy vs. Rg

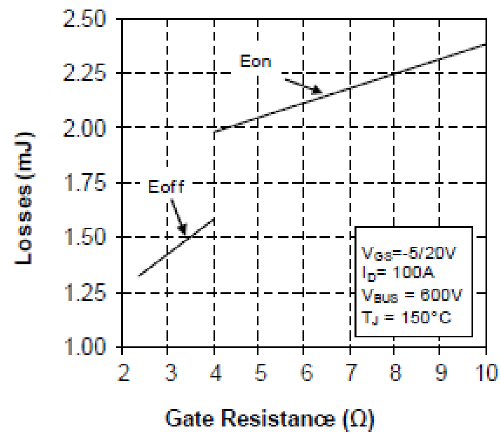


Figure 9 • Switching Energy vs. Current

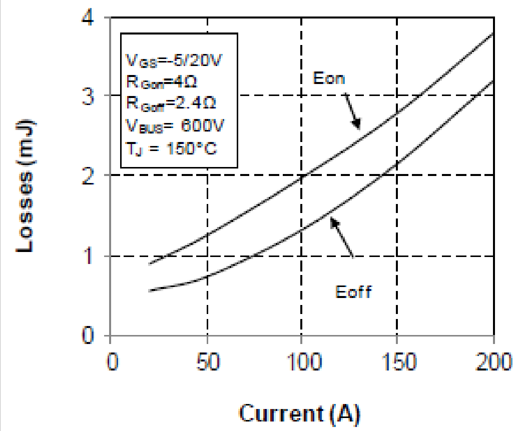


Figure 10 • Capacitance vs. Drain Source Voltage

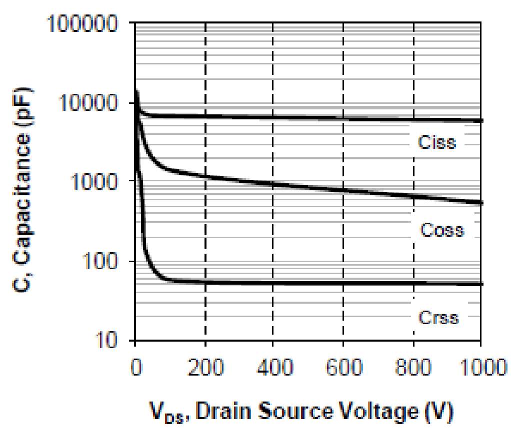


Figure 11 • Gate Charge vs. Gate Source Voltage

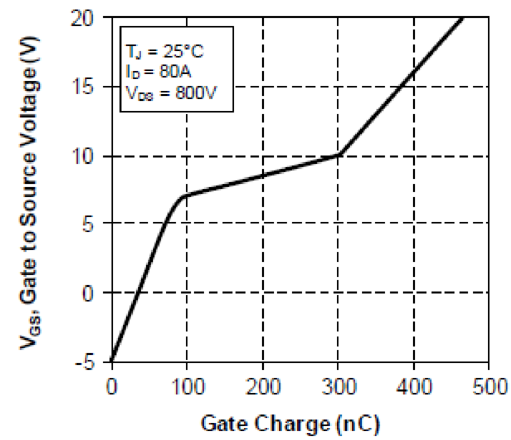


Figure 12 • Body Diode Characteristics, T_J = 25 °C

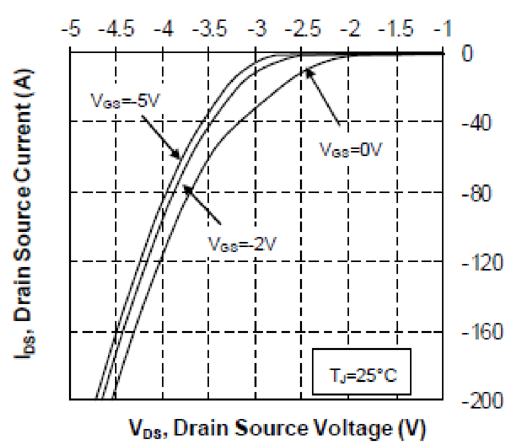


Figure 13 • 3rd Quadrant Characteristics, T_J = 25 °C

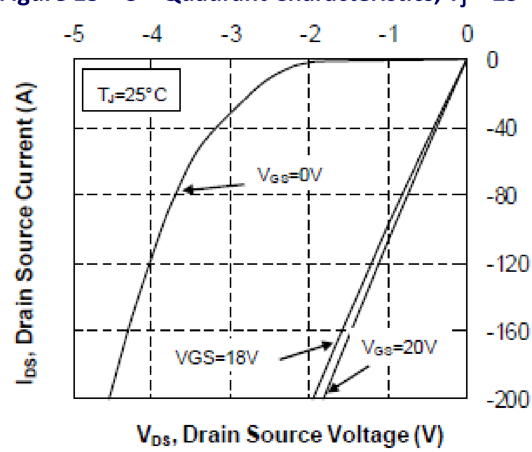


Figure 14 • Body Diode Characteristics, T_J = 175 °C

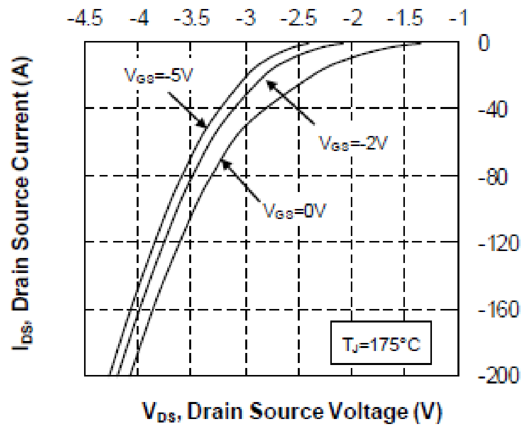


Figure 15 • 3rd Quadrant Characteristics, T_J = 175 °C

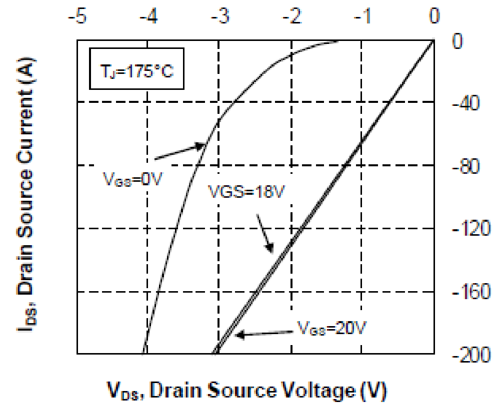
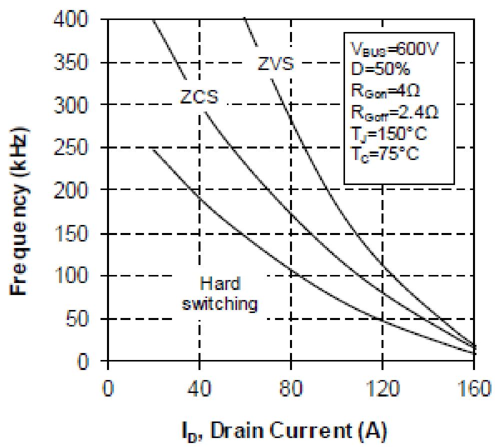


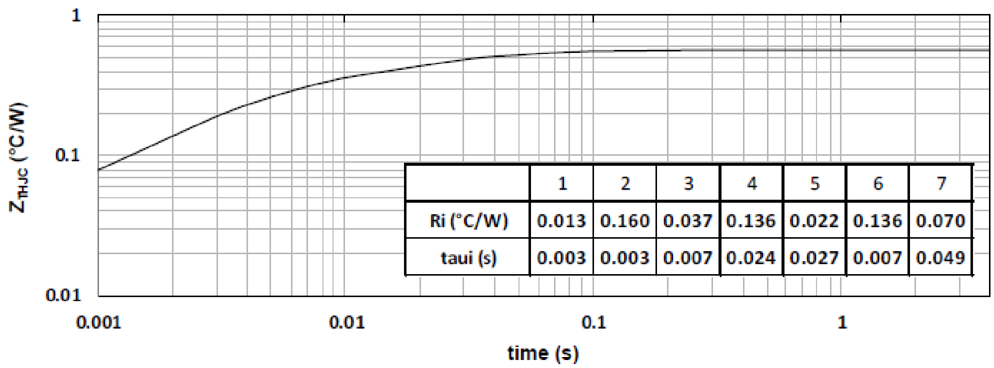
Figure 16 • Operating Frequency vs. Drain Current



3.5 Typical SiC Diode Performance Curves

This sections shows the typical SiC diode performance curves of the MSCSM120TAM16CTPAG device.

Figure 17 • Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.013	0.160	0.037	0.136	0.022	0.136	0.070
taui (s)	0.003	0.003	0.007	0.024	0.027	0.007	0.049

Figure 18 • Forward Characteristics

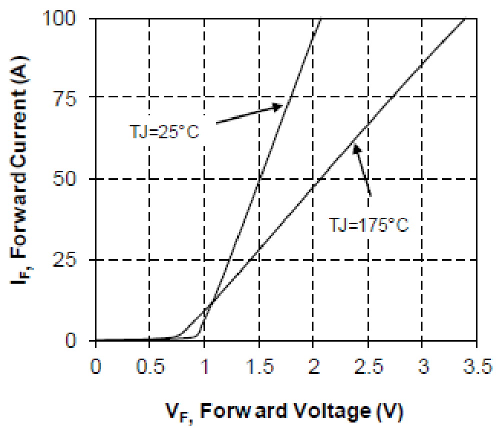
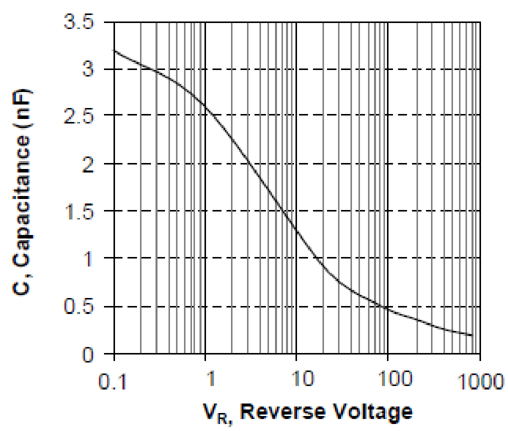


Figure 19 • Capacitance vs. Reverse Voltage



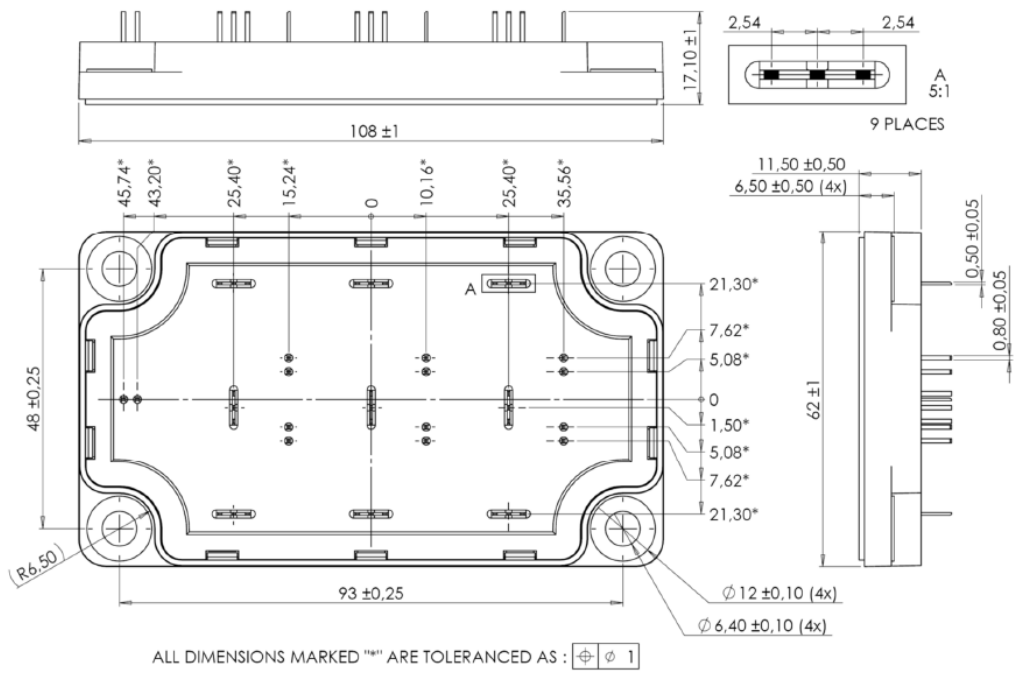
4 Package Specifications

This section shows the package specification of the MSCSM120TAM16CTPAG device.

4.1 Package Outline Drawing

The following figure illustrates the package outline of the MSCSM120TAM16CTPAG device. The dimensions in the following figure are in millimeters.

Figure 20 • Package Outline Drawing



Note: See application note *1902 - Mounting Instructions for SP6-P (12 mm) Power Modules* on www.microsemi.com

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