CSAC SA65 Chip-Scale Atomic Clock



Features

- Wide temperature range –40°C to +80°C
- Power consumption <120 mW
- Less than 17 cc volume, 1.6" × 1.39" × 0.45"
- 10 MHz CMOS-compatible output
- 1PPS output and 1PPS input for synchronization
- RS-232 interface for monitoring and control
- Short term stability (Allan Deviation) of 3.0×10^{-10} at τ =1 sec

Applications¹

- GPS receivers
- Backpack radios
- Anti-IED jamming systems
- Autonomous sensor networks
- Unmanned vehicles
- Underwater sensor systems
- Stability for various other communication and transmission applications

¹The CSAC is not tested, qualified, or rated for space applications.

With extremely low power consumption of <120 mW and a volume of <17 cc, the Microchip Chip Scale Atomic Clock (CSAC) brings the accuracy and stability of an atomic clock to portable applications.

The CSAC provides RF and 1PPS outputs at standard CMOS levels, with short-term stability (Allan Deviation) of 3.0×10^{-10} at $\tau = 1$ sec, typical long-term aging of $<9 \times 10^{-10}$ /month, and maximum frequency change of $\pm 3 \times 10^{-10}$ over the operating temperature range of -40° C to $+80^{\circ}$ C.

The CSAC accepts a 1PPS input that may be used to synchronize the unit's 1PPS output to an external reference clock with ± 100 ns accuracy. It may also use the 1PPS input to discipline its phase and frequency to within 1 ns and 1.0×10^{-12} , respectively.

Comprehensive control, monitoring, and calibration of the SA65 is accomplished via a standard CMOS-level RS-232 serial interface built in to the SA65. The interface is also used to set and read the CSAC's internal time-of-day clock.





Specifications¹

Electrical

	RF Outputs		
Frequency	10 MHz		
Format	CMOS		
Amplitude	0V to Vcc		
Load Impedance	1 MΩ		
Quantity	1		
1	PPS Output		
Rise/fall Time (10%–90%) at Load Capacitance 10 pF	<10 ns		
Pulse Width	100 µs		
Level	0V to Vcc		
Logic High (VOH) Min	2.80V		
Logic Low (VOL) Max	0.30V		
Load Impedance	1 MΩ		
Quantity	1		
1PPS Input			
Format	Rising edge		
Low Level	<0.5V		
High Level	2.5V to Vcc		
Load Impedance	1 ΜΩ		
Quantity	1		
Serial	Communications		
Protocol	RS-232		
Format	CMOS 0V to Vcc		
Tx/Rx Impedance	1 MΩ		
Baud Rate	57600		
Built-In Test E	quipment (BITE) Output		
Format	CMOS 0V to Vcc		
Load Impedance	1 ΜΩ		
Logic	0= Normal operation		
	1= Alarm ower Input		
Operating	<120 mW		
Warmup	<140 mW		
Input Voltage (Vcc)	3.2–5.1 VDC (3.3V Recommended)		

¹At input voltage Vcc = 3.3 Vpc and ambient temperature = 25 °C, unless otherwise specified.

Environmental

Specification ²	Details	
Operating Temperature	-40°C to +80°C	
Total Sensitivity of Frequency to Temperature over specified range	$\pm 3 \times 10^{-10}$	
Total Sensitivity of Frequency to Voltage over specified range	$\pm 4 \times 10^{-10}$	
Magnetic sensitivity (≤2.0 Gauss)	±9 × 10 ⁻¹¹ /Gauss	
Radiated Emissions	Compliant to FCC part 15, Class B, when mounted properly onto host PCB	
Vibration	Maintains lock under MIL- STD-810G, Operational, 7.7 grms per Figure 514.7E-1. Category 24	
Humidity	0%–95% RH per MIL-STD-810, Method 507.4	
Storage and Transpo	ort (Non-operating)	
Temperature	–55°C to +105°C	
Vibration	MIL-STD-810G, 7.7 grms per Figure 514.7E-1. Category 24	
Shock	MIL-STD-202-213A, Condition E, 1000 g	

Performance Parameters

Specification	Details		
Time to Lock	<120s		
Analog Tuning	Range: $\pm 2.2 \times 10^{-8}$ Resolution: 1 × 10 ⁻¹¹ Input: 0V-2.5 V into 100 kΩ		
Digital Tuning	Range: $\pm 1 \times 10^{-6}$ Resolution: 1 × 10 ⁻¹²		

²Maximum Rate of Change 0.5°C per Minute



Phase Noise (SSB)

Frequency	SA65			
1 Hz	<-44 dBc/Hz			
10 Hz	<-64 dBc/Hz			
100 Hz	<-110 dBc/Hz			
1 kHz	<-128 dBc/Hz			
10 kHz	<-135 dBc/Hz			
100 kHz	<-140 dBc/Hz			
Frequency Accuracy				
Maximum Offset at Shipment	$\pm 5 \times 10^{-11}$			
Maximum Retrace (48 hrs Off)	$\pm 5 \times 10^{-10}$			

Aging

1 PPS Sync

Type ²	SA65
Monthly	<9 × 10 ⁻¹⁰
Yearly	<1 × 10 ⁻⁸

±100 ns

²Typical after 30 days of continuous operation.

Short-Term Stability (Allan Deviation)

Туре	SA65		
τ = 1 s	3 × 10 ⁻¹⁰		
τ = 10 s	1.5 × 10 ⁻¹⁰		
τ = 100 s	3 × 10 ⁻¹¹		
τ = 1000 s	1.5 × 10 ⁻¹¹		

Physical

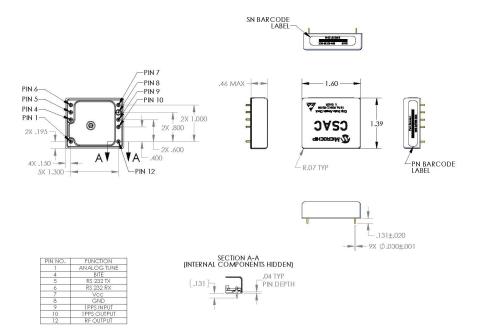
Туре	SA65		
Weight	<35 g (<1.23 oz)		
Size	1.6" × 1.39" × 0.45"		
MTBF	>100,000 hours		

Solder

Туре	Details		
Standard	Hand solder using 63/37 tin/lead solder with maximum soldering tip of 329°C (625°F)		
RoHS Compliant	Hand solder using 96.5/3/0.5 tin/ silver/ copper with maximum solder tip temperature of 370 °C (698 °F) and a dwell time of <5 s		

Ordering Information

Part Number	Description	Output Frequency	Allan Deviation	Temp Range
090-02789-002 090-02789-012	CSAC - Industrial, RoHS 3 CSAC - Industrial, Standard	10 MHz	$\leq 3 \times 10^{-10} \tau = 1$ $\leq 1.0 \times 10^{-10} \tau = 10$ $\leq 3 \times 10^{-11} \tau = 100$	-40°C to +80°C
090-02789-001 090-02789-011	CSAC - Commercial, RoHS 3 CSAC - Commercial, Standard	10 MHz	$\leq 4 \times 10^{-10} \tau = 1$ $\leq 1.5 \times 10^{-10} \tau = 10$ $\leq 4 \times 10^{-11} \tau = 100$	-10°C to +65°C



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Microchip: 090-02789-011 090-02789-012