

65 kW Transient Voltage Suppressor (TVS) Device

MRT65KP48A – MXLRT65KP75CA(e3)



Product Overview

The MRT65KP48A – MXLRT65KP75CA series of 65 kW Transient Voltage Suppressors (TVSs) provide a selection of standoff voltages (V_{WM}) from 48 to 75V. They protect a variety of voltage-sensitive components from destruction or degradation. These high-reliability devices are available in either unidirectional or bidirectional versions and are available with a variety of upscreening options for enhanced reliability. RoHS compliant versions are available. They can protect against the secondary effects of lightning per IEC61000-4-5 (see protection classes), RTCA/DO-160, and against voltage pulses from inductive switching environments or induced by RF radiation. Since their response time is virtually instantaneous at < 5 ns, they can also be used in protection from ESD and EFT per IEC61000-4-2 and IEC61000-4-4.

Features

- Available in both unidirectional and bidirectional configurations
- Suppresses transients up to 65 kW at 6.4/69 μ s
- Fast response with less than 5 ns turn-on time
- Preferred 65 kW TVS for aircraft power bus protection
- 3 σ lot norm screening performed on standby current I_D for all M prefix devices
- 100% surge tested devices
- Enhanced reliability screening in reference to MIL-PRF-19500 is available. Refer to [Hi-Rel Non-Hermetic Product Portfolio](#) for more details on the screening options. (See part nomenclature for all options.)
- High reliability controlled devices have wafer fabrication and assembly lot traceability for all M prefix devices.
- Moisture classification is level 1 with no dry pack required per IPC/JEDEC J-STD-020F for all M prefix devices.
- RoHS compliant versions are available.

Figure 1. Case 5A DO-204AR Package



Applications/Benefits

- Available in working standoff voltage (V_{WM}) range 48 to 75 volts
- Economical axial-lead plastic encapsulated TVS series for thru-hole mounting
- Protection from high power switching transients, induced RF, and lightning threats with comparatively small package size (1/4 inch diameter)
- Protection from ESD and EFT per IEC61000-4-2 and IEC61000-4-4
- Pin injection protection per RTCA/DO-160G up to level 5 for Waveform 4 (6.4/69 μ s), up to Level 3 for Waveform 5A (40/120 μ s) at 70 °C
- Compatible with “abnormal surge voltage (dc)” in 16.6.2.4 (Category A, B, and Z) of RTCA/DO-160G
- The MRT65KP48(C)A is designed for Category A in protecting 80V components¹.
- The MRT65KP54A to MRT65KP60CA is designed for Category B in protecting 90V or 100V components¹.
- The MRT65KP75(C)A is designed for Category Z in protecting 125V components¹.
- Consult factory for other voltages with similar Peak Pulse Power (P_{PP}) capabilities.

Note:

1. Including switching transistors, MOSFETS & IGBTs in offline switching power supplies

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1. Maximum Ratings

Table 1-1. Maximum Ratings at 25 °C Unless Otherwise Noted

Parameters/Test Conditions	Symbol	Value	Unit
Junction and storage temperature	T_J and T_{STG}	-55 to +150	°C
Peak pulse power dissipation ¹ at 6.4/69 μ s	P_{PP}	65	kW
Average power dissipation at $T_L = 25$ °C ²	P_D	7	W
$T_{clamping}$ (0 volts to $V_{(BR)}$ min, theoretical)	Unidirectional	< 100	ps
	Bidirectional	< 5	ns
Solder temperature at 10 seconds	—	260	°C

Notes:

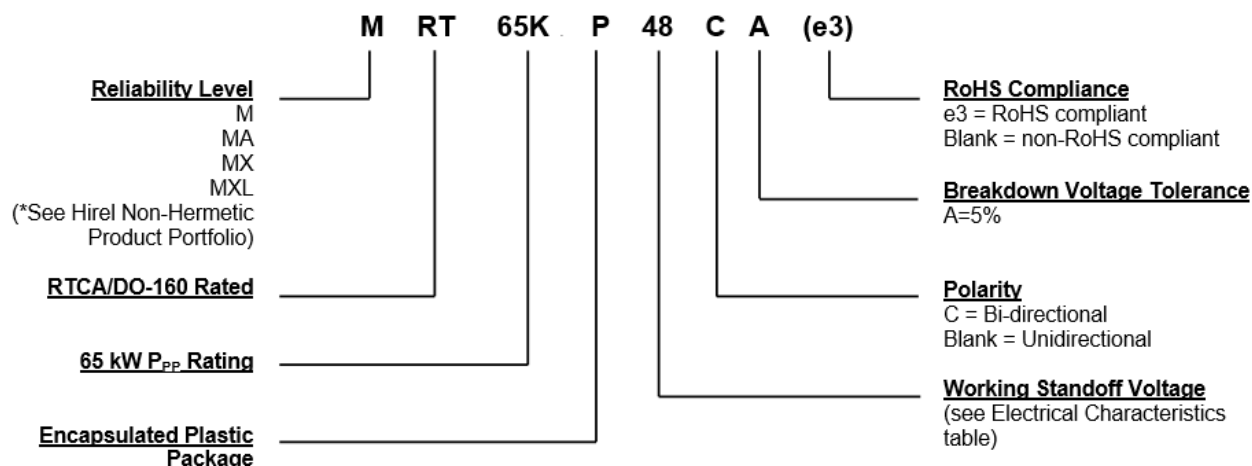
1. With impulse repetition rate (duty factor) of 0.01% or less (see [Figure 4-1](#) and [Figure 4-2](#) for t_W waveform and derating effects)
2. At 3/8 (10 mm) lead length from body

1.1 Mechanical Packaging

- Case: Void-free transfer molded thermosetting epoxy body meeting UL94V-0.
- Terminals: Tin-lead or RoHS compliant annealed matte-tin plating. Solderable per MIL-STD-750, method 2026.
- Marking: Reliability level, part number, date code
- Polarity: Cathode indicated by band. Bidirectional not marked.
- Tape and Reel option: Standard per EIA-296 (add “TR” suffix to part number). Consult factory for quantities.
- Weight: Approximately 1.6 grams
- See [Package Dimensions](#)

2. Part Nomenclature

Figure 2-1. Part Nomenclature



2.1 Symbols and Definitions

Table 2-1. Symbols and Definitions

Symbol	Definition
$\alpha_{V(BR)}$	Temperature coefficient of breakdown voltage: The change in breakdown voltage divided by the change in temperature that caused it expressed in %/°C or mV/°C.
C_T	Total capacitance: The total small signal capacitance between the diode terminals of a complete device.
$I_{(BR)}$	Breakdown current: The current used for measuring breakdown voltage $V_{(BR)}$.
I_D	Standby current: The current through the device at working standoff voltage.
I_{PP}	Peak impulse current: The peak current during an impulse.
P_{PP}	Peak pulse power: The peak power that can be applied for a specific pulse width and waveform. The product of I_{PP} and V_C .
$V_{(BR)}$	Breakdown voltage: The voltage across the device at a specified current $I_{(BR)}$ in the breakdown region.
V_C	Clamping voltage: The voltage across the device in a region of low differential resistance during the application of an impulse current (I_{PP}) for a specified waveform.
V_{WM}	Working standoff voltage: The maximum-rated value of DC or repetitive peak positive cathode-to-anode voltage that may be continuously applied over the standard operating temperature.

3. Electrical Characteristics

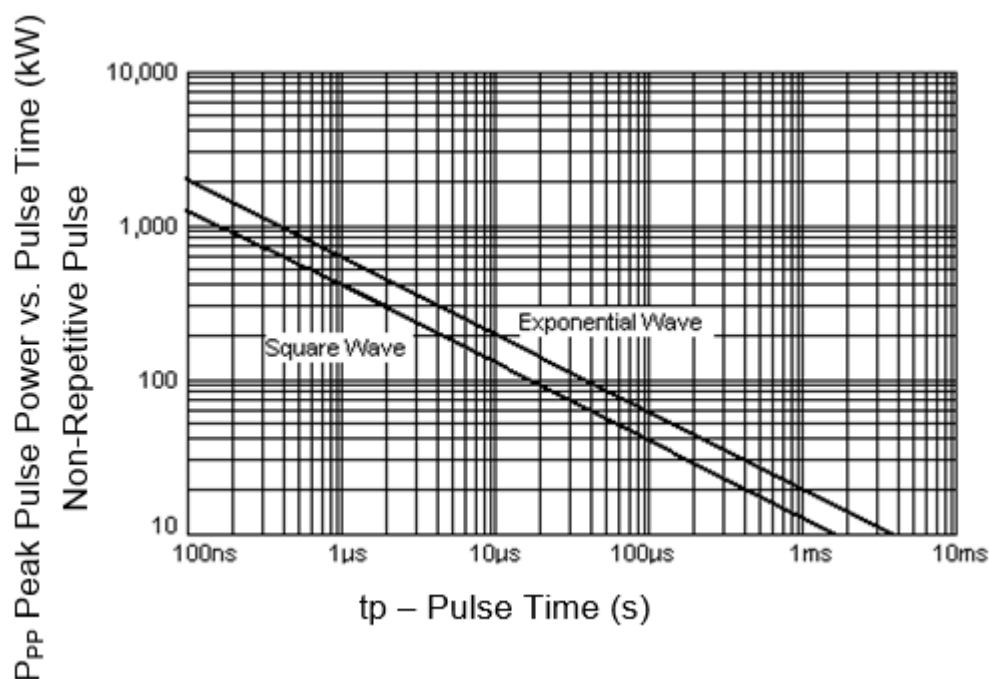
Table 3-1. Electrical Characteristics at 25 °C Unless Otherwise Stated

Part Number	Working Standoff Voltage ¹ V_{WM}	Minimum Breakdown Voltage at $I_{(BR)}$ $V_{(BR)} I_{(BR)}$		Maximum Clamping at I_{PP} ² V_C	Maximum Standby Current at V_{WM} I_D	Maximum Peak Pulse Current ³ at 6.4/69 μs I_{PP}
	Volts	Volts	mA	Volts	μA	Amps
MRT65KP48(C)A	48	53.3	5	77.7	5	836
MRT65KP54(C)A	54	60.0	5	87.5	5	742
MRT65KP60(C)A	60	66.7	5	97.3	5	668
MRT65KP75(C)A	75	83.3	5	122	5	533

1. Normal selection criteria for TVS devices is by working standoff voltage (V_{WM}) and should be equal or greater than DC or continuous peak operating voltage.
2. Clamping voltage does not include any variable parasitic lead inductance effects observed during the 6.4 μs rise time due to lead length.
3. TVS devices are tested to maximum peak pulse current (I_{PP}) with clamping voltage monitored. This surge capability is one of the most significant electrical characteristics of the device and should be considered as part of customer quality inspections. The maximum peak pulse current (I_{PP}) shown represents the performance capabilities by design.

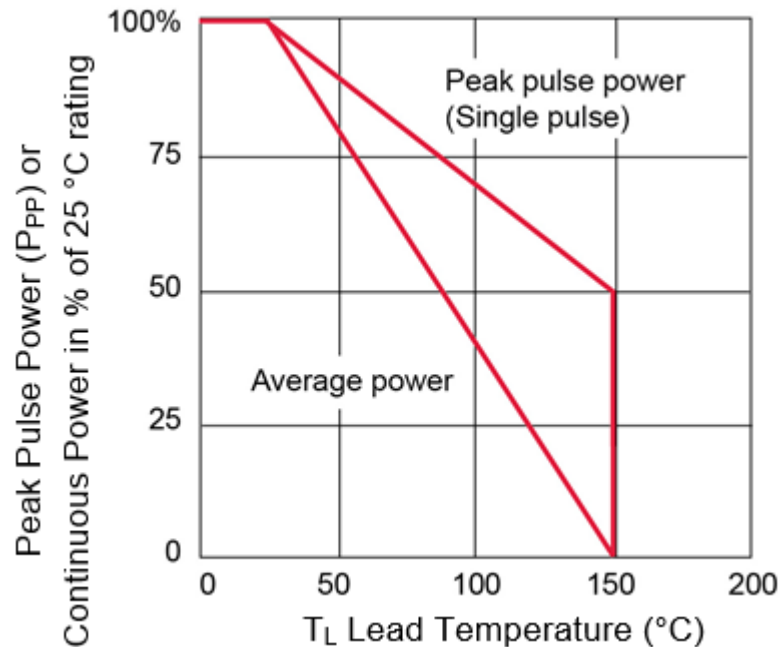
4. Graphs

Figure 4-1. Peak Pulse Power Vs. Pulse Time to 50% of Exponentially Decaying Pulse



Note:

1. This P_{PP} versus time graph allows the designer to use these parts over a broad power spectrum using the guidelines illustrated in [MicroNote 104](#). Aircraft transients are described with exponentially decaying waveforms. For suppression of square-wave impulses, derate power and current to 66% of that for the exponential decay shown in the preceding figure.

Figure 4-2. Power Derating

Installation

TVS devices used across power lines are subject to relatively high magnitude surge currents and are more prone to adverse parasitic inductance effects in the mounting leads. Minimizing the shunt path of the lead inductance and their $V = -L di/dt$ effects will optimize the TVS effectiveness. Examples of optimum installation and poor installation are illustrated in [Figure 4-3](#) to [Figure 4-6](#). [Figure 4-3](#) illustrates minimal parasitic inductance with attachment at end of device. Inductive voltage drop is across the input leads. Virtually no “overshoot” voltage results as illustrated with [Figure 4-5](#). The loss of effectiveness in protection caused by excessive parasitic inductance is illustrated in [Figure 4-4](#) and [Figure 4-6](#). Also see [MicroNote 111](#) for further information on “Parasitic Lead Inductance in TVS”.

Figure 4-3. Minimal Parasitic Inductance

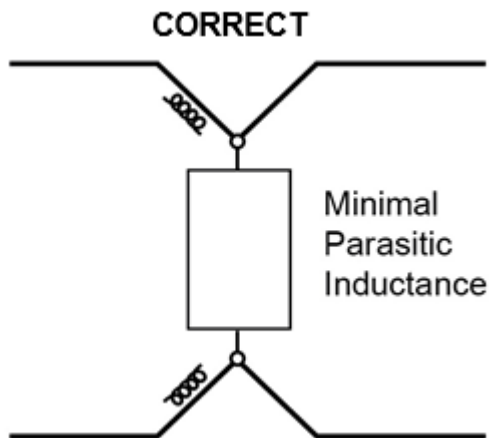


Figure 4-4. Excessive Parasitic Inductance

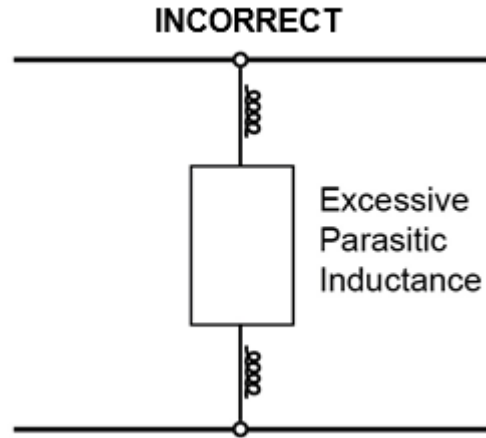


Figure 4-5. No "Overshoot" Voltage Results

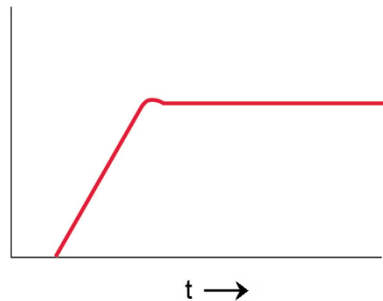


Figure 4-6. Voltage "Overshoot" Results

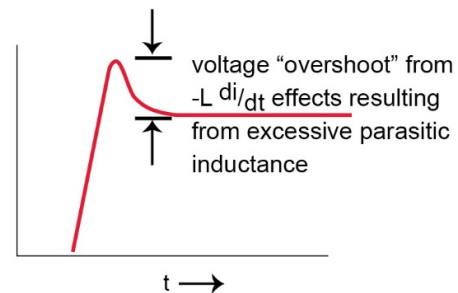


Figure 4-7. Waveform 3

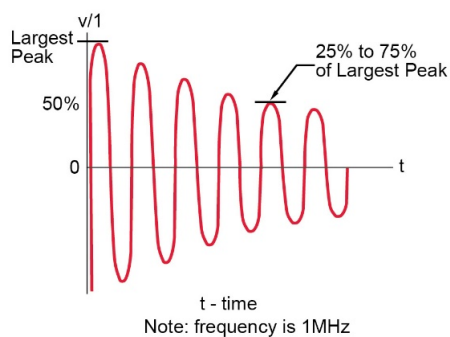


Figure 4-8. Waveform 4

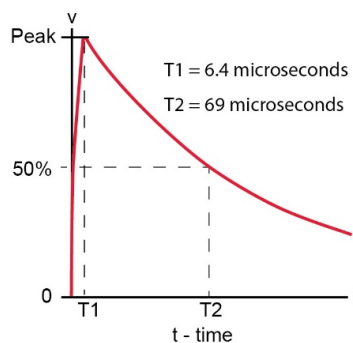
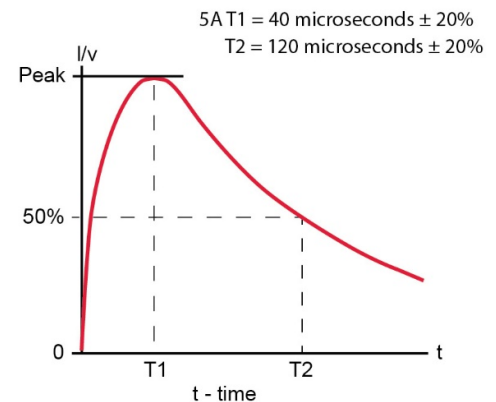


Figure 4-9. Waveform 5A



The 1 MHz damped oscillatory waveform (3) has an effective pulse width of 4 μ s. Equivalent peak pulse power at each of the pulse widths represented in RTCA/DO-160 for waveforms 3, 4 and 5A

have been determined referencing [Figure 4-1](#) as well as MicroNotes [104](#) and [120](#) and are listed in the following table.

Table 4-1. Peak Pulse Power and Pulse Widths of Waveforms¹⁻²

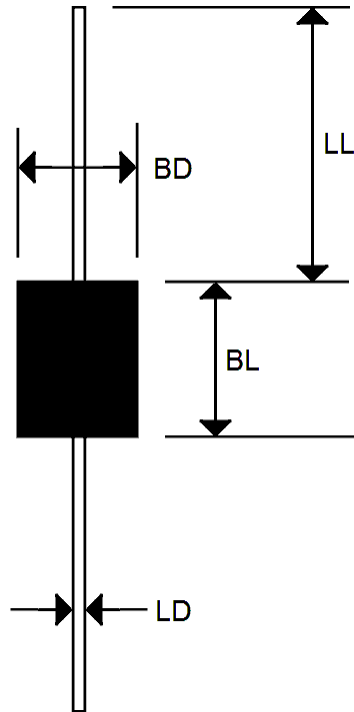
Waveform Number	Pulse Width μs	Peak Pulse Power kW	Peak Pulse Current Conversion Factor From Rated I_{PP} at 6.4/69 μs^3
3	4	220	3.40x
4	6.4/69	65	1.00x
5A	40/120	45	0.70x

Notes:

1. High current fast rise-time transients of 250 ns or less can more than triple the V_C from parasitic inductance effects ($V = -Ldi/dt$) compared to the clamping voltage shown in the initial electrical characteristics [Table 3-1](#) as also described in [Figure 4-4](#) and [Figure 4-6](#).
2. Also see MicroNotes [127](#), [130](#), and [132](#) for further information on transient voltage suppressors with reference to aircraft industry specification RTCA/DO-160.
3. Multiply by the conversion factor shown with reference to the maximum rated I_{PP} in the electrical characteristics [Table 3-1](#).

5. Package Dimensions

Figure 5-1. Package Dimensions



Dim.	Dimensions			
	Inch		Millimeters	
	Min.	Max.	Min.	Max.
LL	0.750	—	19.05	—
BL	0.365	0.385	9.27	9.78
BD	0.235	0.255	5.97	6.48
LD	0.047	0.053	1.194	1.346

6. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
A	01/2024	Initial revision.

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