MIC6211

Operational Amplifier

Features

- 4V to 32V Operation
- Small Footprint Package
- Unity Gain Stable
- 2.5 MHz Unity Gain Bandwidth
- Rail-to-Rail Output
- 6 V/µs Typical Slew Rate
- Short Circuit Protected

Applications

- Analog Blocks
- Data Acquisition
- Sensor Interface
- Portable Instrumentation
- Active Filtering

General Description

The MIC6211 op amp is a general-purpose, high performance, single- or split-supply, operational amplifier in a space-saving, surface-mount package.

The MIC6211 operates from 4V to 32V, single or differential (split) supply. The input common-mode range includes ground. The device features a 2.5 MHz unity gain bandwidth, 6 V/µs slew rate, and is internally unity-gain compensated.

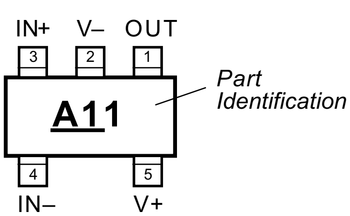
Inputs are protected against reverse polarity (input voltage less than V–) and ESD (electrostatic discharge). Output is current-limited for both sourcing and sinking. Output short-circuits of unlimited duration are allowed, provided the power dissipation specification is not exceeded.

The MIC6211 is available in the tiny, 5-lead SOT-23-5 surface-mount package.

Package Type

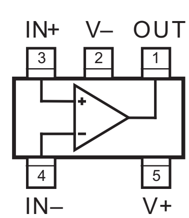
Pin Configuration

SOT23–5 (M5)

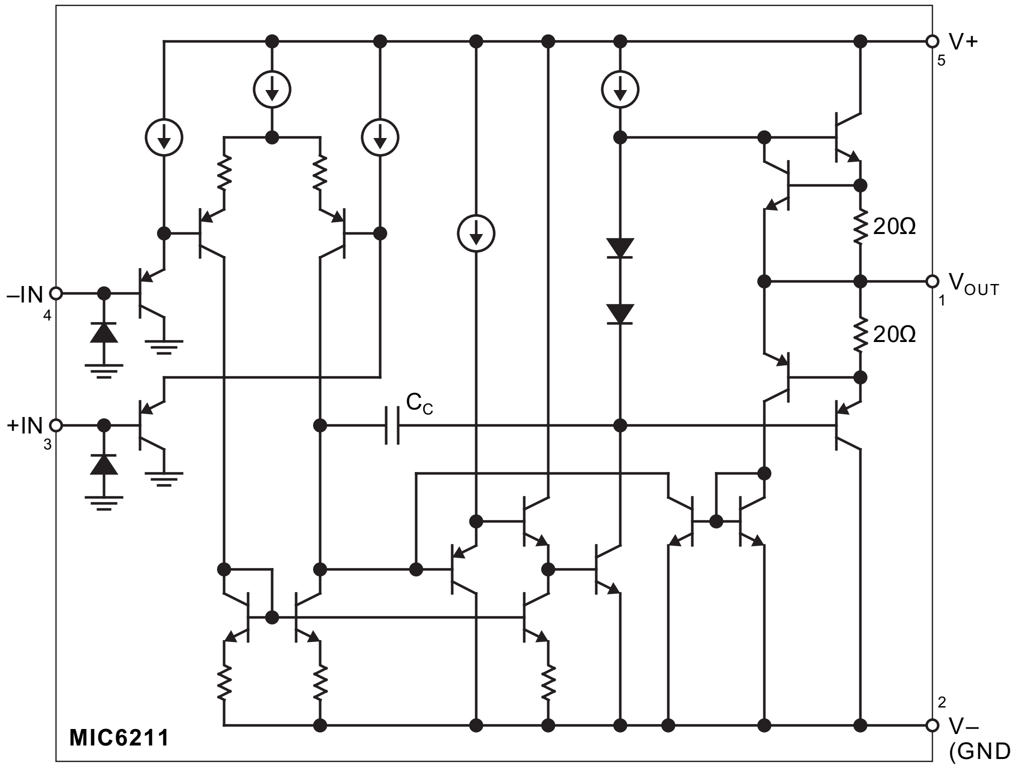


Functional Configuration

SOT23–5 (M5)



Functional Diagram



1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings †

Supply Voltage (V_{V+} to $V_{V–}$) .... 36V or ±18V
Differential Input Voltage (V_{IN+} to $V_{IN–}$) .... ±36V
Input Voltage (V_{IN+}, $V_{IN–}$) .... (V_{V-} – 0.3V) to V_{V+}
Output Short-Circuit Current Duration .... Indefinite

Operating Ratings ‡

Supply Voltage (V+ to V-) .... +4V to +32V

† Notice: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational sections of this specification is not intended. Exposure to maximum rating conditions for extended periods may affect device reliability.

‡ Notice: The device is not guaranteed to function outside the operating ratings.

ELECTRICAL CHARACTERISTICS (DIFFERENTIAL SUPPLY)

Electrical Characteristics: V+ = +15V, V– = –15V V_{CM} = 0V; R_L = 2 kΩ; T_A = 25°C, T_A= T_J; unless otherwise noted.						
Parameters	**Symbol**	**Min.**	**Typ.**	**Max.**	**Units**	**Conditions**
Input Offset Voltage	V_{OS}	—	2	7	mV	—
Average Input Offset Drift	TCV_{OS}	—	7	—	µV/°C	(Note 1)
Input Bias Current	I_B	—	50	250	nA	—
Input Offset Current	I_{OS}	—	8	30	nA	—
Input Voltage Range	V_{CM}	+13.5	+13.8	—	V	—
		-15.0	-15.3			
Common-Mode Rejection Ratio	CMRR	65	100	—	dB	V_{CM} = +13.5V, -15.0V
Power Supply Rejection Ratio	PSRR	65	110	—	dB	V_S = ±2.5V to ±15V
Large-Signal Voltage Gain	A_{VOL}	25	180	—	V/mV	V_O = ±10V
Maximum Output Voltage Swing	V_{OUT}	±12.5	±14	—	V	—
Bandwidth	B_W	—	2.5	—	MHz	—
Slew Rate	S_R	—	6	—	V/µs	—
Short-Circuit Output Current	I_{SC}	30	50	—	mA	Sourcing or Sinking
Supply Current	I_S	—	1.3	2.0	mA	—

Note 1: Not production tested.

ELECTRICAL CHARACTERISTICS (SINGLE SUPPLY)

Electrical Characteristics: V+ = +5V, V- = 0V, V_{CM} = 0.1V; T_A = 25°C, T_A= T_J; unless otherwise noted.						
Parameters	**Symbol**	**Min.**	**Typ.**	**Max.**	**Units**	**Conditions**
Input Offset Voltage	V_{OS}	—	2	7	mV	—
Average Input Offset Drift	TCV_{OS}	—	7	—	µV/°C	(Note 1)
Input Bias Current	I_B	—	65	250	nA	—
Input Offset Current	I_{OS}	—	8	30	nA	—
Input Voltage Range	V_{CM}	+3.5	+3.7	—	V	
		0	-0.3			
Common-Mode Rejection Ratio	CMRR	45	70	—	dB	V_{CM} = 0V to 3.5V
Power Supply Rejection Ratio	PSRR	65	105	—	dB	V_S = ±2.5V to ±15V
Large-Signal Voltage Gain	A_{VOL}	15	170	—	V/mV	V_O = 1.5V to 3.5V, R_L = 2 kΩ
Maximum Output Voltage Swing	V_{OUT}	±3.8	+4.0	—	V	R_L = 10 kΩ to GND
			+1.0	+1.2		R_L = 10 kΩ to +5V
Short-Circuit Output Current	I_{SC}	20	40	—	mA	Sourcing or Sinking
Supply Current	I_S	—	1.2	1.8	mA	—

Note 1: Not production tested.

TEMPERATURE SPECIFICATIONS (Note 1)

Parameters	Symbol	Min.	Typ.	Max.	Units	Conditions
Temperature Ranges						
Ambient Temperature Range	T_A	–40	—	+85	°C	—
Package Thermal Resistance						
Thermal Resistance SOT-23-5	θ_{JA}	—	200	—	°C/W	Mounted to PCB

Note 1: The maximum allowable power dissipation is a function of ambient temperature, the maximum allowable junction temperature and the thermal resistance from junction to air (i.e., T_A, T_J, θ_{JA}). Exceeding the maximum allowable power dissipation will cause the device operating junction temperature to exceed the maximum +85°C rating. Sustained junction temperatures above +85°C can impact the device reliability.

2.0 TYPICAL PERFORMANCE CURVES

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.

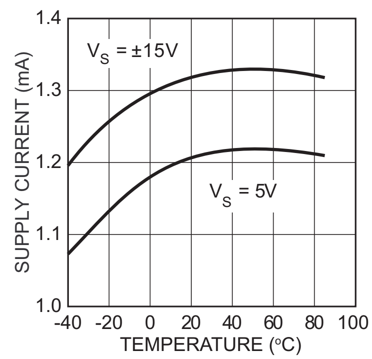


FIGURE 2-1: *Supply Current vs. Temperature.*

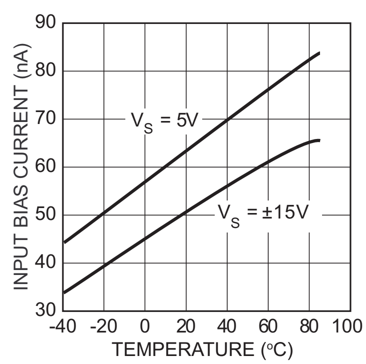


FIGURE 2-2: *Input Bias Current vs. Temperature.*

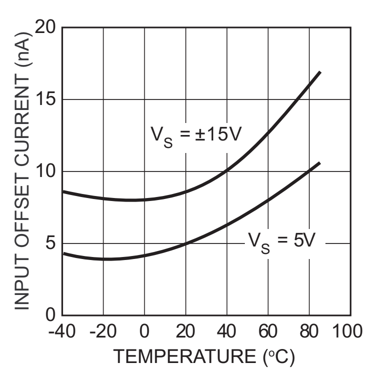


FIGURE 2-3: *Input Offset Current vs. Temperature.*

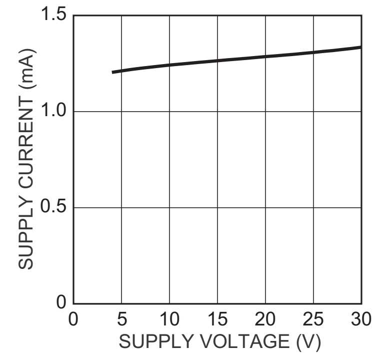


FIGURE 2-4: *Supply Current vs. Supply Voltage.*

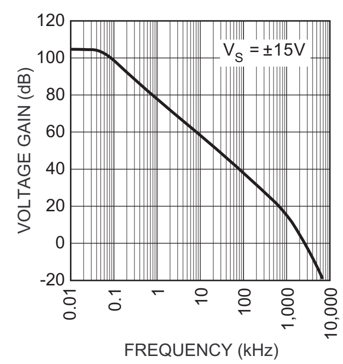


FIGURE 2-5: *Voltage Gain vs. Frequency.*

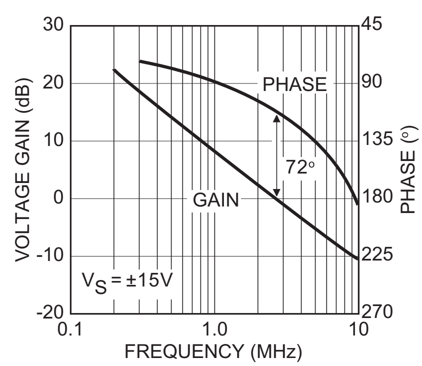


FIGURE 2-6: *Gain and Phase vs. Frequency.*

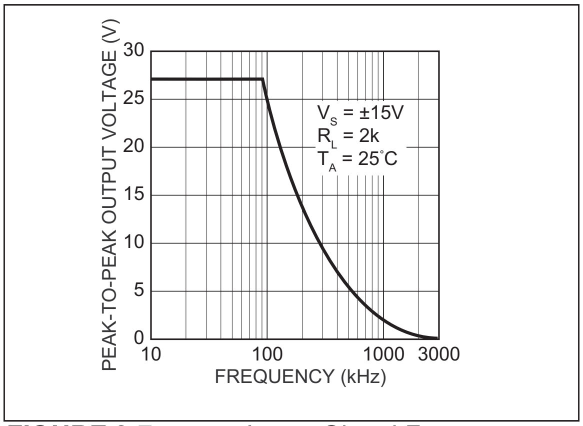


FIGURE 2-7: *Large-Signal Frequency Response.*

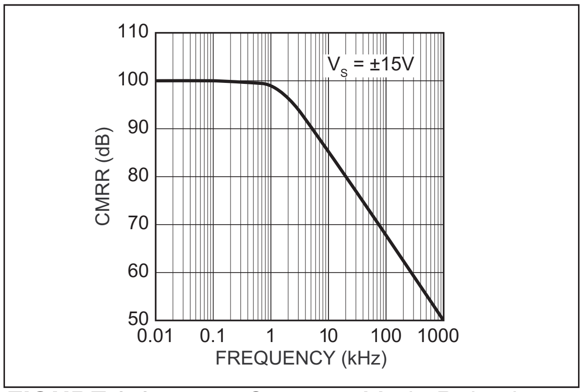


FIGURE 2-8: *Common Mode Rejection Ratio vs. Frequency.*

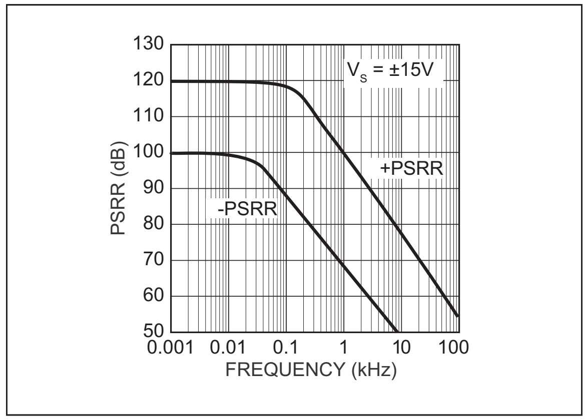


FIGURE 2-9: *Power Supply Rejection Ratio vs. Frequency.*

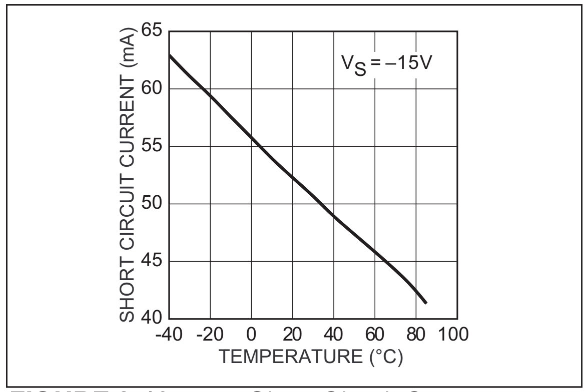


FIGURE 2-10: *Short-Circuit Current vs. Temperature.*

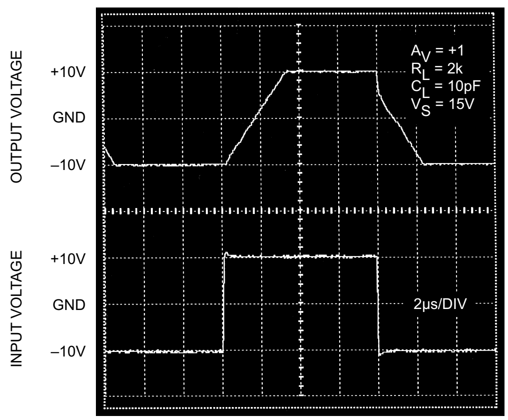


FIGURE 2-11: *Large-Signal Transient Response.*

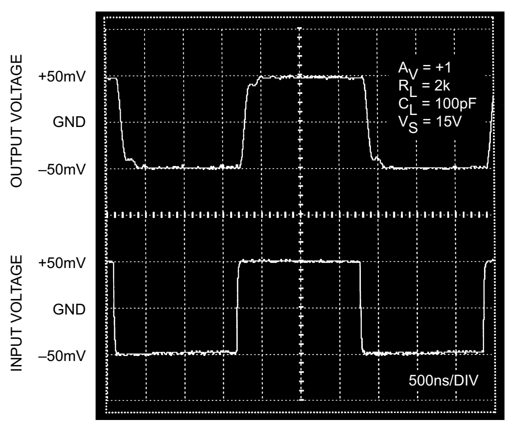


FIGURE 2-12: *Small-Signal Transient Response.*

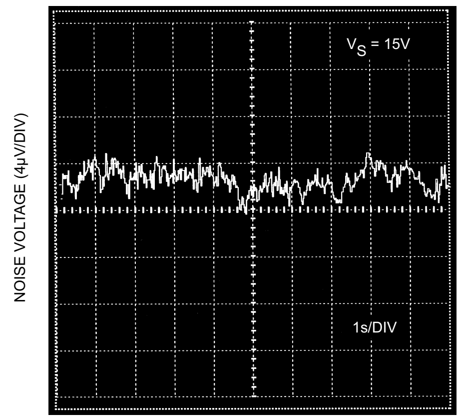


FIGURE 2-13: *0.1 Hz to 10 Hz Noise.*

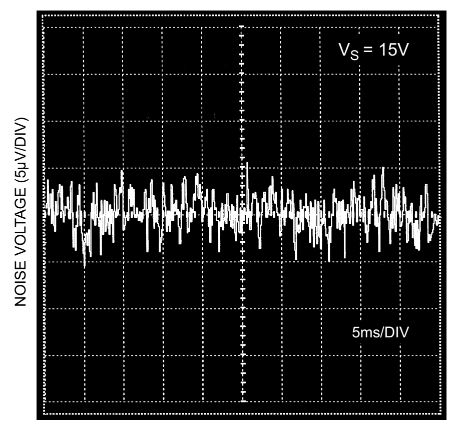


FIGURE 2-14: *Wideband Noise.*

3.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 3-1.

TABLE 3-1: PIN FUNCTION TABLE

Pin Number	Symbol	Description
1	OUT	Amplifier Output.
2	V–	Negative Supply. Negative supply for split supply application or ground for single supply application.
3	IN+	Non-Inverting Input.
4	IN–	Inverting Input.
5	V+	Positive Supply.

4.0 APPLICATION INFORMATION

4.1 Common-Mode Range and Output Voltage

The input common-mode range of the MIC6211 is from the negative supply voltage to 1.2V below the positive supply voltage. The output voltage swings within 1V of the positive and negative supply voltage.

4.2 Voltage Buffer

Figure 4-1 shows a standard voltage follower/buffer. The output voltage equals the input voltage. This circuit is used to buffer a high impedance signal source. This circuit works equally well with single or split supplies.

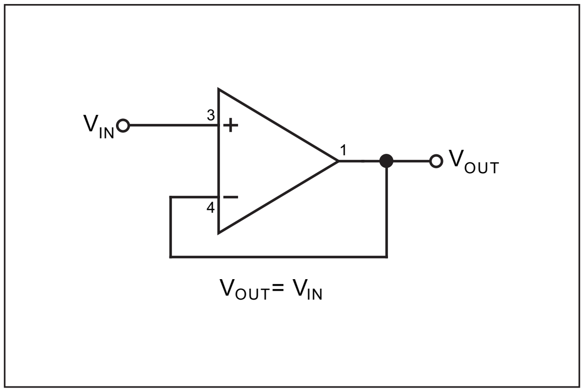


FIGURE 4-1: *Voltage Buffer.*

4.3 Inverting Amplifier

Figure 4-2 shows an inverting amplifier with its gain set by the ratio of two resistors. This circuit works best with split supplies, but will perform with single supply systems if the non-inverting input (+ input) is biased up above ground.

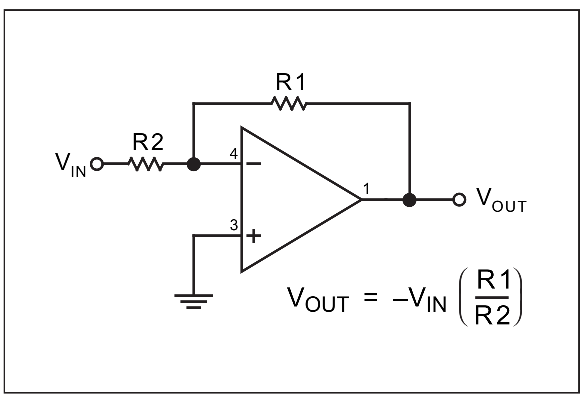


FIGURE 4-2: *Inverting Amplifier.*

4.4 Voltage Controlled Current Sink

Figure 4-3 is a voltage controlled current sink. A buffer transistor forces current through a programming resistor until the feedback loop is satisfied. Current flow is V_{IN}/R. This circuit works with single or split supplies.

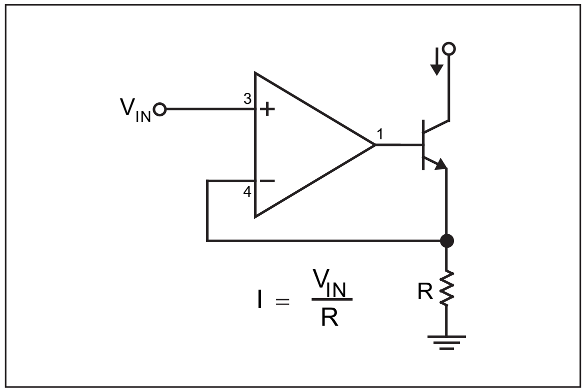


FIGURE 4-3: *Voltage Controlled Current Sink.*

4.5 High-Pass Filter

Figure 4-4 is an active filter with 20 dB (10×) gain and a low frequency cutoff of 10 Hz. The high gain-bandwidth of the MIC6211 allows operation beyond 100 kHz. This filter configuration is designed for split supplies.

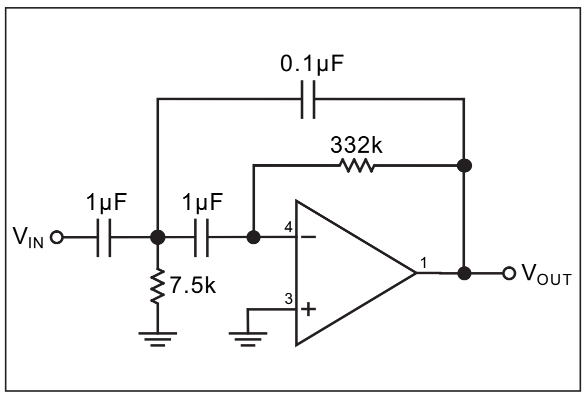


FIGURE 4-4: *High–Pass Filter.*

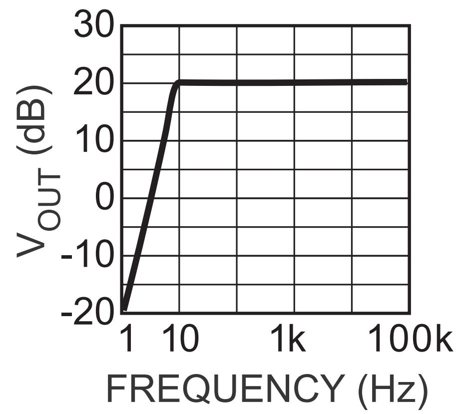
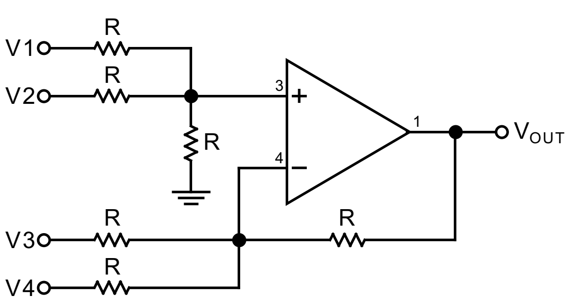


FIGURE 4-5: *High-Pass Filter Response.*

4.6 Summing Amplifier

Figure 4-5 is a single supply summing amplifier. In this configuration, the output voltage is the sum of V1 and V2, minus the sum of V3 and V4. By adding more resistors to either the inverting or non-inverting input, more voltages may be summed. This single supply version has one important restriction: the sum of V1 and V2 must exceed the sum of V3 and V4, since the output voltage cannot pull below zero with only a single supply.



All resistors are equal.

$V_{OUT} = V1 + V2 - V3 - V4$

V1 + V2 > V3 + V4 for single supply operation

FIGURE 4-6: *Summing Amplifier.*

5.0 PACKAGING INFORMATION

5.1 Package Marking Information

5-Lead SOT-23* (Front)	Example
XXX	A11

5-Lead SOT-23* (Back)	Example
NNN	469

Legend:	XX...X	Product code or customer-specific information
	Y	Year code (last digit of calendar year)
	YY	Year code (last 2 digits of calendar year)
	WW	Week code (week of January 1 is week ‘01’)
	NNN	Alphanumeric traceability code
	(e3)	Pb-free JEDEC® designator for Matte Tin (Sn)
	*	This package is Pb-free. The Pb-free JEDEC designator ((e3)) can be found on the outer packaging for this package.
	●, ▲, ▼	Pin one index is identified by a dot, delta up, or delta down (triangle mark).

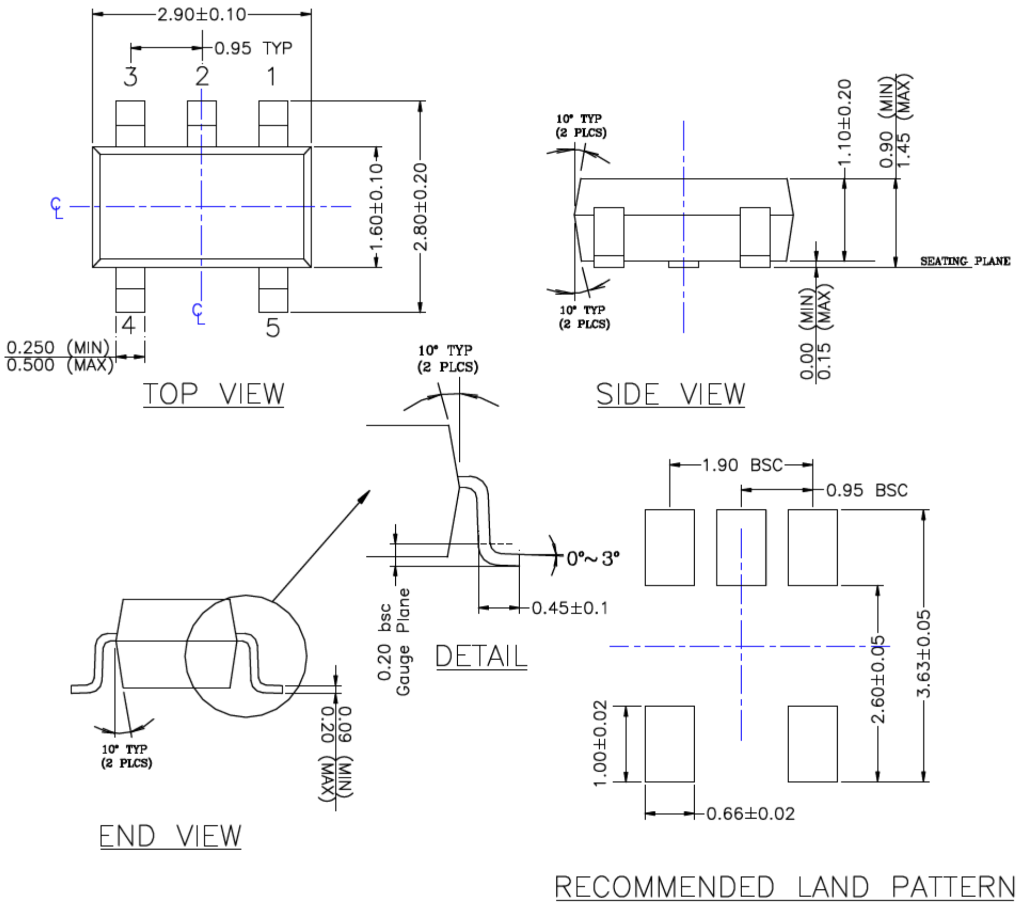
Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for customer-specific information. Package may or may not include the corporate logo.

Underbar (_) and/or Overbar (‾) symbol may not be to scale.

5-Lead SOT23 Package Outline and Recommended Land Pattern

TITLE
5 LEAD SOT23 PACKAGE OUTLINE & RECOMMENDED LAND PATTERN

DRAWING #	SOT23-5LD-PL-1	UNIT	MM



NOTE:
1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & BURR.
2. PACKAGE OUTLINE INCLUSIVE OF SOLER PLATING.
3. DIMENSION AND TOLERANCE PER ANSI Y14.5M, 1982.
4. FOOT LENGTH MEASUREMENT BASED ON GAUGE PLANE METHOD.
5. DIE FACES UP FOR MOLD, AND FACES DOWN FOR TRIM/FORM.
6. ALL DIMENSIONS ARE IN MILLIMETERS.

Note: For the most current package drawings, please see the Microchip Packaging Specification located at http://www.microchip.com/packaging.

APPENDIX A: REVISION HISTORY

Revision A (July 2020)

- Converted Micrel document MIC6211 to Microchip data sheet template DS20006346A.
- Minor text changes throughout.

NOTES:

PRODUCT IDENTIFICATION SYSTEM

To order or obtain information, e.g., on pricing or delivery, contact your local Microchip representative or sales office.

PART NO.	X	XX	-XX
Device	Temperature	Package	Media Type

Device:	MIC6211:	Operational Amplifier
Temperature:	Y =	–40°C to +85°C
Package:	M5 =	5-Lead SOT-23
Media Type:	TR =	3,000/Reel

Examples:

a) MIC6211YM5-TR: Op Amp, –40°C to +85°C Junction Temperature Range, 5-Lead SOT-23 Package, 3,000/Reel

Note 1: Tape and Reel identifier only appears in the catalog part number description. This identifier is used for ordering purposes and is not printed on the device package. Check with your Microchip Sales Office for package availability with the Tape and Reel option.

NOTES:

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ISBN: 978-1-5224-6417-4

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