



VOID-LESS HERMETICALLY SEALED ULTRAFAST RECOVERY GLASS RECTIFIERS

Qualified per MIL-PRF-19500/477

Qualified Levels: JAN, JANTX, JANTXV and JANS

DESCRIPTION

This "Ultrafast Recovery" rectifier diode series is military qualified and is ideal for high-reliability applications where a failure cannot be tolerated. The industry-recognized 6.0 amp rated rectifiers with working peak reverse voltages from 50 to 150 volts are hermetically sealed with void-less glass construction using an internal "Category 1" metallurgical bond. These devices are available in both surface mount MELF and leaded package configurations. Microsemi also offers numerous other rectifier products to meet higher and lower current ratings with various recovery time requirements including standard, fast and ultrafast device types in both through-hole and surface mount packages.

Important: For the latest information, visit our website http://www.microsemi.com.

FEATURES

- JEDEC registered surface mount equivalent of 1N5807, 1N5809, 1N5811 series.
- Void-less hermetically sealed glass package.
- Quadruple-layer passivation.
- Extremely robust construction.
- Internal "Category 1" metallurgical bonds.
- JAN, JANTX, JANTXV and JANS qualifications are available per MIL-PRF-19500/477.
- RoHS compliant versions available (commercial grade only).

APPLICATIONS / BENEFITS

- Ultrafast recovery 6 amp rectifier series from 50 to 150 V.
- Military, space and other high-reliability applications.
- Switching power supplies or other applications requiring extremely fast switching & low forward
- High forward surge current capability.
- Low thermal resistance.
- Controlled avalanche with peak reverse power capability.
- Inherently radiation hard as described in Microsemi MicroNote 050.

MAXIMUM RATINGS @ T_A= 25 °C unless otherwise specified

Parameters/Test Conditions	Symbol	Value	Unit
Junction and Storage Temperature	T_J and T_{STG}	-65 to +175	°C
Thermal Resistance Junction-to-End Cap Figure 1	$R_{\Theta JEC}$	6.5	°C/W
Thermal Resistance	R _{OJX}	52	°C/W
Working Peak Reverse Voltage: 1N5807 1N5809 1N5811	V RWM	50 100 150	V
Forward Surge Current (3)	I _{FSM}	125	Α
Average Rectified Output Current @ T _L = +75 °C at 3/8 inch lead length ⁽¹⁾	I _{O1}	6.0	А
Average Rectified Output-Current @ T _A = +55 °C at 3/8 inch lead length (2)	I ₀₂	3.0	А
Capacitance @ V _R = 10 V, f = 1 MHz; Vsig = 50 mV (p-p)	CJ	60	pF
Reverse Recovery Time (4)	t _{rr}	30	ns
Solder Temperature @ 10 s	T_SP	260	°C

Notes: 1. I_{O1} is rated at T_{EC} = 75 °C. Derate at 60 mA/°C for T_{EC} above 75 °C.

- 2. I_{O2} is derated at 25 mA/°C above $T_A = 55$ °C for PC boards where thermal resistance from mounting point to ambient is sufficiently controlled where T $_{J(max)}$ 175 °C is not exceeded. 3. T $_A$ = 25 °C @ I $_O$ = 3.0 A and V $_{RWM}$ for ten 8.3 ms surges at 1 minute intervals.
- 4. $I_F = 1.0 \text{ A}$, $I_{RM} = 1.0 \text{ A}$, $I_{R(REC)} = .0.10 \text{ A}$ and di/dt = 100 A/ μ s min.



Package (US)



"B" MELF Package (URS)

Also available in:

"B" Package (axial-leaded) **1N5807, 09 and 11**

MSC – Lawrence

6 Lake Street, Lawrence, MA 01841 Tel: 1-800-446-1158 or (978) 620-2600 Fax: (978) 689-0803

MSC - Ireland

Gort Road Business Park. Ennis, Co. Clare, Ireland Tel: +353 (0) 65 6840044 Fax: +353 (0) 65 6822298

Website:

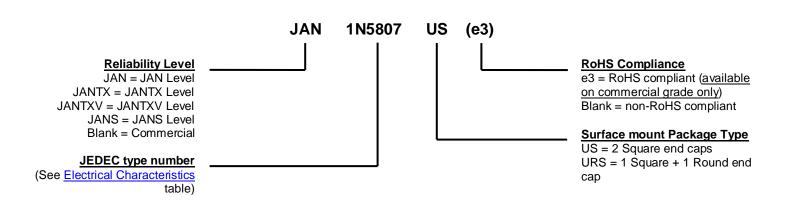
www.microsemi.com



MECHANICAL and PACKAGING

- CASE: Hermetically sealed void-less hard glass with tungsten slugs.
- TERMINALS: Tin/lead (Sn/Pb) or RoHS compliant matte/tin (commercial grade only) over nickel plate over copper.
- MARKING: Body coated in blue with part number.
- POLARITY: Cathode indicated by band.
- TAPE & REEL option: Standard per EIA-296. Consult factory for quantities.
- WEIGHT: 539 milligrams.
- See <u>Package Dimensions</u> on last page.

PART NOMENCLATURE



SYMBOLS & DEFINITIONS			
Symbol	Definition		
V_{BR}	Minimum Breakdown Voltage: The minimum voltage the device will exhibit at a specified current.		
V_{RWM}	Working Peak Reverse Voltage: The maximum peak voltage that can be applied over the operating temperature range.		
Io	Average Rectified Output Current: Output current averaged over a full cycle with a 50 Hz or 60 Hz sine-wave input and a 180 degree conduction angle.		
V _F	Maximum Forward Voltage: The maximum forward voltage the device will exhibit at a specified current.		
I_R	Maximum Leakage Current: The maximum leakage current that will flow at the specified voltage and temperature.		
С	Capacitance: The capacitance in pF at a frequency of 1 MHz and specified voltage.		
t _{rr}	Reverse Recovery Time: The time interval between the instant the current passes through zero when changing from the forward direction to the reverse direction and a specified recovery decay point after a peak reverse current occurs.		

ELECTRICAL CHARACTERISTICS @ T_A = 25 °C unless otherwise stated

	BREAKDOWN VOLTAGE (MIN.) @ 100 μA V _(BR)	VOL ⁻ @ 4 A (8.3	MAXIMUM FORWARD REVERSE VOLTAGE CURRENT @ 4 A (8.3 ms pulse) (MAX.) V _{FM} @ V _{RWM} I _R		SURGE CURRENT (MAX) I _{FSM} (Note 1)	REVERSE RECOVERY TIME (MAX) t _{rr} (Note 2)	
TYPE		Volts		μΑ			
	Volts	25 °C	125 °C	25 °C	125 °C	Amps	ns
1N5807	60	0.875	0.800	5	525	125	30
1N5809	110	0.875	0.800	5	525	125	30
1N5811	160	0.875	0.800	5	525	125	30

NOTES: 1. $T_A = 25$ °C @ $I_O = 3.0$ A and V_{RWM} for ten 8.3 ms surges at 1 minute intervals.

2. I_F = 1.0 A, I_{RM} = 1.0 A, $I_{R(REC)}$ = 0.10 A and di/dt = 100 A/ μs min.

GRAPHS

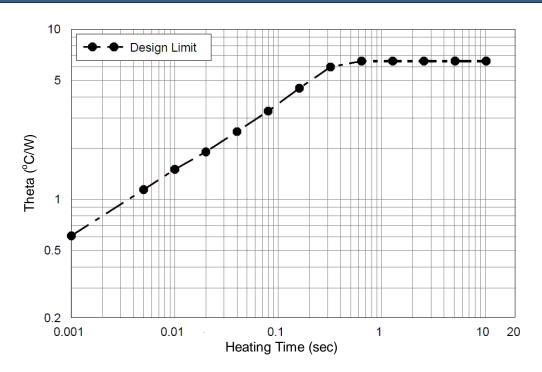


FIGURE 1

Maximum Thermal Impedance

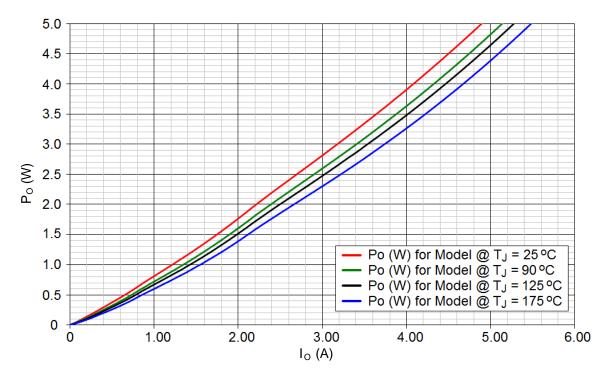


FIGURE 2
Rectifier Power vs I_O (Average Forward Current)



GRAPHS (continued)

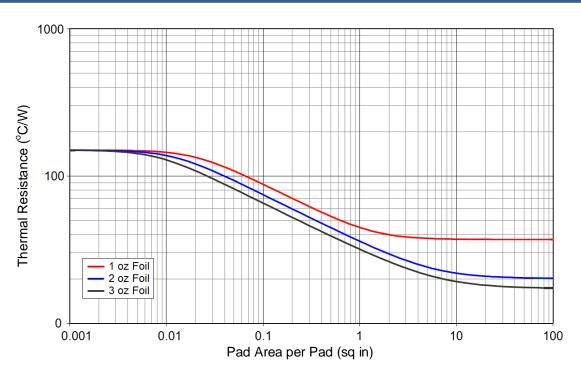


FIGURE 3

Thermal Resistance vs FR4 Pad Area At Ambient
PCB horizontal (for each pad) with 1, 2, and 3 oz copper

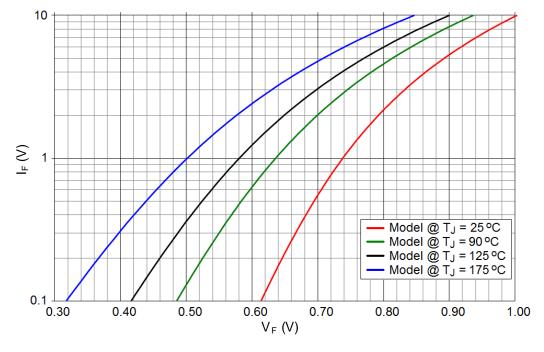
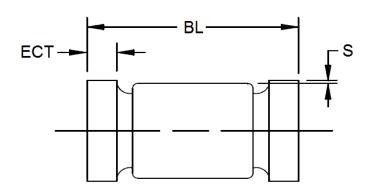
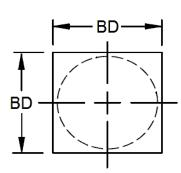


FIGURE 4
Forward Voltage vs Forward Current



PACKAGE DIMENSIONS



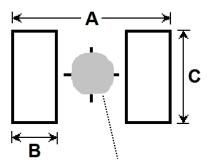


NOTES:

- 1. Dimensions are in inches.
- 2. Millimeters are given for general information only.
- 3. Dimensions are pre-solder dip.
- Minimum clearance of glass body to mounting surface on all orientations.
- Cathode marking to be either in color band, three dots spaced equally or a color dot on the face of the end tab.
- 6. Color dots will be .020 inch (0.51 mm) diameter minimum and those on the face of the end tab shall not lie within .020 inch (0.51 mm) of the mounting surface.
- In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.
- On "URS" one end cap shall be square and the other end cap shall be round.

	DIMENSIONS				
Ltr	INCH		MILLIMETERS		Notes
	Min	Max	Min	Max	
BD	.137	.148	3.48	3.76	8
BL	.200	.225	5.08	5.72	
ECT	.019	.028	0.48	0.71	8
S	.003		0.08		

PAD LAYOUT



DIM	INCH	MILLIMETERS
Α	0.288	7.32
В	0.070	1.78
С	0.155	3.94

NOTE: If mounting requires adhesive separate from the solder, an additional 0.080 inch diameter contact may be placed in the center between the pads as an optional spot for cement.

Mouser Electronics

Authorized Distributor

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Microchip:

JANTX1N5811US/TR JANTXV1N5811US/TR JANTXV1N5809US/TR JAN1N5811US/TR JANTX1N5809US/TR JANTX1N5807US/TR JANS1N5809US/TR JANS1N5811US/TR 1N5809US/TR JANTXV1N5807US/TR 1N5811USE3/TR 1N5811USE3 JANS1N5811US JANTXV1N5811US JAN1N5811US JANTX1N5811US JANTX1N5811US JANTX1N5807URS JANTX1N5809URS JAN1N5807URS JANTX1N5809URS JAN1N5807URS JAN1N5809URS JAN1N5809URS JAN1N5809URS JAN1N5809URS JANS1N5809URS JANS1N5809URS JANS1N5809URS JANS1N5809URS JANS1N5809URS/TR 1N5809URS/TR 1N5809URS/TR JAN1N5807US/TR 1N5807US/TR JAN1N5809URS/TR JAN1N5809URS/TR JAN1N5809URS/TR JAN1N5809URS/TR JAN1N5809URS/TR JAN1N5809URS/TR JAN1N5809URS/TR JANS1N5809URS/TR JANS1N5809URS/TR JANS1N5809URS/TR JANS1N5809URS/TR JANS1N5809URS/TR JANS1N5809URS/TR JANS1N5809URS/TR JANS1N5809URS/TR JANTX1N5809URS/TR JANTX1N5809URS/TR JANTXV1N5809URS/TR JANTXV1N5809URS/TR