



HV2925
Analog Switch
Evaluation Board
User's Guide

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Preface

NOTICE TO CUSTOMERS

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our website (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a “DS” number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is “DSXXXXXXXXA”, where “XXXXXXXX” is the document number and “A” is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics, to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the HV2925 Analog Switch Evaluation Board. Items discussed in this chapter include:

- [Document Layout](#)
- [Conventions Used in this Guide](#)
- [Recommended Reading](#)
- [The Microchip Website](#)
- [Customer Support](#)
- [Document Revision History](#)

DOCUMENT LAYOUT

This document describes how to use the HV2925 Analog Switch Evaluation Board as a development tool to emulate and debug firmware on a target board. The manual layout is as follows:

- **Chapter 1. “Product Overview”** – Important information about the HV2916 device.
- **Chapter 2. “Installation and Operation”** – This chapter includes instructions for how to begin using the HV2925 Analog Switch Evaluation Board.
- **Chapter 3. “GUI Description”** – This chapter describes the features of the GUI PC software.
- **Chapter 4. “PCB Design and Layout Notes”** – This chapter explains important points of the PCB design of the HV2925 Analog Switch Evaluation Board.
- **Appendix A. “Schematic and Layouts”** – Shows the schematic and layout diagrams for the HV2925 Analog Switch Evaluation Board.
- **Appendix B. “Bill of Materials (BOM)”** – Lists the parts used to build the HV2925 Analog Switch Evaluation Board.
- **Appendix C. “Demo Board Waveforms”** – Describes the various demo waveforms for the HV2925 Analog Switch Evaluation Board.

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CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	<i>MPLAB® IDE User's Guide</i>
	Emphasized text	...is the <i>only</i> compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	<u><i>File>Save</i></u>
Bold characters	A dialog button	Click OK
	A tab	Click the Power tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	#define START
	Filenames	autoexec.bat
	File paths	c:\mcc18\h
	Keywords	_asm, _endasm, static
	Command-line options	-Opa+, -Opa-
	Bit values	0, 1
	Constants	0xFF, 'A'
Italic Courier New	A variable argument	<i>file.o</i> , where <i>file</i> can be any valid filename
Square brackets []	Optional arguments	mcc18 [options] <i>file</i> [options]
Curly brackets and pipe character: { }	Choice of mutually exclusive arguments; an OR selection	errorlevel {0 1}
Ellipses...	Replaces repeated text	var_name [, var_name...]
	Represents code supplied by user	void main (void) { ... }

RECOMMENDED READING

This user's guide describes how to use the HV2925 Analog Switch Evaluation Board. Another useful document is listed below. The following Microchip document is available and recommended as a supplemental reference resource:

- **HV2825/HV2925/HV2926 Data Sheet – “Single 5V Bias, Low Harmonic Distortion, 64-Channel, High-Voltage Analog Switch” (DS20006849A)**

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- **General Technical Support** – Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip consultant program member listing
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- Field Application Engineer (FAE)
- Technical Support

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Technical support is available through the website at:
<http://www.microchip.com/support>.

DOCUMENT REVISION HISTORY

Revision A (December 2023)

- Initial release of this document.

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NOTES:

Chapter 1. Product Overview

1.1 INTRODUCTION

The HV2925 Analog Switch Evaluation Board (EV65S53A) works with the Microchip HV MUX Controller Board (EV34G23A) to provide single 5V bias, low harmonic distortion, 64-channel, high-voltage analog switch demonstration, including basic switch on/off operation, and 2:1 MUX operation to transmit $\pm 100V$ high-voltage pulse burst from two built-in MD1822 and TC6320 pulser circuits.

1.2 HV2925 DEVICE SHORT OVERVIEW

The HV2925 device is a single 5V bias, low harmonic distortion, 64-channel, high-voltage analog switch (with bleed resistors in SWS and SWT pins). It is designed to be used in applications requiring high-voltage switching, controlled by low-voltage control signals, such as medical ultrasound imaging, driving piezoelectric transducers and printers. Each analog switch has a typical 6Ω on-resistance and can conduct currents up to $\pm 2.9A$. The switches can withstand $\pm 100V$ (at the SWS pins) and can conduct pulses with widths up to $2.5\mu s$ without the need for high voltage supplies. It requires only a +5V single bias voltage supply at V_{DD} and V_{LL} for the switch operation. The user can also use +3.3V instead of +5V for the logic voltage, V_{LL} , in order to reduce power consumption.

The HV2925 device has a digital serial interface to control the 64 analog switches individually. The digital interface clock operates at frequencies up to 66 MHz.

The HV2925 has an asymmetric topology which is smaller in size compared to a no-high-voltage bias analog switch with symmetric topology. A switch in HV2925 has an SWT terminal and an SWS terminal. Since the HV2925 has asymmetric topology, the SWT pin and the SWS pin are not interchangeable. The SWT pin can pass a high-voltage pulsed signal applied to the SWS pin, when the switch is ON. When the switch is OFF, high-voltage should not be applied to the SWT pin. In a medical ultrasound system, the SWS pin is connected to Tx/Rx and the SWT pin is connected to a single piezoelectric transducer element in order to avoid high-voltage at the SWT pin during the switch OFF.

1.3 HV2925 ANALOG SWITCH EVALUATION BOARD FEATURES

- HV2925 Single 5V Bias, Low Harmonic Distortion, 64-Channel, High-Voltage Analog Switch with Bleed Resistors in SWS and SWT Pins
- Designed to work with the Microchip HV MUX Controller Board (EV34G23A)
- Two 2:1 MUX channels with built-in MD1822 and TC6320 Pulsers
- 5 MHz 3-level High-Voltage Pulse Burst Outputs
- On-Board 330 pF/2.5 k Ω Dummy Load on SW6T, SW7T, SW55T, SW56T Pins
- Pulser ON/OFF and Time Domain Control through the PC GUI (Graphic User Interface) and the HV MUX Controller Board

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1.4 WHAT IS THE HV2925 ANALOG SWITCH EVALUATION BOARD?

The HV2925 Analog Switch Evaluation Board can control the HV2925 device and built-in pulsers that are connected to the two 2:1 MUX switches for demonstration using the HV MUX Controller Board and GUI. Four switch outputs (SWT pins) from two 2:1 MUXes have SMA connectors which can be connected to four transducer elements. The other side of the 2:1 MUX (SWS pins) is connected to the outputs of two built-in MD1822 and TC6320 pulsers. The HV2925 Analog Switch Evaluation Board can drive four transducer elements with 5 MHz, $\pm 100\text{V}$ pulse signals.

The HV2925 Analog Switch Evaluation Board features one HV2925/LFBGA 11 x 11 x 1.5 mm 169-Ball TFBGA packaged integrated circuit, two MD1822K6-G 3 x 3 x 1 mm 16-Lead QFN packaged integrated circuits and four TC6320K6-G 4 x 4 x 1 mm 8-Lead DFN packaged NMOS and PMOS paired integrated circuits.

The HV2925 Analog Switch Evaluation Board uses two high-speed 20-signal pairs, carrying capable right-angle backplane connectors, which are designed to work with the HV MUX Controller Board as a control signal source.

The HV MUX Controller Board has an FPGA that generates pulser waveforms and logic control signals, and a USB bridge IC that connects the control board to a PC. By using a Microsoft® Windows® operated PC and the GUI software, the user can control the HV2925 device and two built-in pulsers on the HV2925 Analog Switch Evaluation Board.

Four switch terminals, consisting of two 2:1 MUX configurations on the PCB, have SMA connectors to which the user can connect loads. The jumpers close to the SMA connectors are for connecting the on-board dummy R-C load (330 pF//2.5 k Ω) optionally to the switch outputs.

WARNING

Risk warning of electrical shock. This board uses multiple hazardous high voltages. Disconnect all high-voltage supplies before working on it. Electrical safety precautions must be taken when working on or using this board.

1.5 HV2925 ANALOG SWITCH EVALUATION BOARD TECHNICAL PARAMETERS

TABLE 1-1: HV2925 ANALOG SWITCH EVALUATION BOARD TECHNICAL PARAMETERS

Parameter	Value
Pulser Frequency	5 MHz
Number of Pulses in the Burst	1 to 90
T _{OFF} Time Between Pulse Bursts	5 to 30 ms
Pulse Peak Voltage and Current	0 to $\pm 100\text{V}$ and $\pm 2.9\text{A}$ (typical)
Interface of FPGA Control Signals and USB PC-GUI Software	J1 and J2 Connects to EV34G23A Controller Interface Board
Pulser R-C Test Load and User's Transducer Interface	Built-in, 330 pF//2.5 k Ω per Channel with jumper and 50 Ω SMA
PCB Board Dimension	115 mm x 110 mm

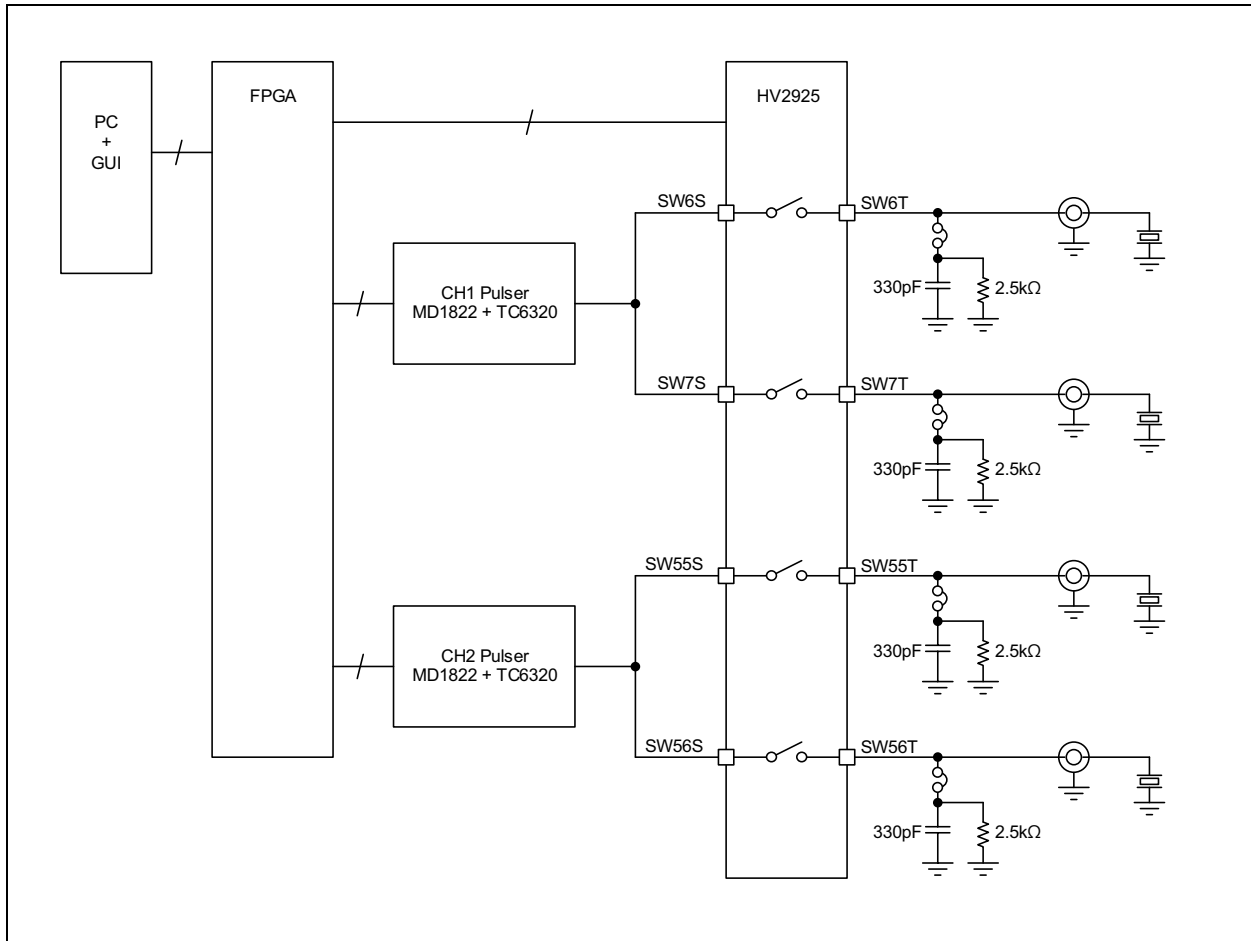


FIGURE 1-1: HV2925 Analog Switch Evaluation Board Simplified Block Diagram.

1.6 HV2925 ANALOG SWITCH EVALUATION BOARD KIT CONTENTS

The HV2925 Analog Switch Evaluation Board includes:

- HV2925 Analog Switch Evaluation Board (EV65S53A)
- Important Information Sheet

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NOTES:

Chapter 2. Installation and Operation

2.1 GETTING STARTED

The HV2925 Analog Switch Evaluation Board is fully assembled and tested. The board requires five power supply voltage rails: +5V for HV2925 and +3.3V, +10V and $\pm 100V$ for the pulser.

2.1.1 Additional Tools Required for Operation

1. An oscilloscope with a minimum of 500 MHz bandwidth and two high-impedance probes. Make sure the grounds of the power supply sources are correctly connected to the same ground as the testing oscilloscope ground;
2. A Microchip HV MUX Controller Board (EV34G23A);
3. A Microsoft® Windows® 7 or higher PC with the HV MUX Controller Board GUI software installed and running;
4. J1 and J2 connected to the HV MUX Controller Board;
5. HV MUX Controller Board connected through the USB port to the PC.

2.2 HV MUX GUI INSTALLATION

The HV MUX GUI software installer can be downloaded from the Microchip website at www.microchip.com. Search for the evaluation board on the website by the part number: **EV65S53A**.

1. Open the HV_MUX_64CH.
2. Initiate the HV MUX GUI software installer by launching the Application Install dialog box.

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FIGURE 2-1: HV MUX GUI – Application Install Dialog Box.

- 3. Click **Next** to start the installation.



FIGURE 2-2: HV MUX GUI – License Agreement Dialog Box.

- 4. Read the License Agreement and accept it by checking the box corresponding to "I accept the agreement". Click **Next** to proceed with the installation.

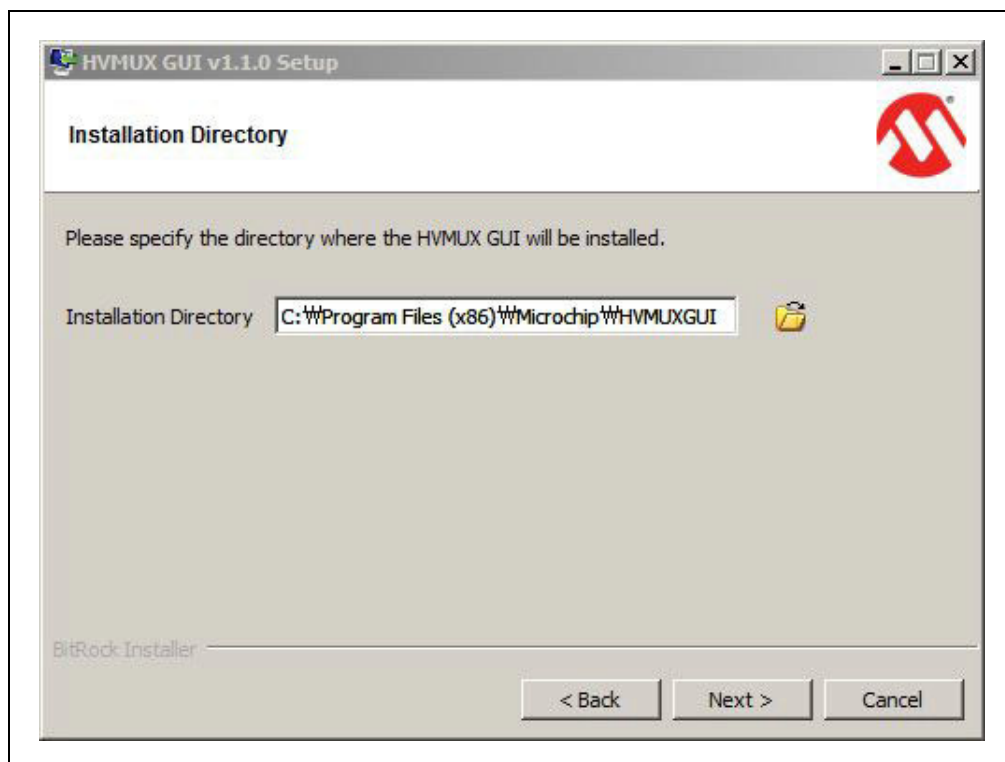


FIGURE 2-3: HV MUX GUI – Installation Directory Dialog Box.

5. On the Installation Directory dialog box, browse for the desired location or click **Next** to install in the default location.



FIGURE 2-4: HV MUX GUI – Ready to Install Dialog Box.

6. Once the installation path is chosen, the software is ready to install. Click **Next**.

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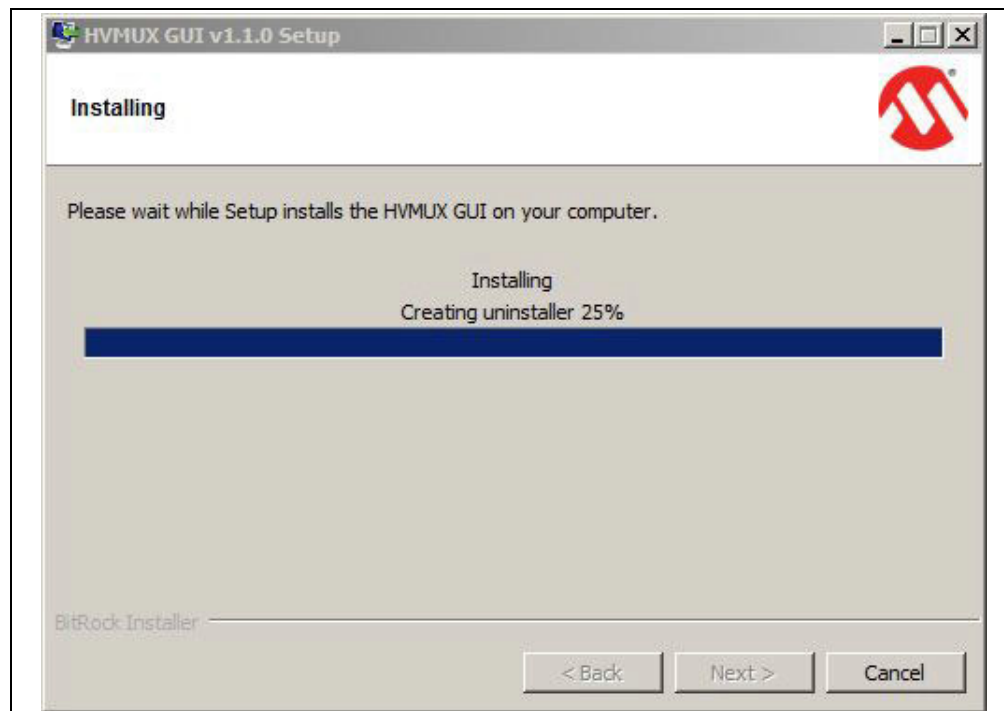


FIGURE 2-5: HV MUX GUI – Installation Status Dialog Box.

7. The Installation Status window appears, showing the installation progress.
8. After the installation has completed, click **Next**.



FIGURE 2-6: HV MUX GUI – Installation Complete Dialog Box.

9. Once the Installation Complete dialog box appears, click the **Finish** button to exit the installer.

2.3 HV2925 ANALOG SWITCH EVALUATION BOARD SETUP PROCEDURE

To operate the HV2925 Analog Switch Evaluation Board, the following steps must be completed:

1. Attach the HV2925 Analog Switch Evaluation Board to the HV MUX Controller Board (EV34G23A) with the J1 and J2 connectors.
2. Connect all the jumpers on J5, J6, J7 and J11 for the on-board R-C load.
3. Connect all the power supplies to the voltage supply input connectors J3 and J4, as indicated in [Table 2-1](#), by observing the polarity.

CAUTION

Observe the polarity of each power supply rail and set the voltage and current limit carefully.

4. Turn on the V_{LL} first and then turn on the V_{DD} .
5. Turn on the 3V3.
6. Turn on the V_{GP} and V_{PP}/V_{NN} .
7. Connect a USB cable from the HV MUX Controller Board to the PC.
8. Connect +12V/1A power to the HV MUX Controller Board and turn on OFF/ON switch.
9. Run the HV MUX GUI software on the PC.
10. Click the **Initialize HV MUX Controller** button in the GUI. This causes the status window at the bottom of the screen to display an "Initialization Complete" message.
11. Clear the $\overline{STBY}$ check box and select the MODE and 64 CH check boxes. (Do not change Bank Switching states. Not used for the HV2925 Analog Switch Evaluation Board.)
12. Click the **Set HV MUX** button. All digital control signals are applied to the HV2925 device.
13. Set the number of pulses and T_{OFF} time of the pulser.
14. Select CH1 or CH2 to set the Channel 1 pulser or the Channel 2 pulser, respectively.
15. Click the **Start** button for the selected pulser to generate pulse bursts.
16. Click the **Stop** button for the selected pulser to stop generating pulse bursts.

TABLE 2-1: POWER SUPPLY VOLTAGES AND CURRENT LIMIT SETTINGS

Terminal	Rail Name	Voltage	Average Current Limit
J3-1	V_{DD}	+5V	+20 mA
J3-2	GND	0V	—
J3-3	V_{LL}	+3.3V or +5V	+20 mA
J4-1	3V3	+3.3V	+150 mA
J4-2	GND	0V	—
J4-3	V_{GP}	+5V to +11.5V	+10 mA
J4-4	V_{PP}	+100V	+5 mA
J4-5	V_{NN}	-100V	-5 mA

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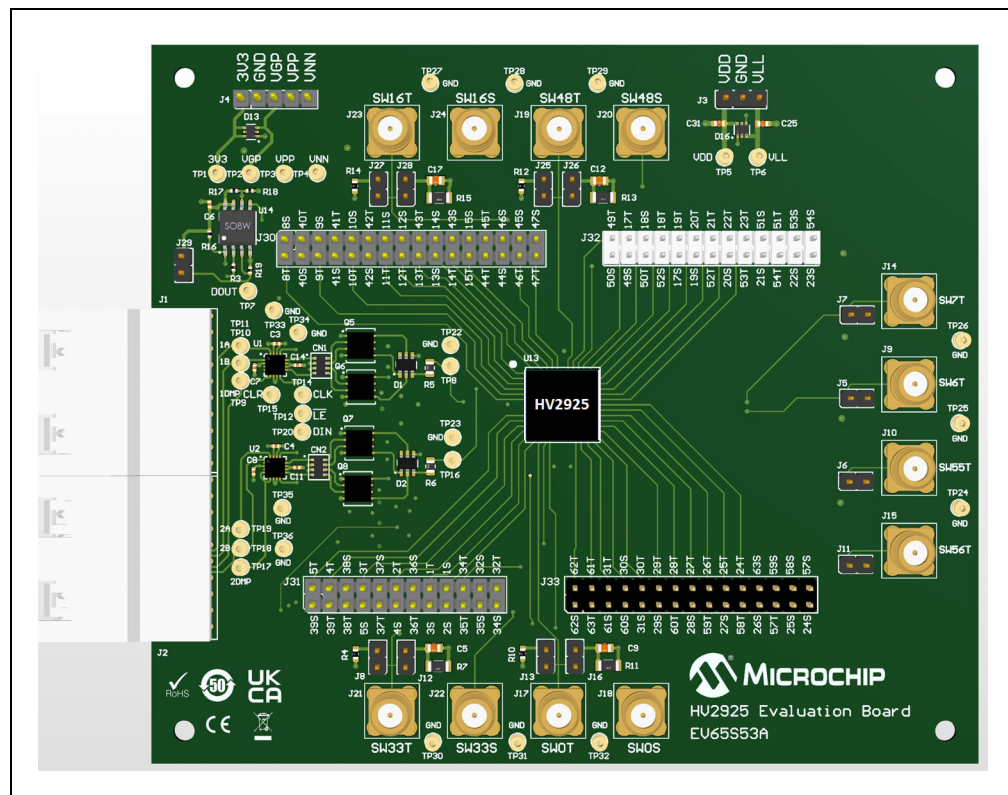


FIGURE 2-7: HV2925 Analog Switch Evaluation Board Front View.

2.3.1 Recommended Power-Up and Power-Down Sequences

Table 2-2 shows the recommended power-up and power-down sequences of the HV2925 Analog Switch Evaluation Board.

TABLE 2-2: HV2925 ANALOG SWITCH EVALUATION BOARD POWER-UP AND POWER-DOWN SEQUENCES

Step	Power-up Description	Step	Power-Down Description
1	V _{LL} ON	1	V _{PP} and V _{NN} OFF
2	V _{DD} ON	2	V _{GP} OFF
3	3V3 ON with Logic Signal Static	3	3V3 OFF with Logic Signal Static
4	V _{GP} ON	4	V _{DD} OFF
5	V _{PP} and V _{NN} ON	5	V _{LL} OFF

2.4 INTERFACE CONNECTIONS

TABLE 2-3: J2 CONTROL INTERFACE SIGNALS

Pin No.	Name	Test Point	I/O Type	Signal Direction
J2-A2	SCK	—	LVC MOS-2.5V Input	EEPROM Serial Clock Input
J2-B2	CSB	—	LVC MOS-2.5V Input	EEPROM Chip Select Input
J2-A3	MISO	—	LVC MOS-2.5V Output	EEPROM Serial Data Output
J2-B3	MOSI	—	LVC MOS-2.5V Input	EEPROM Serial Data Input
J2-A5	CLR	TP15	LVC MOS-3.3V Input	HV2925 Latch Clear Logic Input
J2-B5	CLK	TP14	LVC MOS-3.3V Input	HV2925 Clock Logic Input
J2-C5	$\overline{\text{LE}}$	TP12	LVC MOS-3.3V Input	HV2925 Latch Enable Logic Input
J2-A6	DIN	TP20	LVC MOS-3.3V Input	HV2925 Data In Logic Input
J2-C6	1_A	TP11	LVC MOS-3.3V Input	Ch1 Pulser Input for NMOS to V_{NN}
J2-D6	1_B	TP10	LVC MOS-3.3V Input	Ch1 Pulser Input for PMOS to V_{PP}
J2-A7	1_DMP	TP9	LVC MOS-3.3V Input	Ch1 Pulser Damp Input for PMOS/NMOS to GND
J2-B7	2_A	TP19	LVC MOS-3.3V Input	Ch2 Pulser Input for NMOS to V_{NN}
J2-C7	2_B	TP18	LVC MOS-3.3V Input	Ch2 Pulser Input for PMOS to V_{PP}
J2-D7	2_DMP	TP17	LVC MOS-3.3V Input	Ch2 Pulser Damp Input for PMOS/NMOS to GND

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2.5 HV MUX CONTROLLER BOARD SETUP PROCEDURE

The HV MUX Controller Board generates 3.3V control signals for the HV2925 Analog Switch Evaluation Board and features a Spartan-6 XC6SLX9 FPGA.

1. Before powering up the HV2925 Analog Switch Evaluation Board and the HV MUX Controller Board, make sure that the latest GUI software is installed on the PC.
2. Start the GUI program. If the board is not connected, a “Not Connected” message is displayed in the Status bar, located at the bottom left of the screen.
3. Connect the appropriate power supply and turn on the power switch to power-up the HV MUX Controller Board. The FPGA_OK(LD1), DC_IN (LD2) and PWR_OK(LD4) on the HV MUX Controller Board should light up green. A “Connected” message is displayed on the bottom left of the Status bar of the GUI.

The HV MUX Controller Board is now ready to control the HV2925 Analog Switch Evaluation Board.

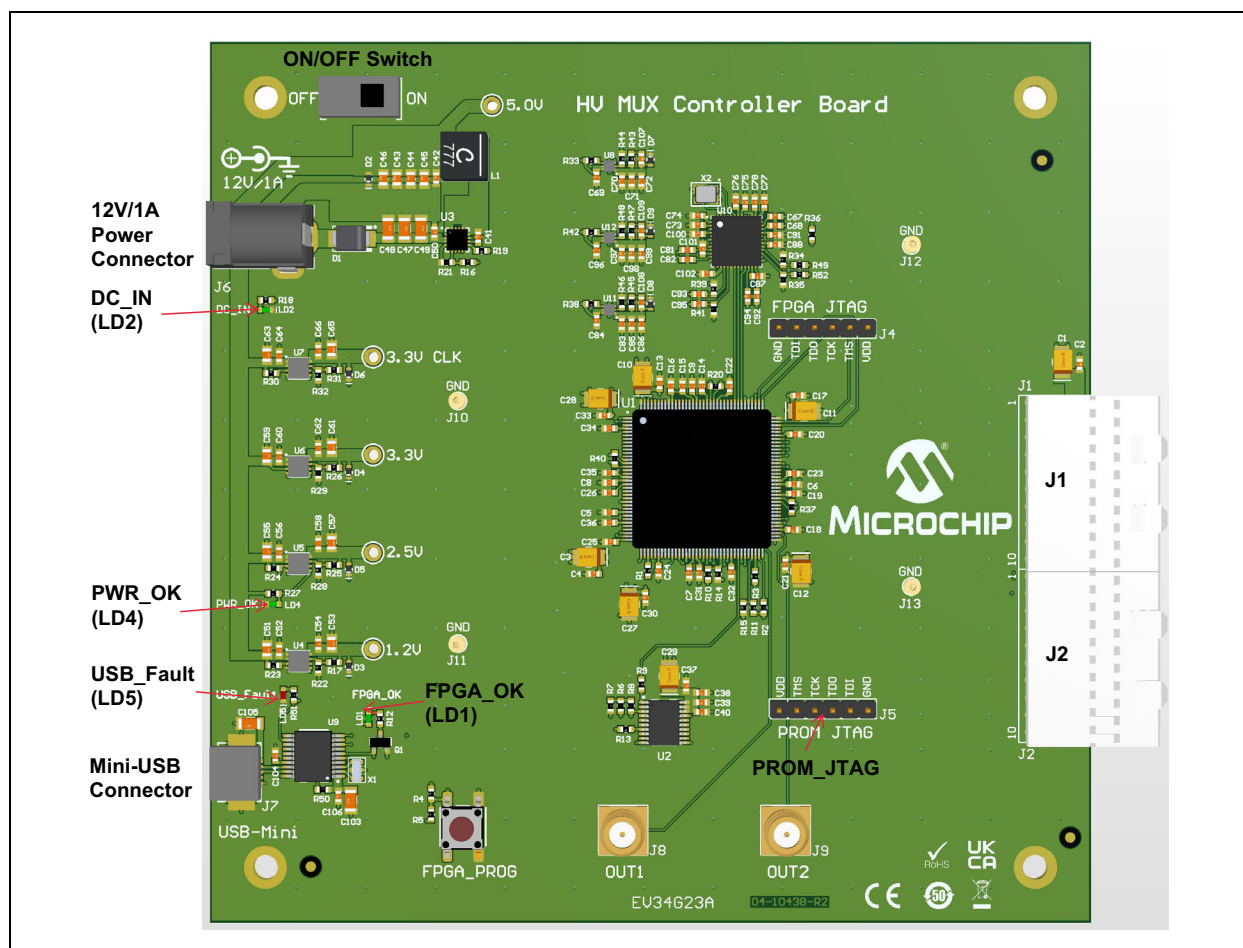


FIGURE 2-8: HV MUX Controller Board (EV34G23A) – Front View.

2.6 TESTING THE HV2925 ANALOG SWITCH EVALUATION BOARD

The user can turn on/off each of the 64 switches through the USB connected PC GUI software program by following the next steps:

1. Click the **Initialize HV MUX Controller** button, located at the top left corner.
2. Clear $\overline{\text{STBY}}$.
3. Select MODE and 64 CH.
4. Put 64-bit data in DIN to set switches ON and OFF. Data 1 means the switch is ON and data 0 means the switch is OFF.
5. Click the **Set HV MUX** button. The GUI and controller board now generate 64-bit data and 64 clocks, followed by one $\overline{\text{LE}}$ negative pulse and the switches are ON and OFF according to DIN in the GUI.
6. Select CLR and click the **Set HV MUX** button to set all switches to OFF.

Note: The typical voltage and waveforms are provided in [Appendix C. “Demo Board Waveforms”](#).

2.7 GENERATION OF PULSER OUTPUT AT SW6T OF HV2925

This section provides the simple step-by-step procedure to make the Ch1 pulser output at the SW6T SMA connector with dummy loads by configuring the GUI.

1. Before powering up the HV2925 Analog Switch Evaluation Board, make sure that the latest GUI software is installed on the PC.
2. Start the GUI program. If the board is not connected, a “Not Connected” message is displayed in the Status bar located at the bottom left of the screen.
3. Power up the HV MUX Controller Board and HV2925 Analog Switch Evaluation Board as described in the previous sections. The prompt “Connected”, is displayed in the Status bar.
4. Click the **Initialize HV MUX Controller** button and check the message window to see “Initialization Complete”.
5. Clear $\overline{\text{STBY}}$.
6. Select MODE.
7. Uncheck CLR.
8. Select 64 CH
9. Change the DIN to Bit 6 from ‘0’ to ‘1’ to set SW6 ON.
10. Click the **Set HV MUX** button to turn on the HV2916 SW6.
11. Change the Pulses to 10.
12. Select CH1.
13. Click the **Start** button. CH1 pulser starts to generate pulse bursts with 10 pulses (5 cycles) and 30 ms T_{OFF} time.

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The Ch1 and Ch2 of the oscilloscope in [Figure 2-9](#) show the SW6T and the SW7T.

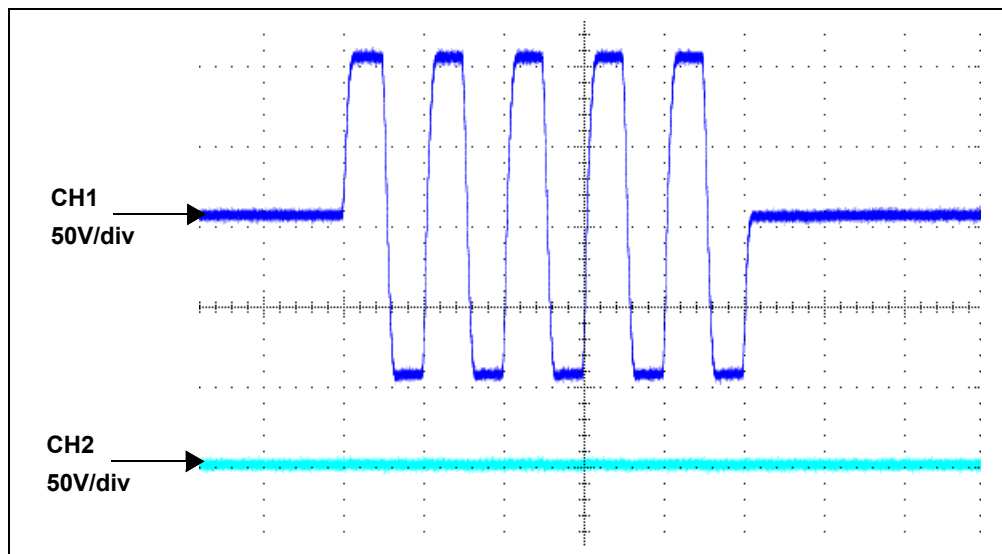


FIGURE 2-9: *Typical Waveform of 2:1 MUX Connected to Pulser.*

Chapter 3. GUI Description

3.1 HV2925 ANALOG SWITCH EVALUATION BOARD GUI DESCRIPTION

Figure 3-1 displays a screen capture of the HV MUX Controller Board GUI.

Table 3-1 provides a detailed description of every item numbered in the screen capture. The selection of the check box, binary data in the DIN entry box and number in the Pulses and T_{OFF} entry box are just settings and don't change the operation of the HV2925 device and built-in pulsers immediately. By clicking the **Set HV MUX**, **Start** and **Stop** buttons, and the control data set by the user in the GUI changes operation of the HV2925 device and turns on/off the built-in pulsers in the HV2925 Analog Switch Evaluation Board. Follow the explanation for each corresponding item.

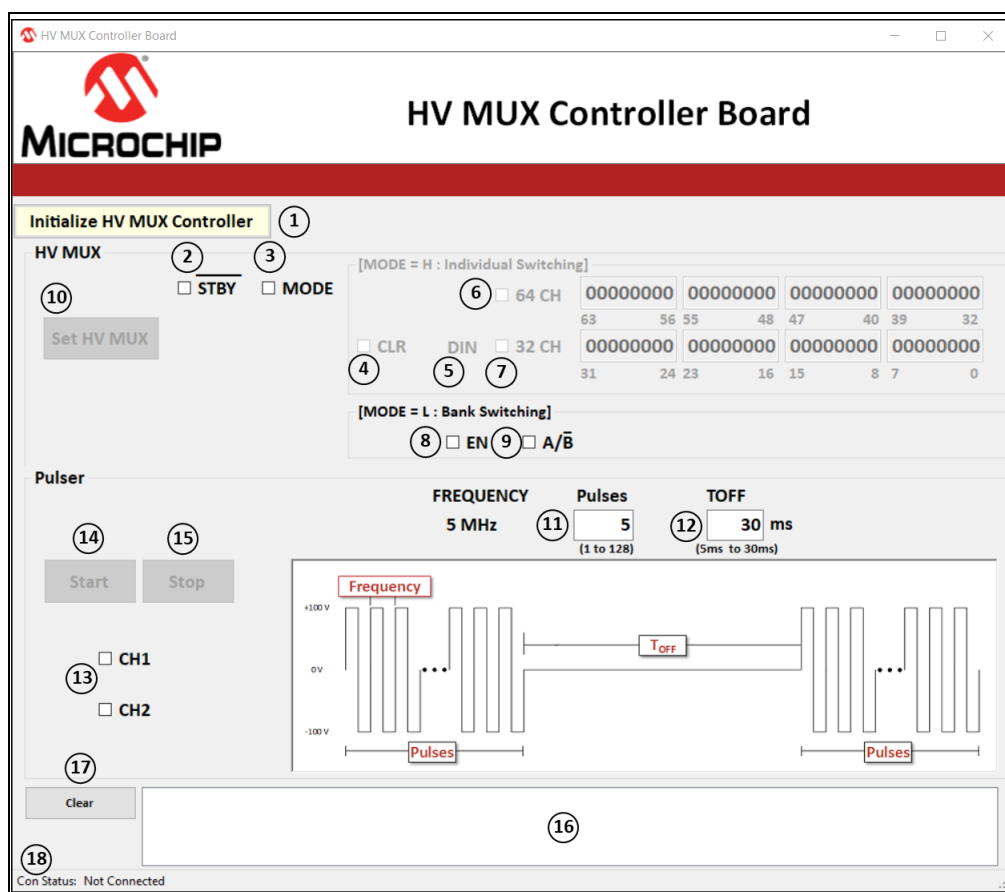


FIGURE 3-1: HV MUX Controller Board GUI Screen Capture.

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TABLE 3-1: HV2925 ANALOG SWITCH EVALUATION BOARD GUI DESCRIPTION

Item No.	Item Name	Item Description
1	Initialize HV MUX Controller	When this button is clicked, the GUI starts the initialization of FPGA on the HV MUX Controller Board and the communication between the GUI and the HV MUX Controller Board. If there is no error, the "Initialization Complete" message is displayed in the Message window.
2	STBY	Stays unselected. Not used for the HV2925 Analog Switch Evaluation Board.
3	MODE	Stays selected. Not used for the HV2925 Analog Switch Evaluation Board.
4	CLR	When this check box is selected, the CLR logic input is set to high and all the switches of the HV2925 device are set to off. When cleared, the CLR logic input is set to low and the 32 switches of HV2925 are set to ON/OFF states according to the DIN data entry.
5	DIN	64/32-bit data entry boxes. Each bit in the boxes is related to each analog switch. If data entry is 1, the associated switch is set to ON. If data entry is 0, the associated switch is set to OFF.
6	64 CH	64-bit data entry box. When selected, the GUI enables 64-bit data to program 64-ch analog switches.
7	32 CH	32-bit data entry box. When selected, the GUI enables 32-bit data to program 32-ch analog switches. Not used for the HV2925 Analog Switch.
8	EN	Deactivated when MODE is selected. Not used for the HV2925 Analog Switch Evaluation Board.
8	A/B	Deactivated when MODE is selected. Not used for the HV2925 Analog Switch Evaluation Board.
10	Set HV MUX	When this button is clicked, the data described in Items 2 through 10 is applied to the HV2925 device. Note that the 64-bit DIN data, 64 clocks and one negative LE pulse are applied only once.
11	Pulses	This text box defines the number of pulses in the pulse burst generated by the selected pulser. A pulse is half of the cycle and the pulse burst always starts the positive pulse first.
12	TOFF	This text box defines the interval between pulse bursts generated by the selected pulser.
13	Ch1/Ch2	When one of these check boxes is selected, the respective pulser is set to generate 5 MHz pulse bursts defined in items 11 and 12 by the user
14	Start	When this button is clicked, the selected pulser starts generating the pulse burst.
15	Stop	When this button is clicked, the selected pulser stops generating the pulse burst.
16	Message Window	This window displays information from the GUI program.
17	Clear	This button clears the messages in the Message window.
18	Connection Status	This window displays the status of the connection between the GUI and the HV MUX Controller Board.

Chapter 4. PCB Design and Layout Notes

4.1 PCB LAYOUT TECHNIQUES FOR HV2925

The HV2925 Analog Switch Evaluation Board has an analog switch to pass high-voltage, high-current and high-frequency pulses. Good PCB design and layout are important to ensure the success of the implementation.

4.1.1 High-Voltage and High-Speed Grounding and Layout Techniques

The user must pay attention to the connecting traces, since the analog switches pass high-voltage and high-speed signals. In particular, a controlled impedance of 50Ω to the ground plane and more trace spacing needs to be applied in this situation.

High-speed PCB trace design practices are used for the HV2925 Analog Switch Evaluation Board PCB layout. The internal circuitry of the HV2925 device can operate at a high frequency, with the primary speed limitation being the load capacitance. Because of these high-speed and high-transient currents that result from driving capacitive loads, the supply voltage bypass capacitors should be located as close to the power supply pins as possible.

All the GND pins should have low-inductance feed through connections that are connected directly to a solid ground plane of the PCB. It is recommended to minimize the trace length to the ground plane and to insert a ferrite bead in the power supply lead to the capacitor to prevent resonance in the power supply lines. It is important to minimize trace lengths and use sufficient trace width to reduce inductance. Surface mount components are highly recommended.

The use of a solid ground plane, good power, and signal layout practices prevent any possible parasitic capacitance coupling. The user should also ensure that the circulating ground return current from a capacitive load does not react with common inductance to create noise voltages in the input logic circuitry.

4.1.2 Decoupling Capacitors Selection

The V_{LL} and V_{DD} supply voltage rails are able to provide fast transient current. Therefore, they should have a low-impedance bypass capacitor close to each of the power supply pins. Use a surface-mounted ceramic capacitor of 0.1 to 2.2 μF capacitance with an appropriate voltage rating.

It is important to verify what type of ceramic capacitor is selected for these bypass capacitors. Low impedance means low-ESR/ESL impedance within the frequency bandwidth range of ultrasound pulses transmitted, including the very fast dV/dt of the pulse's rising and falling edges. A capacitor with low-temperature coefficient and low-voltage coefficient is also recommended. The type of X7R and X5R or other more advanced multilayer-ceramic types should be selected for these purposes.

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NOTES:

Appendix A. Schematics and Layouts

A.1 INTRODUCTION

This appendix contains the following schematics and layouts for the HV2925 Analog Switch Evaluation Board (EV65S53A) and the HV MUX Controller Board (EV34G23A).

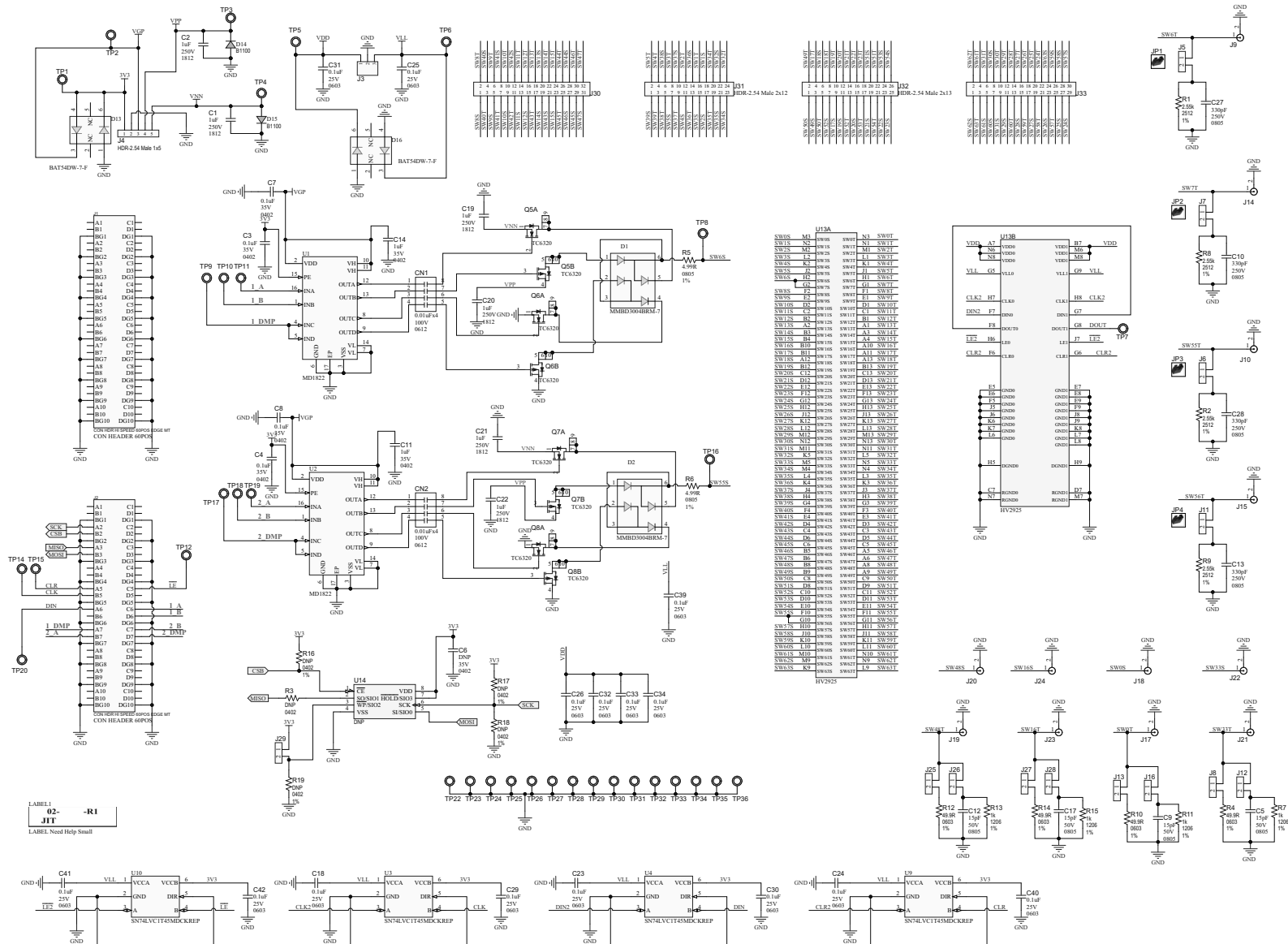
1. HV2925 Analog Switch Evaluation Board (EV65S53A):

- [EV65S53A – Schematic](#)
- [EV65S53A – Top Silk](#)
- [EV65S53A – Top Copper and Silk](#)
- [EV65S53A – Top Copper](#)
- [EV65S53A – Inner 1](#)
- [EV65S53A – Inner 2](#)
- [EV65S53A – Inner 3](#)
- [EV65S53A – Inner 4](#)
- [EV65S53A – Bottom Copper](#)
- [EV65S53A – Bottom Copper and Silk](#)
- [EV65S53A – Bottom Silk](#)

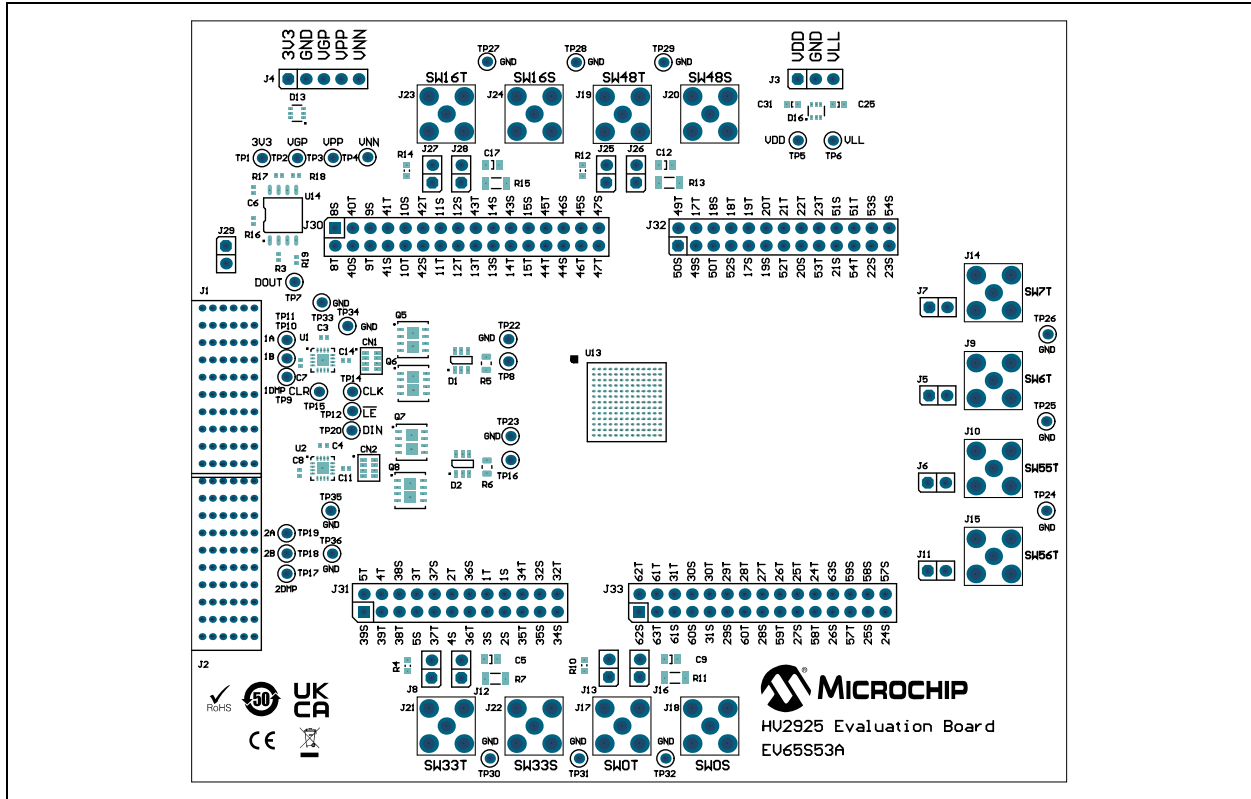
2. HV MUX Controller Board (EV34G23A):

- [EV34G23A – Schematic \(Connection\)](#)
- [EV34G23A – Schematic \(Power Supply\)](#)
- [EV34G23A – Schematic \(USB to SPI\)](#)
- [EV34G23A – Schematic \(Programmable Clock\)](#)
- [EV34G23A – Schematic \(FPGA\)](#)
- [EV34G23A – Schematic \(FPGA Decoupling Capacitors\)](#)
- [EV34G23A – Schematic \(Connectors\)](#)
- [EV34G23A – Top Silk](#)
- [EV34G23A – Top Copper and Silk](#)
- [EV34G23A – Top Copper](#)
- [EV34G23A – Inner 1](#)
- [EV34G23A – Inner 2](#)
- [EV34G23A – Inner 3](#)
- [EV34G23A – Inner 4](#)
- [EV34G23A – Bottom Copper](#)
- [EV34G23A – Bottom Copper and Silk](#)
- [EV34G23A – Bottom Silk](#)

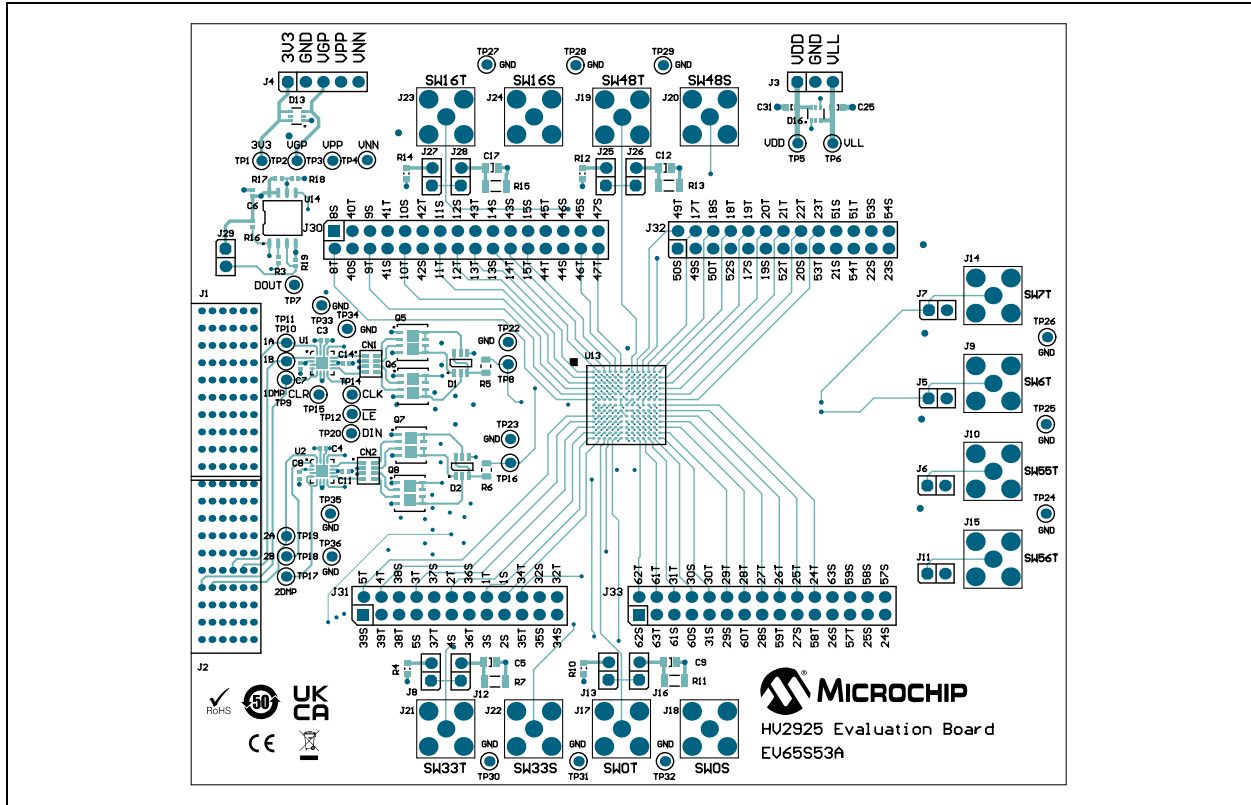
A.2 EV65S53A – SCHEMATIC



A.3 EV65S53A – TOP SILK

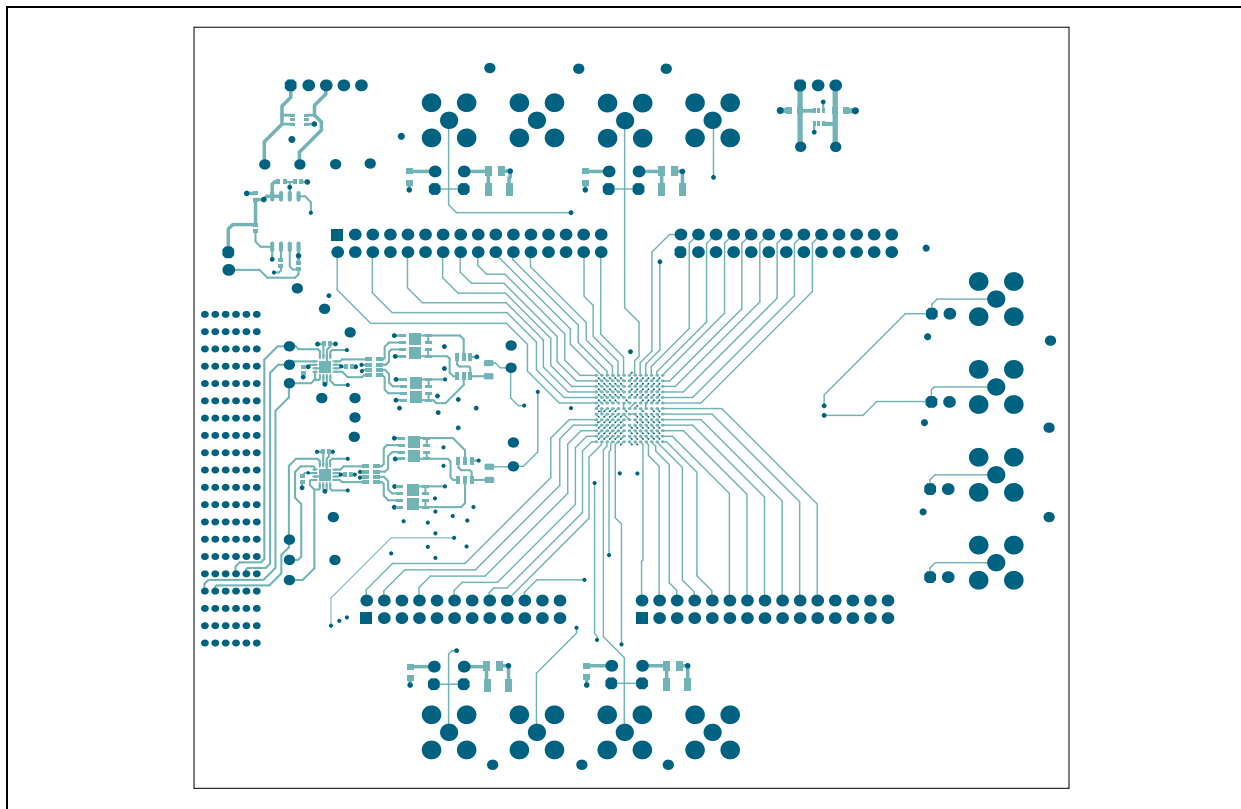


A.4 EV65S53A – TOP COPPER AND SILK

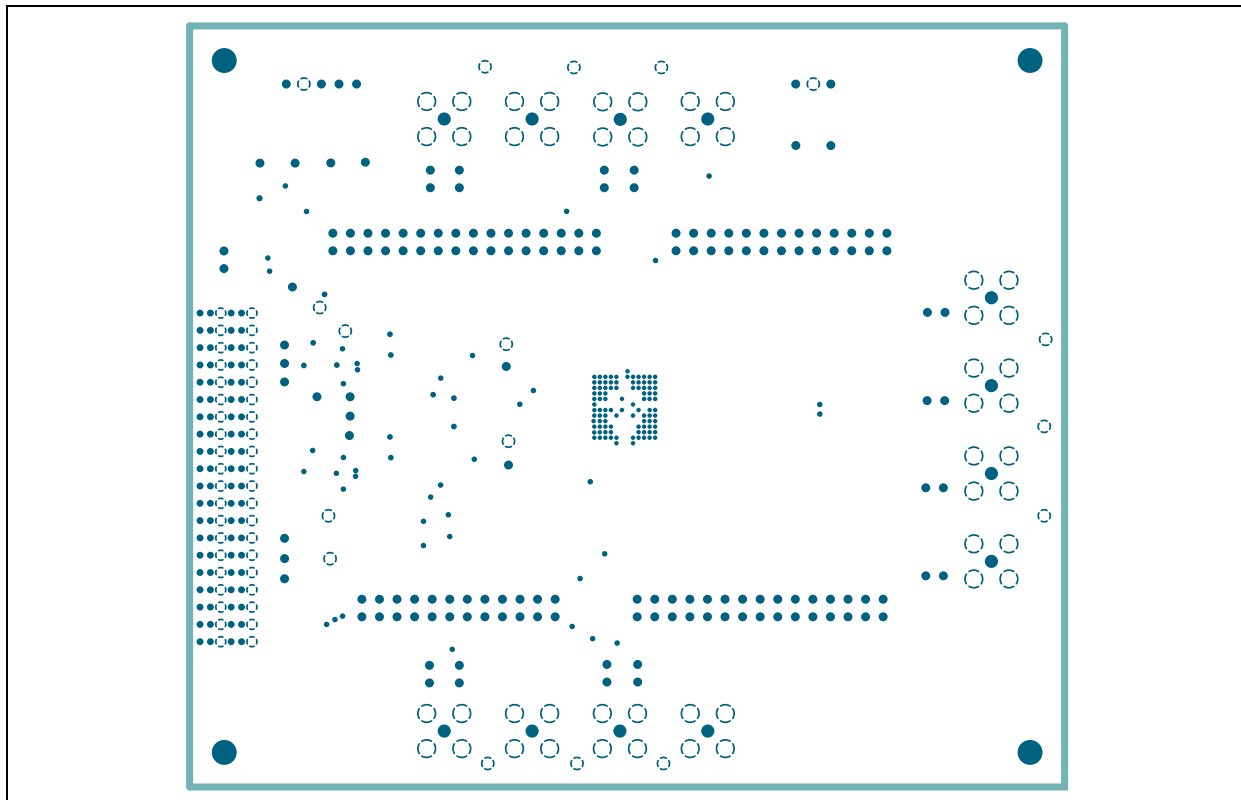


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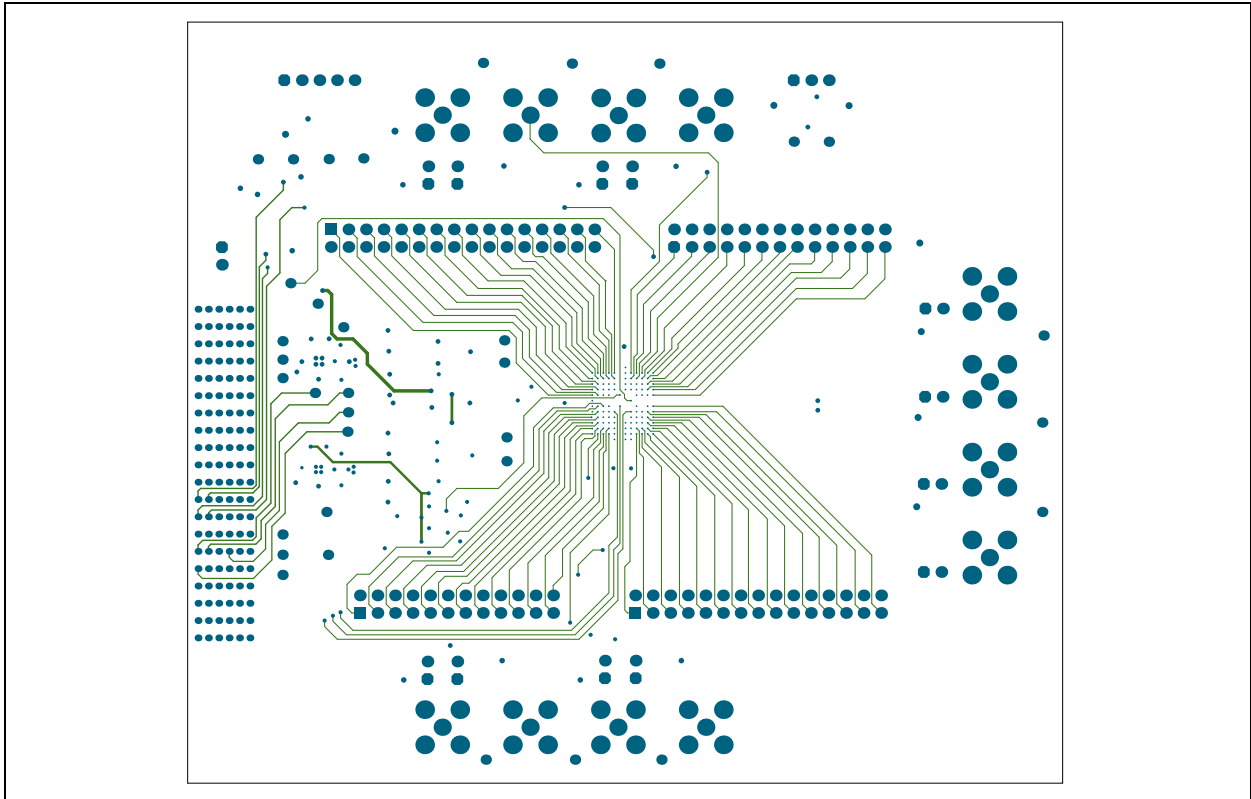
A.5 EV65S53A – TOP COPPER



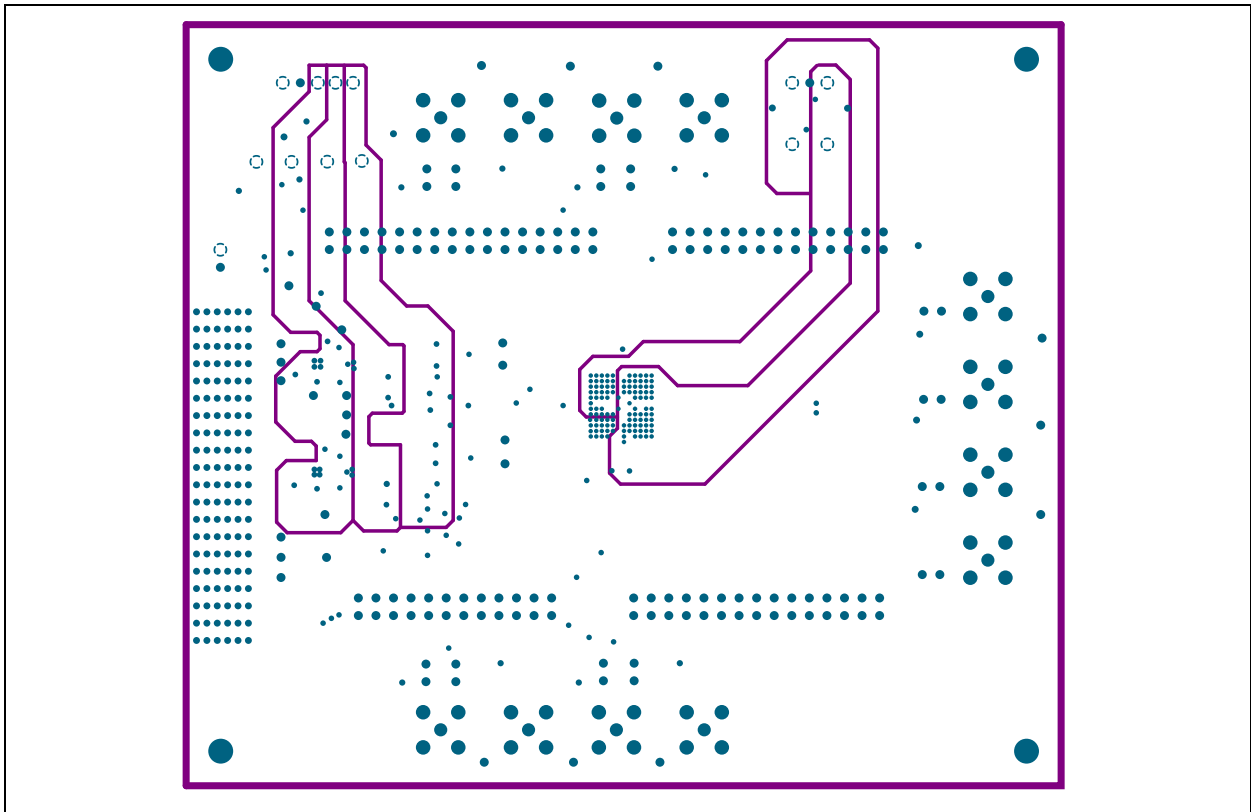
A.6 EV65S53A – INNER 1



A.7 EV65S53A – INNER 2

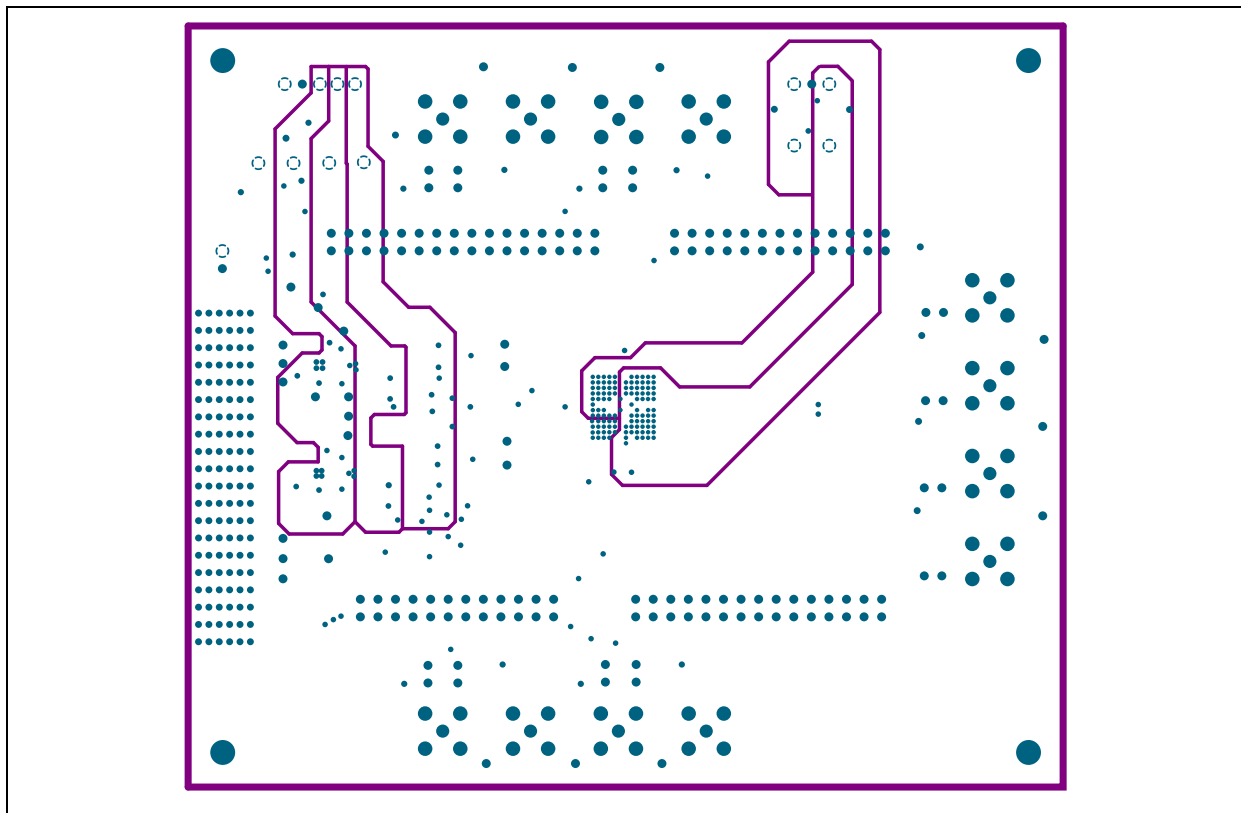


A.8 EV65S53A – INNER 3

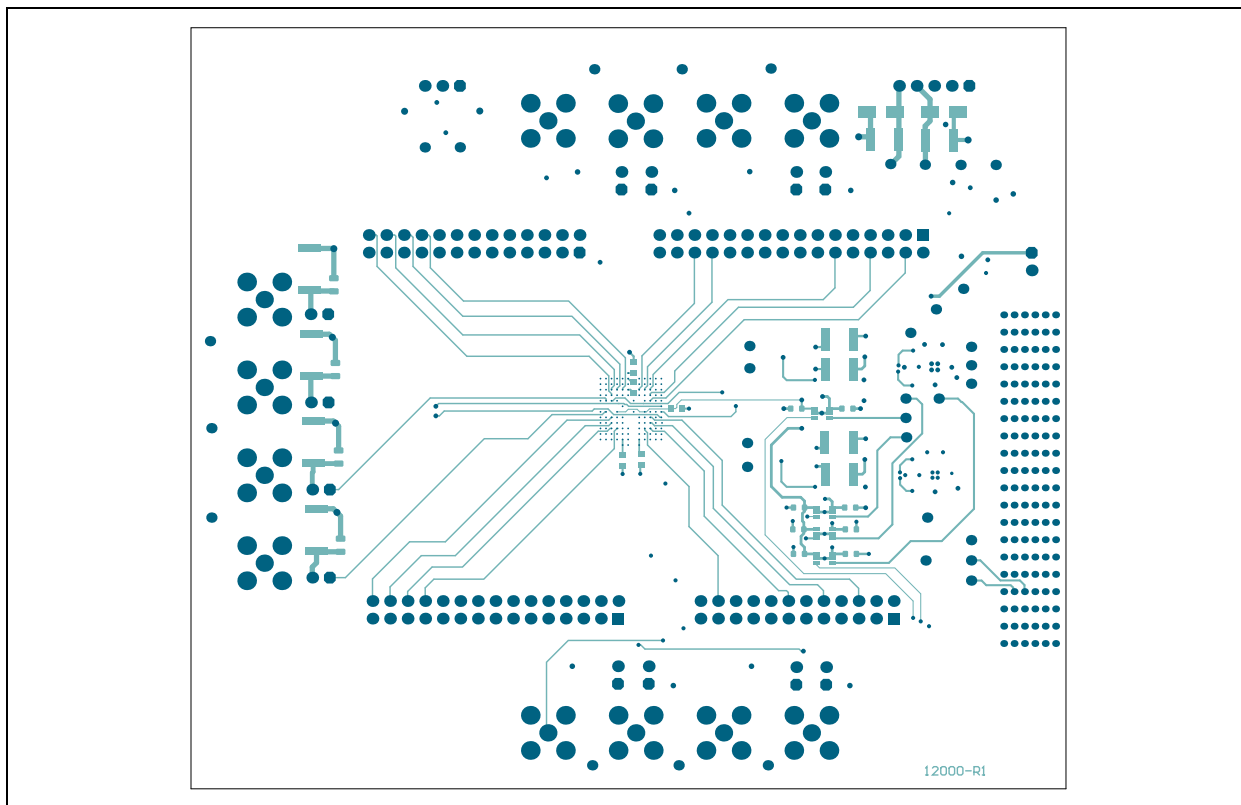


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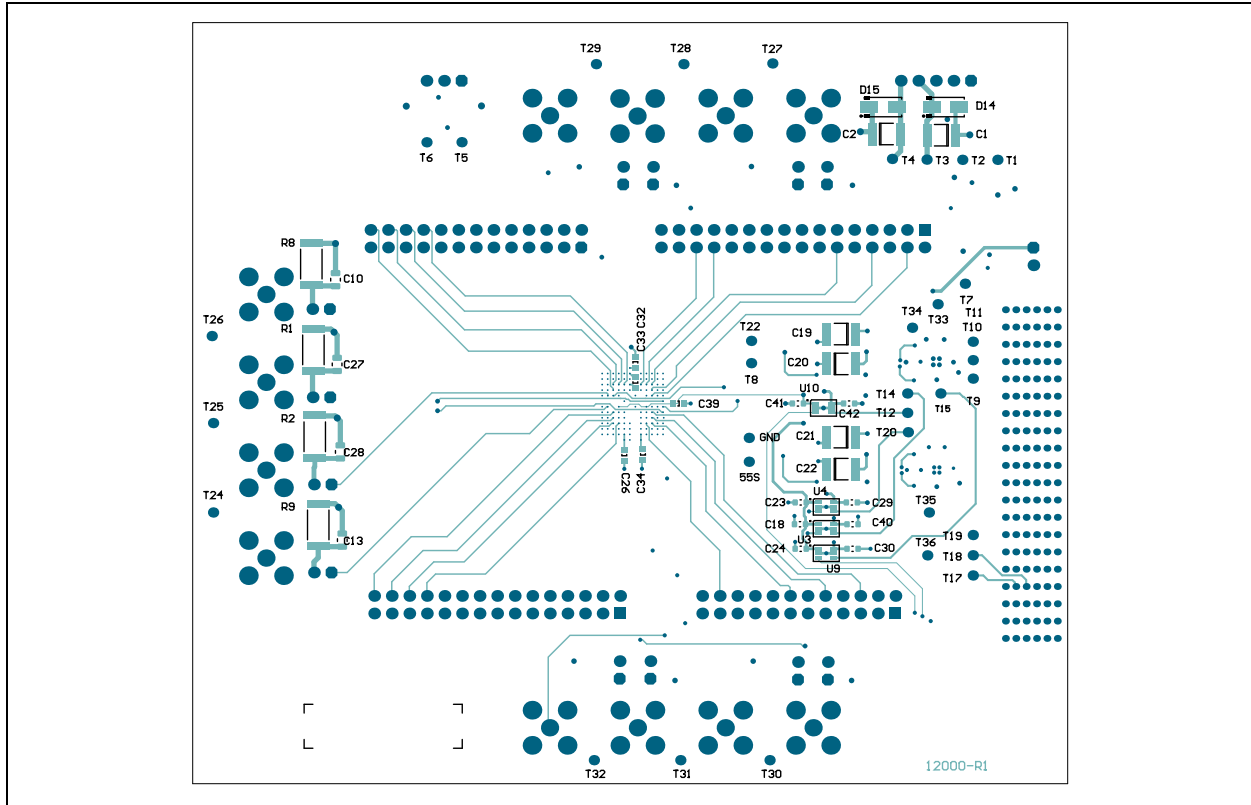
A.9 EV65S53A – INNER 4



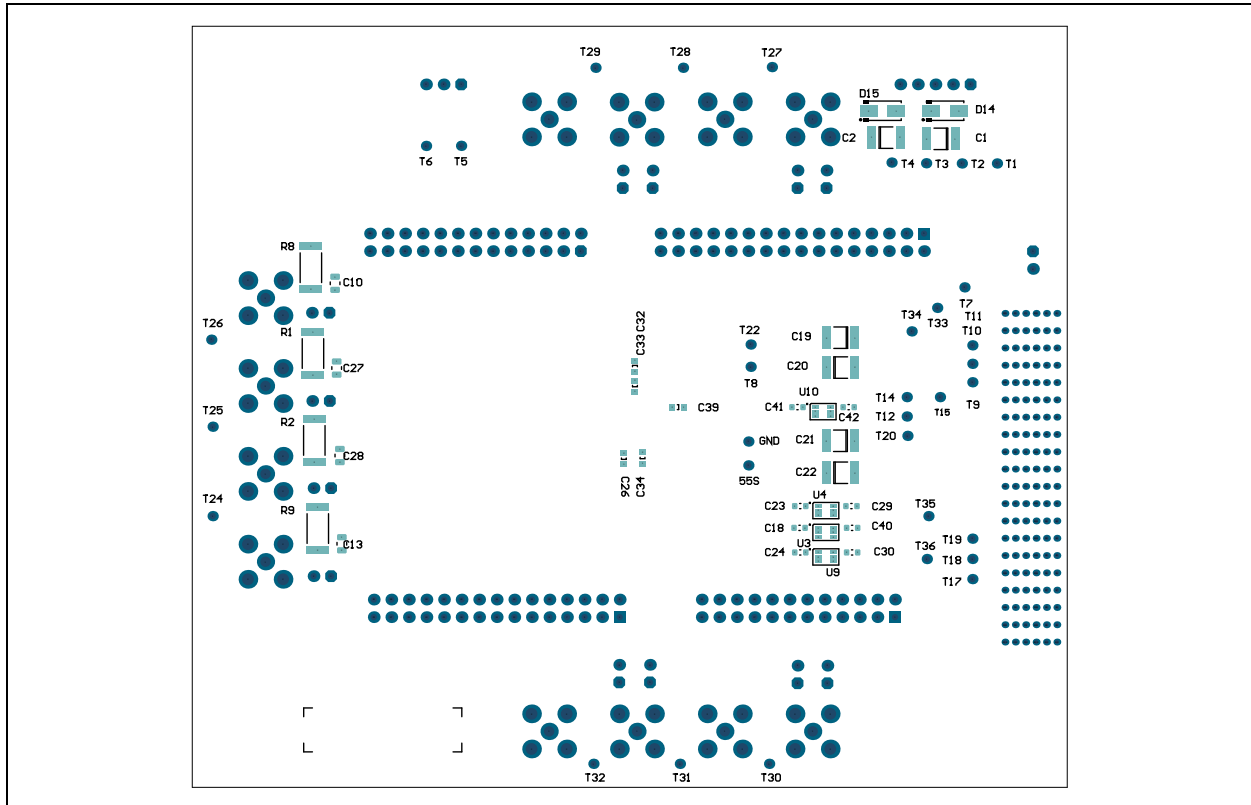
A.10 EV65S53A – BOTTOM COPPER



A.11 EV65S53A – BOTTOM COPPER AND SILK



A.12 EV65S53A – BOTTOM SILK

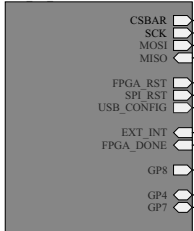


A.13 EV34G23A – SCHEMATIC (CONNECTION)

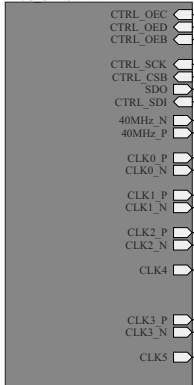
MUPB001_PWR_SchDoc



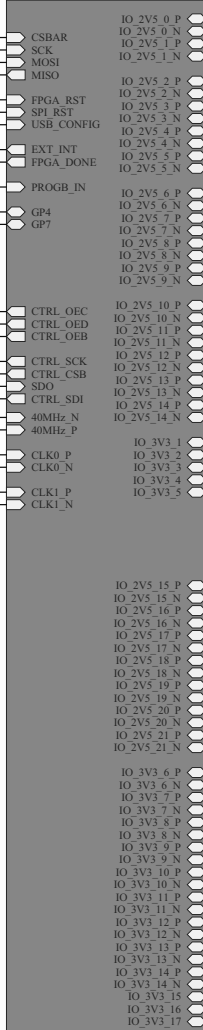
USB TO SPI_SchDoc



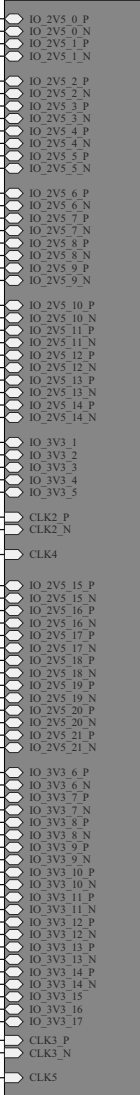
PROG_CLK_SchDoc

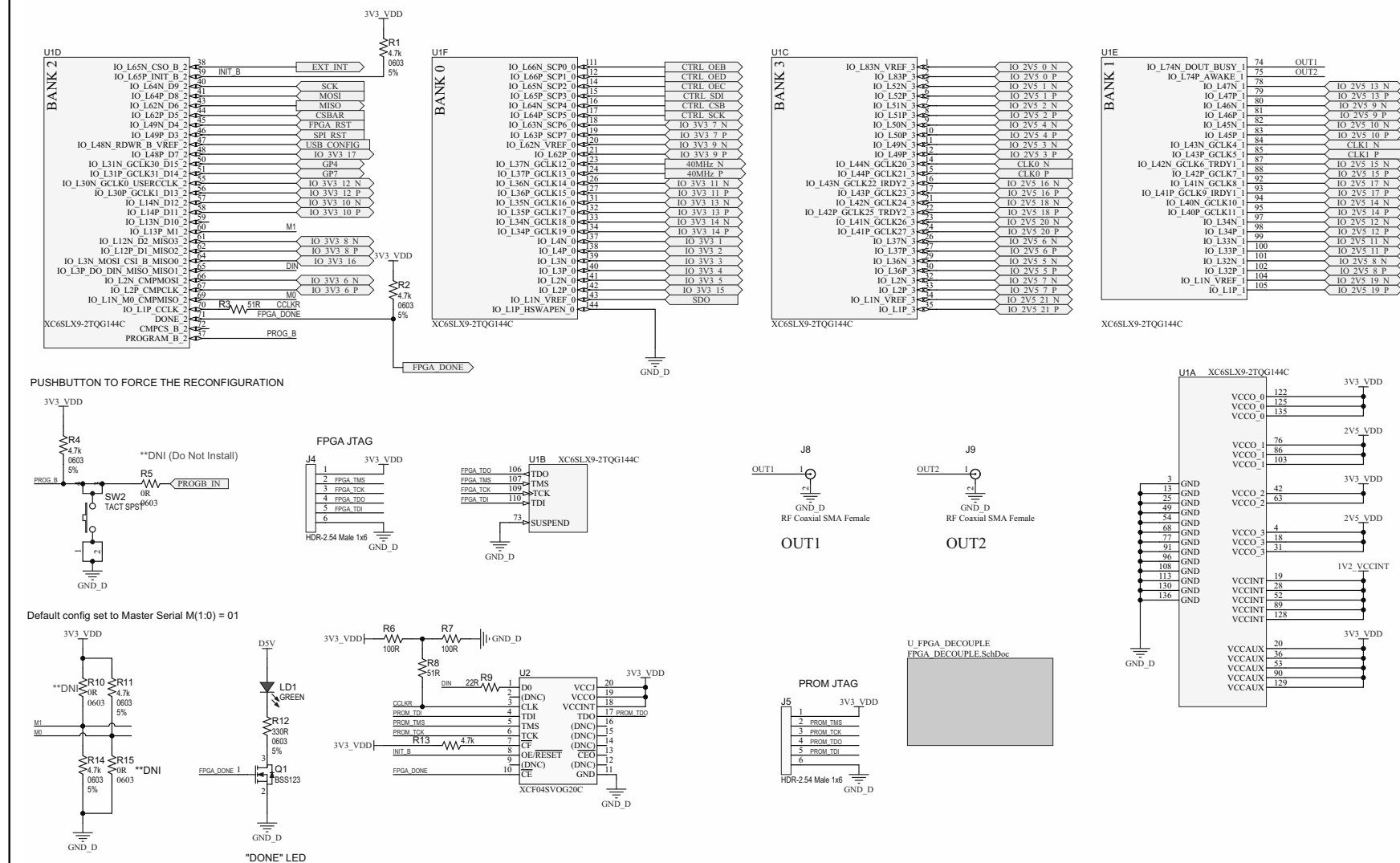


FPGA01_SchDoc

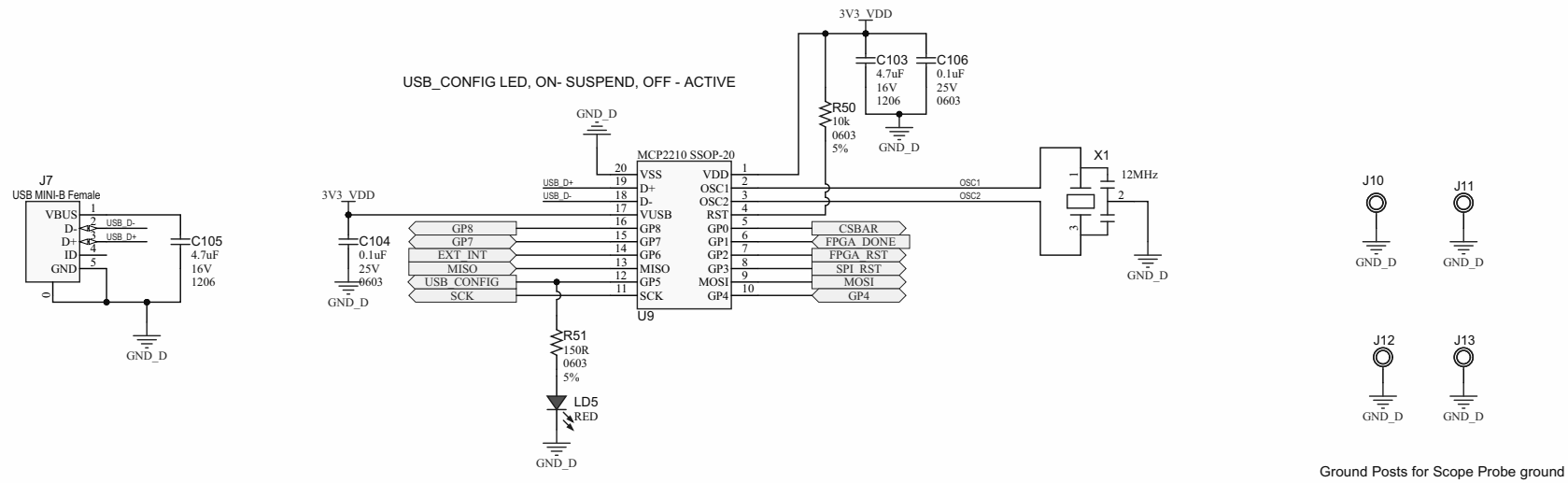


Connector_SchDoc

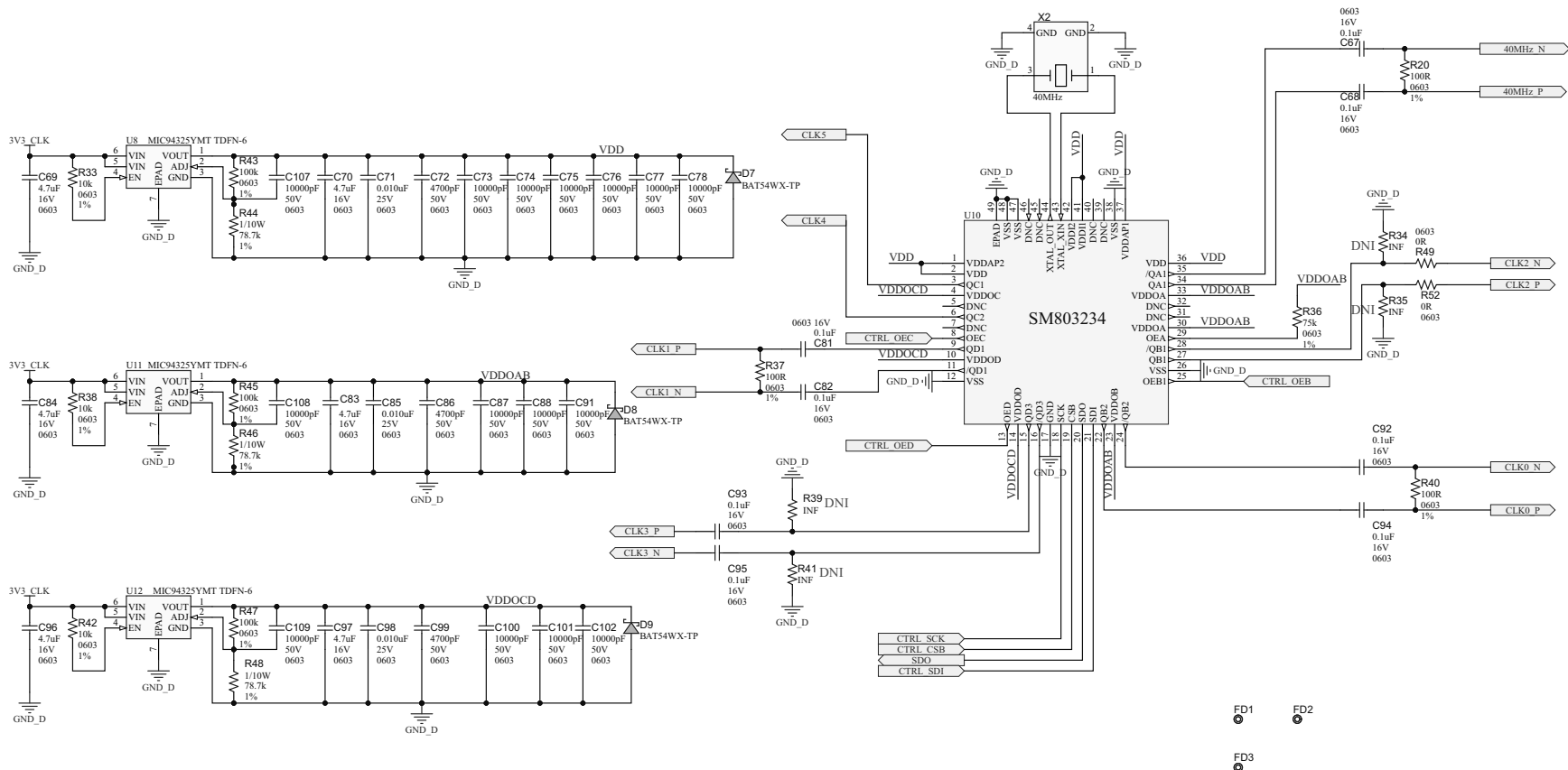




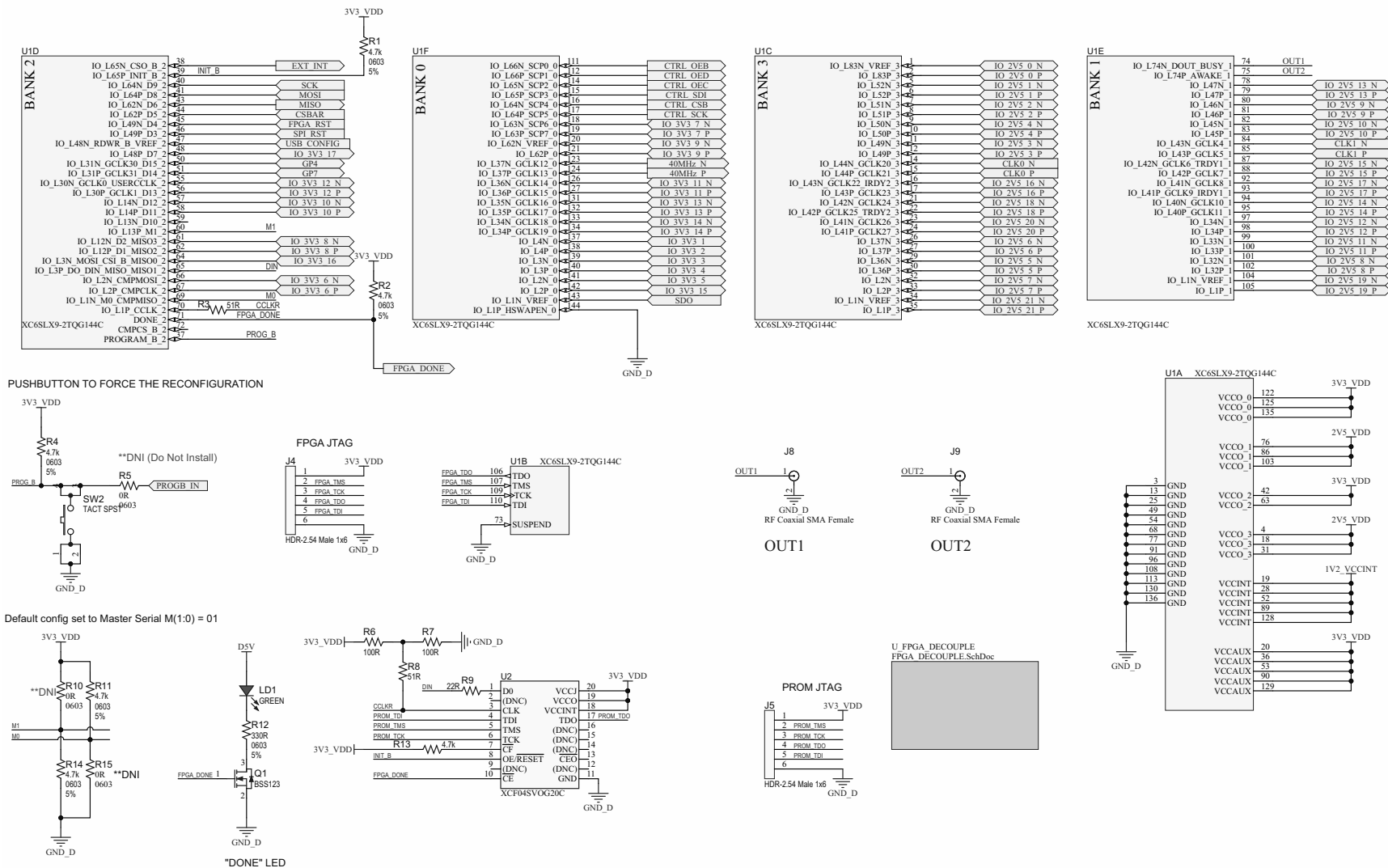
A.15 EV34G23A – SCHEMATIC (USB TO SPI)



A.16 EV34G23A – SCHEMATIC (PROGRAMMABLE CLOCK)

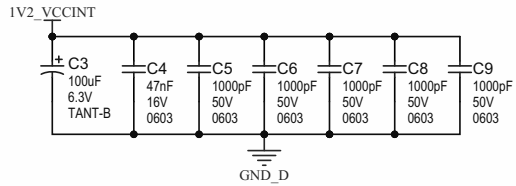


A.17 EV34G23A – SCHEMATIC (FPGA)

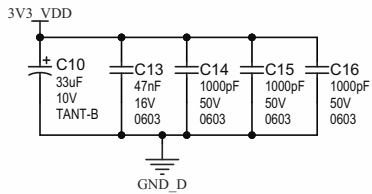


A.18 EV34G23A – SCHEMATIC (FPGA DECOUPLING CAPACITORS)

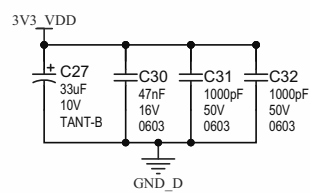
For 1V2_VCCINT



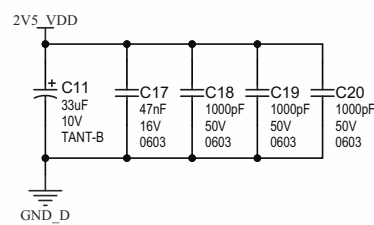
For VCCO_0



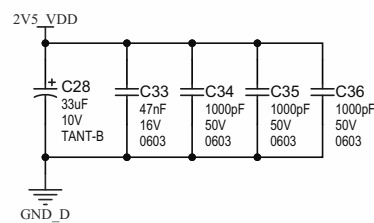
For VCCO_2



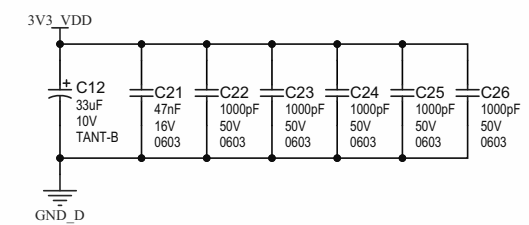
For VCCO_1



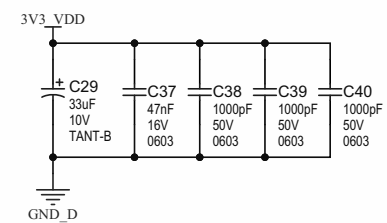
For VCCO_3



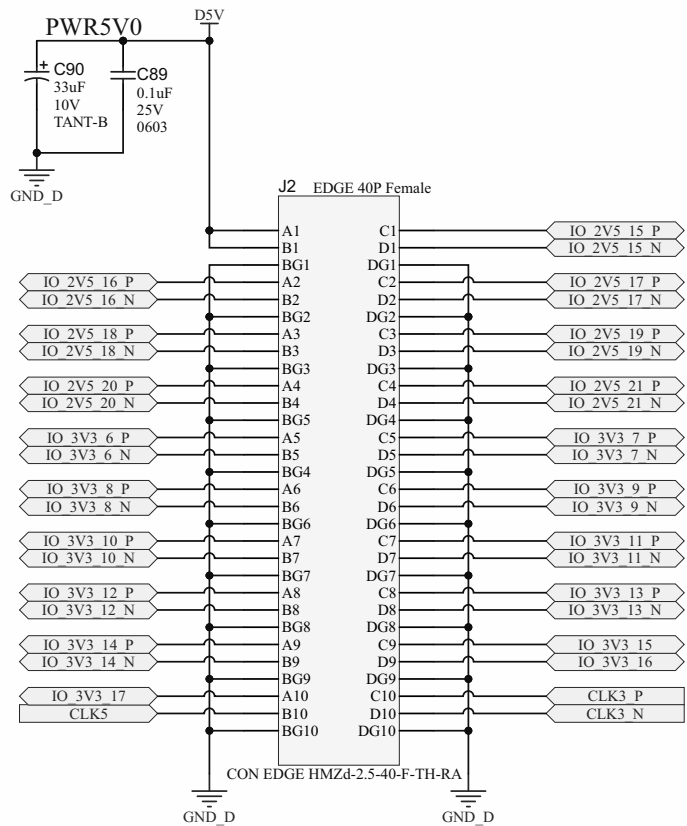
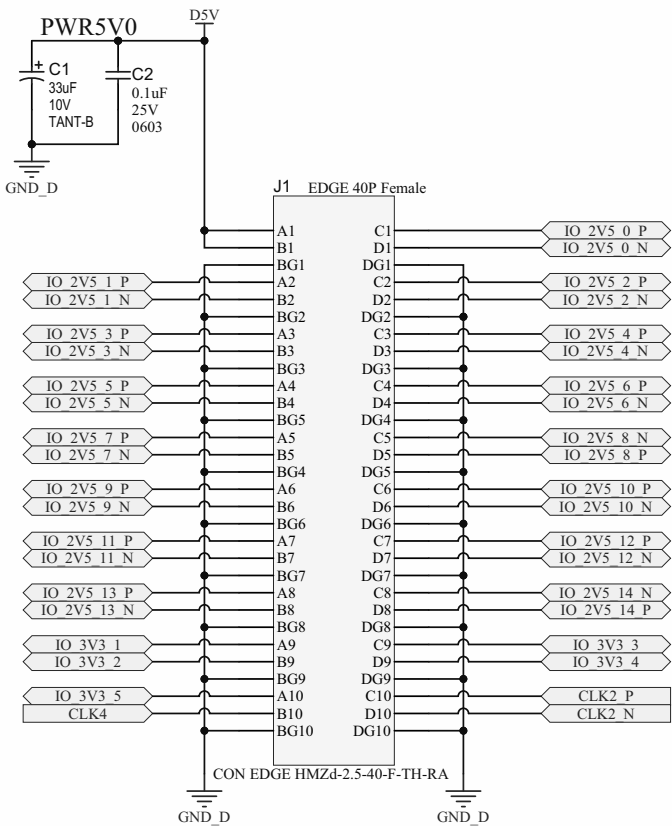
For VCCAUX



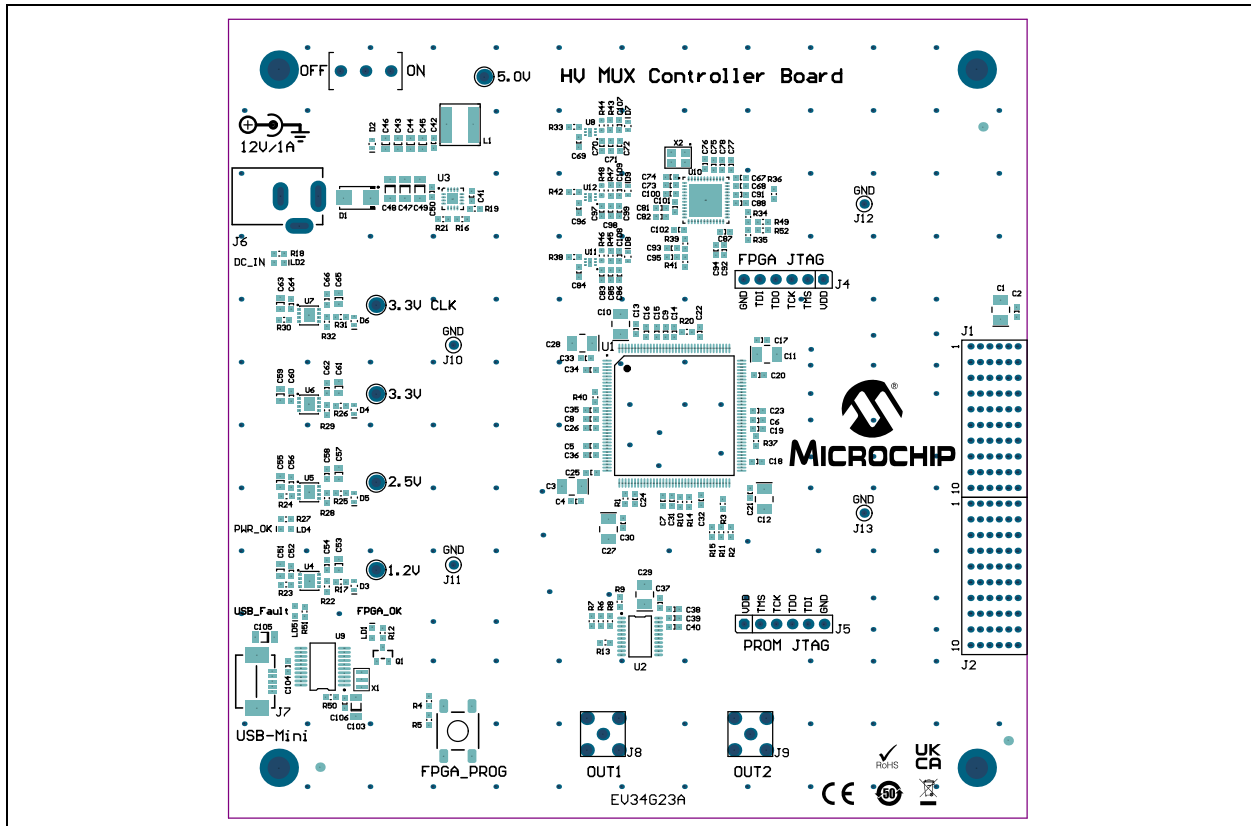
For XCF04S



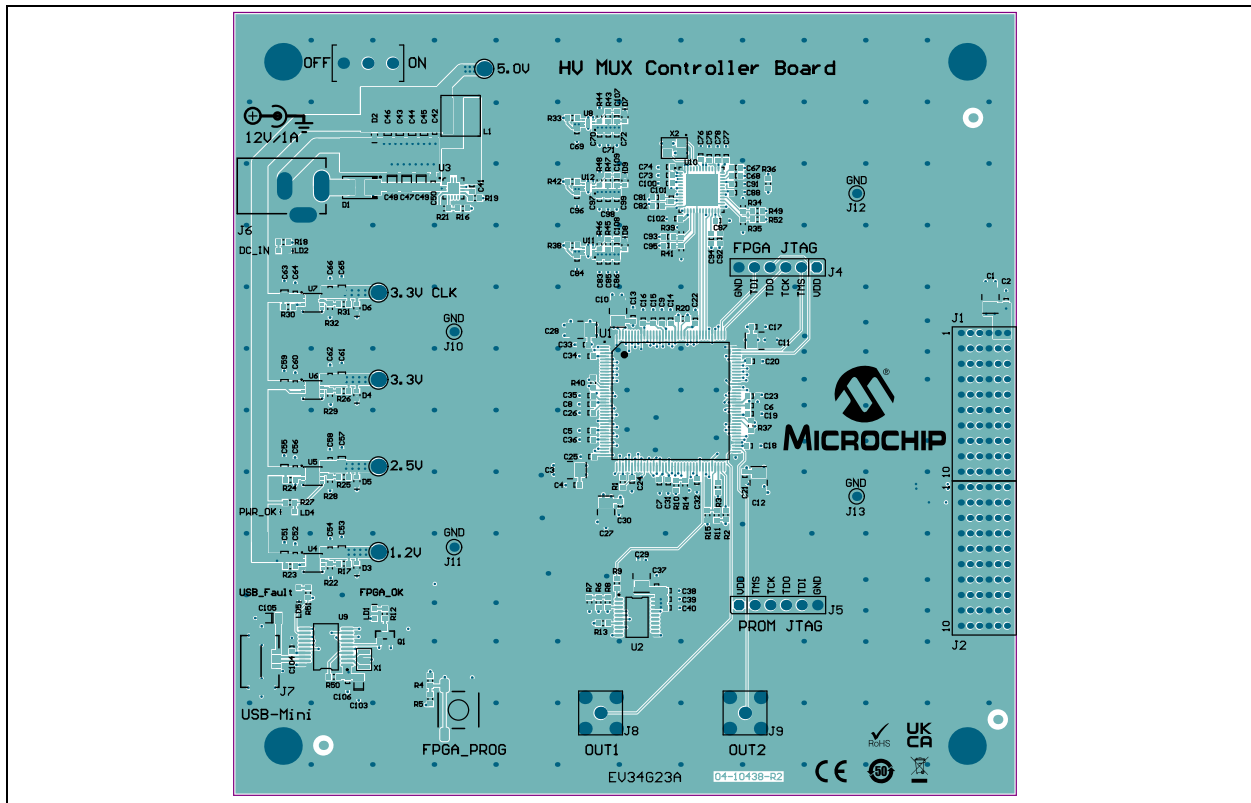
A.19 EV34G23A – SCHEMATIC (CONNECTORS)



A.20 EV34G23A – TOP SILK

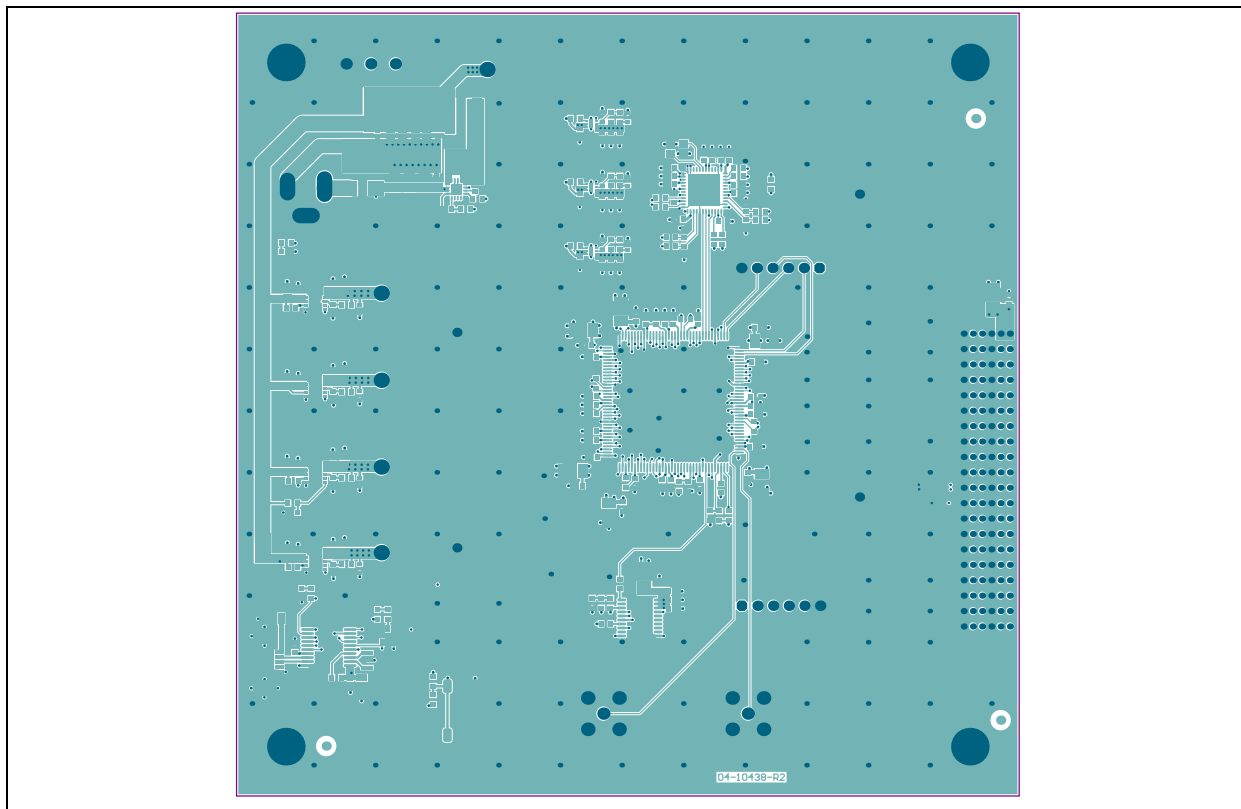


A.21 EV34G23A – TOP COPPER AND SILK

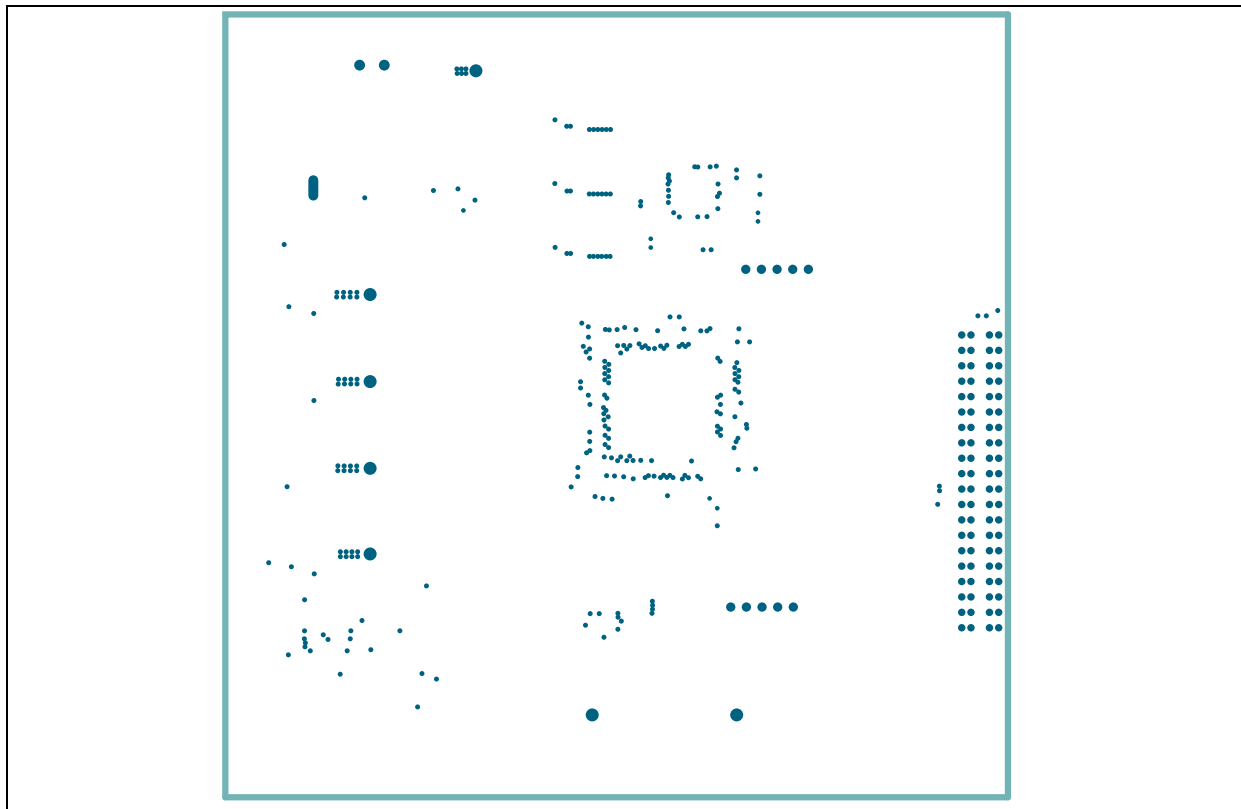


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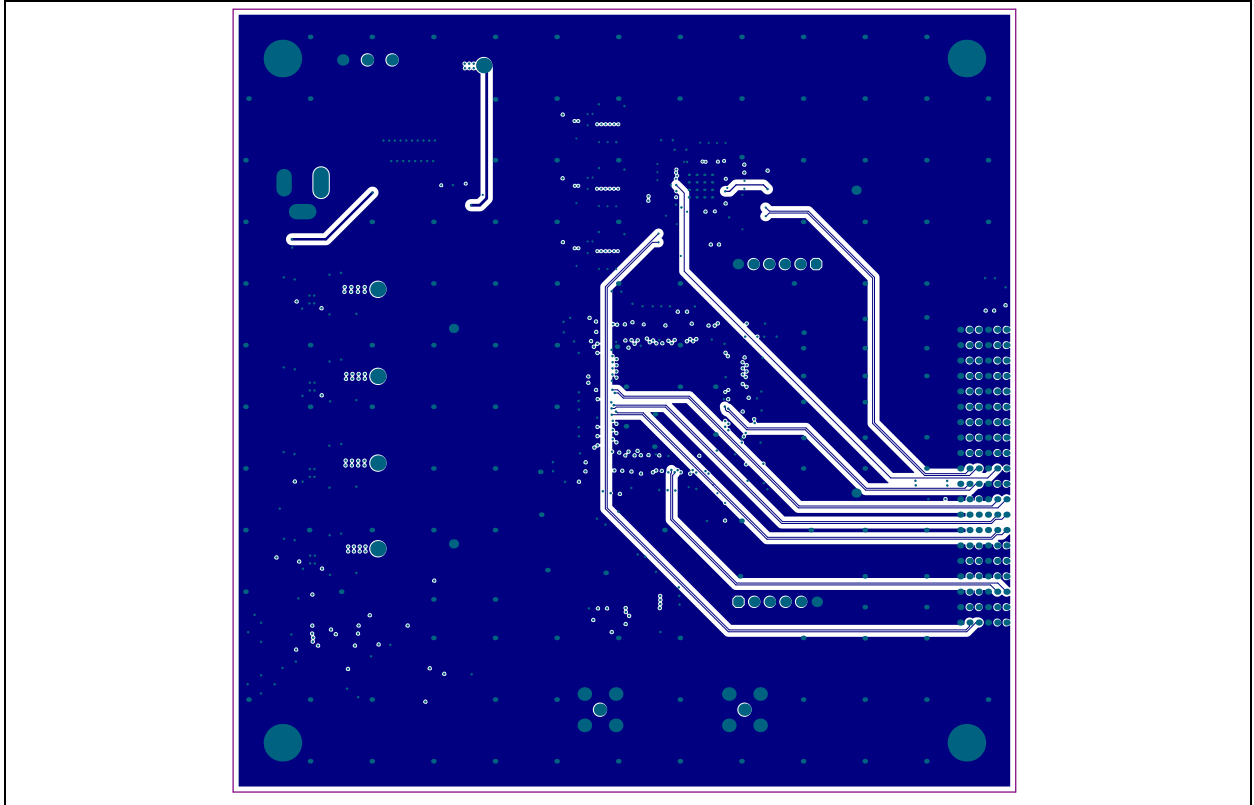
A.22 EV34G23A – TOP COPPER



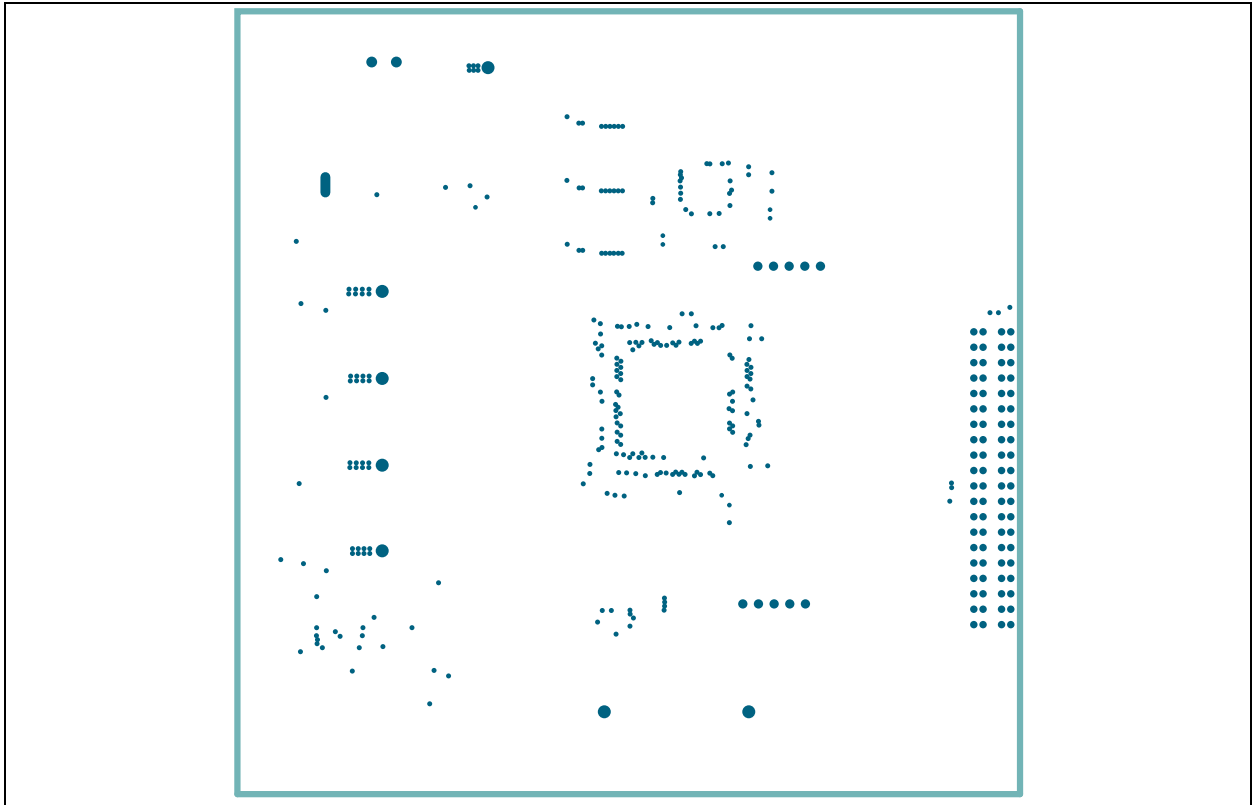
A.23 EV34G23A – INNER 1



A.24 EV34G23A – INNER 2

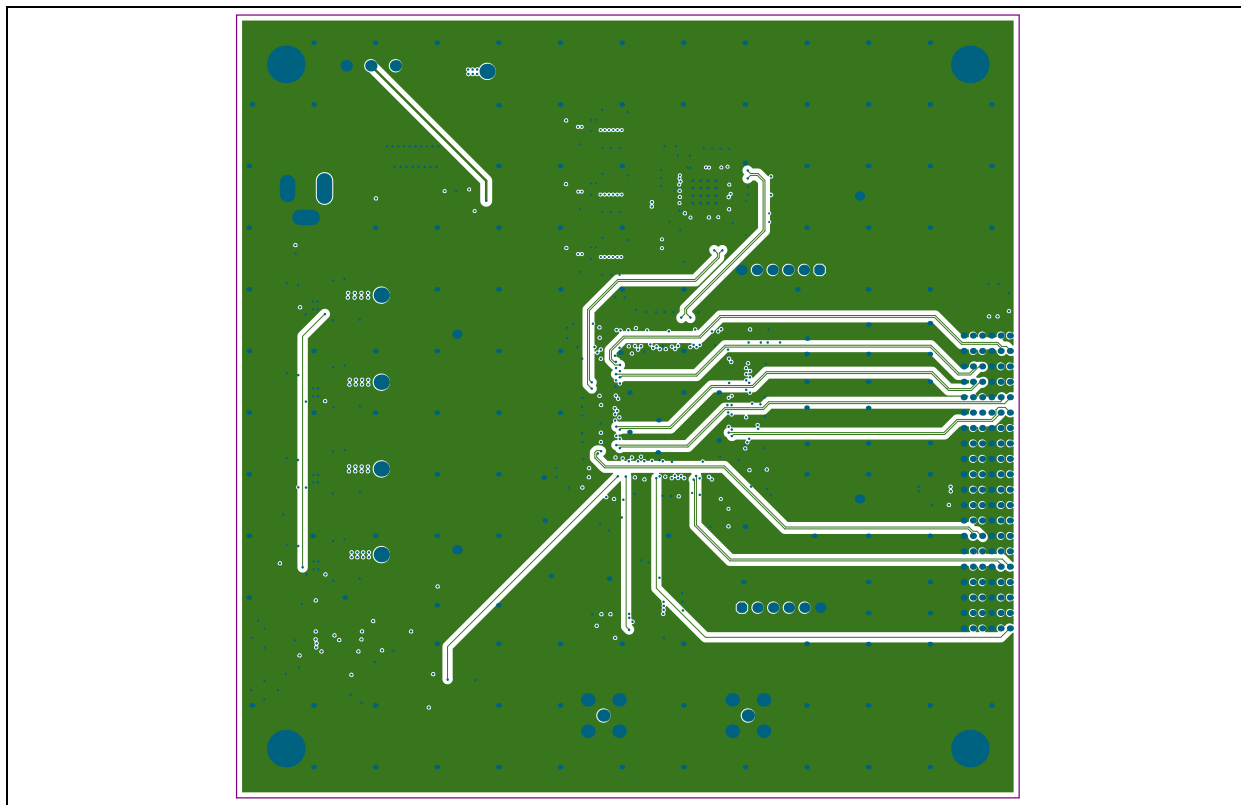


A.25 EV34G23A – INNER 3

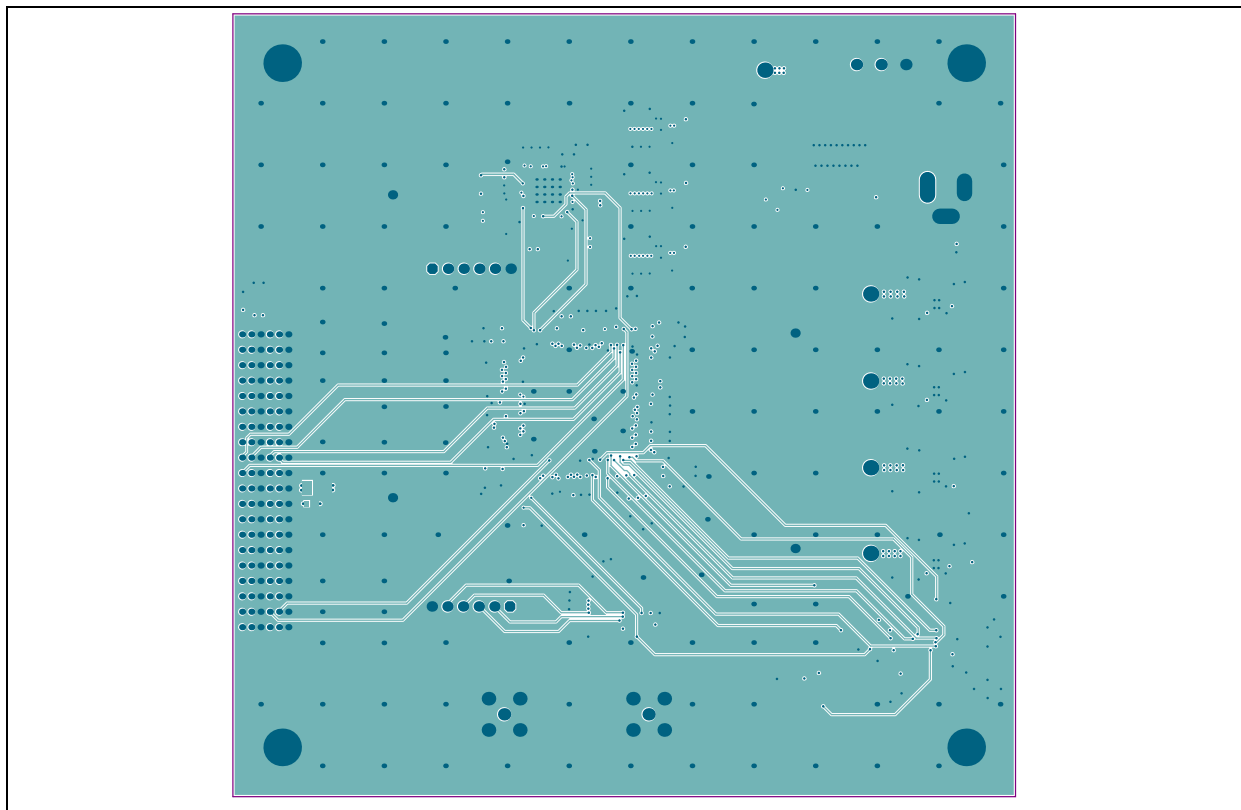


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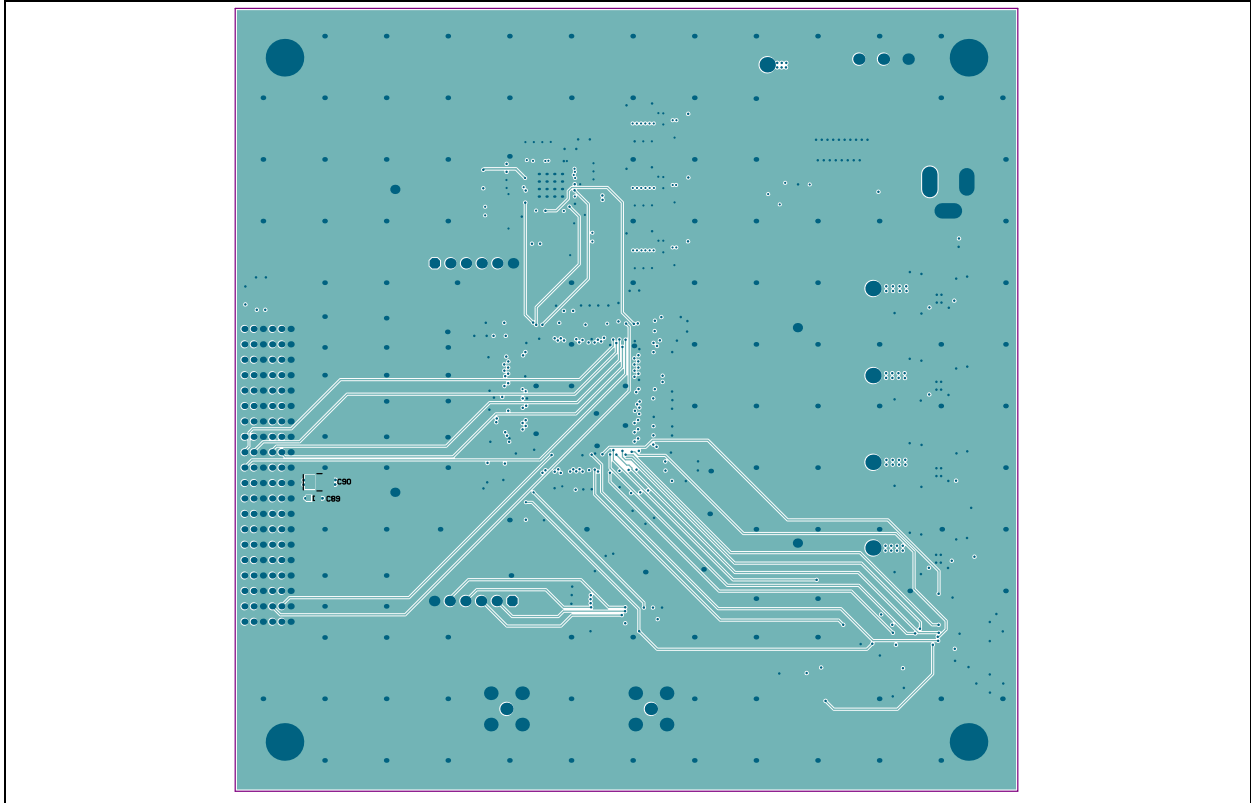
A.26 EV34G23A – INNER 4



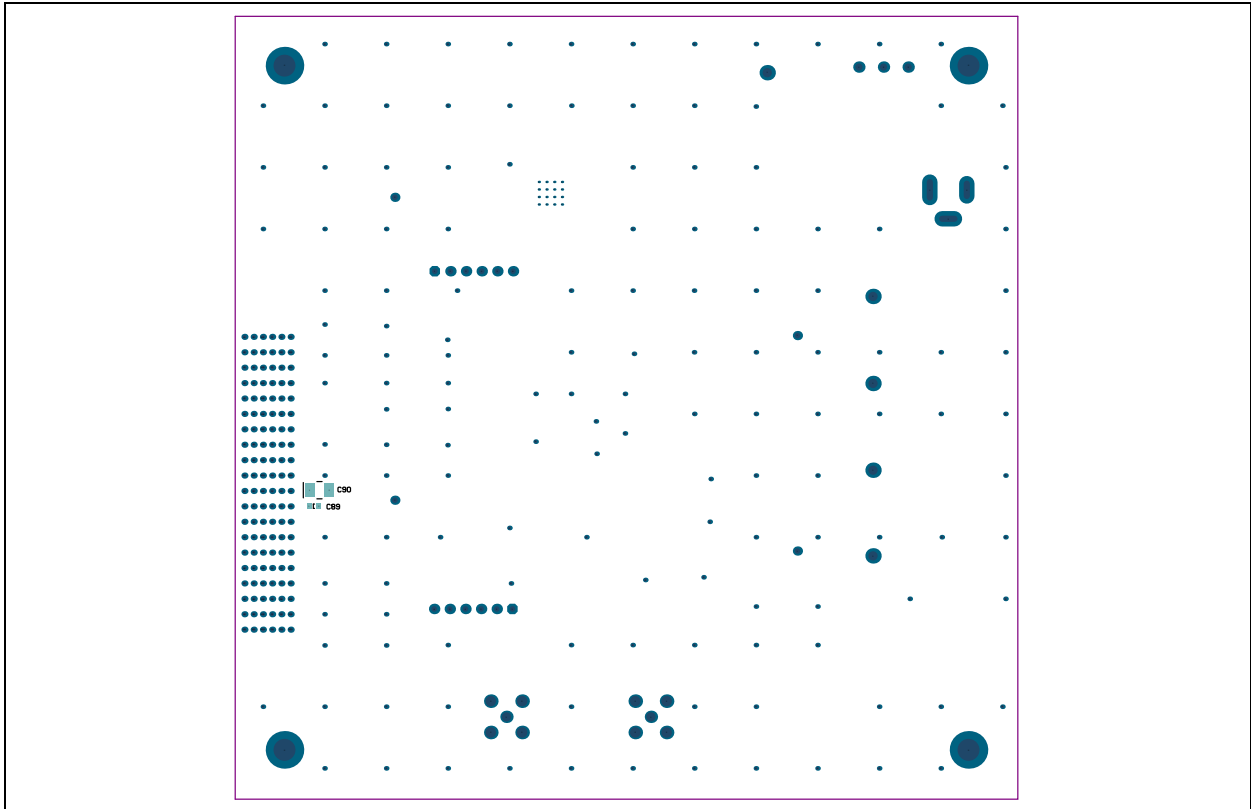
A.27 EV34G23A – BOTTOM COPPER



A.28 EV34G23A – BOTTOM COPPER AND SILK



A.29 EV34G23A – BOTTOM SILK



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NOTES:

Appendix B. Bill of Materials (BOM)

B.1 HV2925 ANALOG SWITCH EVALUATION BOARD – BOM

TABLE B-1: HV2925 ANALOG SWITCH EVALUATION BOARD – BILL OF MATERIALS

Qty.	Reference	Description	Manufacturer	Part Number
6	C1, C2, C19, C20, C21, C22	Capacitor, Ceramic, 1 μ F, 250V, 20%, X7T, SMD, 1812	TDK Corporation	C4532X7T2E105M250KA
4	C3, C4, C7, C8	Capacitor, Ceramic, 0.1 μ F, 35V, 10%, X7R, SMD, 0402	TDK Corporation	CGA2B3X7R1V104K050BB
4	C5, C9, C12, C17	Capacitor, Ceramic, 15 pF, 50V, 5%, C0G, SMD, 0805	AVX Corporation	08055A150JAT2A
1	C6	Capacitor, Ceramic, 0.1 μ F, 35V, 10%, X7R, SMD, 0402 – DO NOT POPULATE	TDK Corporation	CGA2B3X7R1V104K050BB
4	C10, C13, C27, C28	Capacitor, Ceramic, 330 pF, 250V, 5%, C0G/NP0, SMD, 0805	Murata Electronics® North America, Inc.	GRM21A5C2E331JW01D
2	C11, C14	Capacitor, Ceramic, 1 μ F, 35V, 10%, X5R, SMD, 0402	Murata Electronics® North America, Inc.	GRM155R6YA105KE11D
15	C18, C23, C24, C25, C26, C29, C30, C31, C32, C33, C34, C39, C40, C41, C42	Capacitor, Ceramic, 0.1 μ F, 25V, 20%, X7R, SMD, 0603	KEMET	C0603C104M3RACTU
2	CN1, CN2	Capacitor, Array, 0.01 μ Fx4 100V, 20%, X7R, SMD, 0612	Kyocera AVX	W3A41C103MAT2A
2	D1, D2	Diode, Rectifier, Array, 1V, 225 mA, 350V, SMD, SOT-23-6	Diodes Incorporated®	MMBD3004BRM-7-F
2	D13, D16	Diode, Schottky, Array, 1V, 200 mA, 30V, SMD, SOT363	Diodes Incorporated®	BAT54DW-7-F
2	D14, D15	Diode, Schottky, 790 mV 1A, 70V, DO-214AC_SMA	Diodes Incorporated®	B1100-13-F
2	J1, J2	Connector, Header-0.098, Male, 4x10+20GND, Press-Fit, TH, R/A	TE Connectivity, Ltd. AMP Connectors	6469169-1
1	J3	Connector, Header-2.54, Male, 1x3, Tin, 5.84 MH, TH, Vertical	Samtec, Inc.	TSW-103-07-T-S
1	J4	Connector, Header-2.54, Male, 1x5, Gold, 5.84 MH, TH, Vertical	Samtec, Inc.	TSW-105-07-S-S
12	J5, J6, J7, J8, J11, J12, J13, J16, J25, J26, J27, J28	Connector, Header-2.54, Male, 1x2, Gold, 5.84 MH, TH, Vertical	FCI	77311-118-02LF
12	J9, J10, J14, J15, J17, J18, J19, J20, J21, J22, J23, J24	Connector, RF, Coaxial, SMA, Female, 2P, TH, Vertical	Adam Equipment	RF2-04A-T-00-50-G

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

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TABLE B-1: HV2925 ANALOG SWITCH EVALUATION BOARD – BILL OF MATERIALS

Qty.	Reference	Description	Manufacturer	Part Number
1	J29	Connector, Header-2.54, Male, 1x2, Gold, 5.84 MH, TH, Vertical – DO NOT POPULATE	FCI	77311-118-02LF
1	J30	Connector, Header-2.54 Male 2x16 GOLD 5.84MH TH	Sullins Connector Solutions	PRPC016DFBN-RC
1	J31	Connector, Header-2.54 Male 2x12 Gold 5.84MH TH Vertical	Nextron	213-241AE-0021-400
1	J32	Connector, Header-2.54 Male 2x13 Gold 5.84MH TH Vertical	Samtec, Inc.	TSW-113-07-F-D
1	J33	30 Positions Header, Unshrouded, Breakaway Connector 0.100" (2.54mm) Through Hole Gold	Wurth Electronics Inc.	61303021121
4	JP1, JP2, JP3, JP4	Mechanical HW Jumper 2.54 mm 1x2	FCI	63429-202LF
1	LABEL1	Label, Assembly w/Rev Level (Small Modules) per MTS-0002		
1	PCB1	HV2925 Analog Switch Evaluation Board – Printed Circuit Board	Microchip Technology Inc.	12000-R1
4	Q5, Q6, Q7, Q8	Analog MOSFET Dual, P-N-CH, 200V, 2A, DFN-8	Microchip Technology Inc	TC6320K6-G
4	R1, R2, R8, R9	Resistor, Thick Film, 2.55k, 1%, 1W, SMD, 2512	Digikey	PT2.55KAFCT-ND
1	R3	Resistor, Thick Film, 0R 1/16W, SMD, 0402 – DO NOT POPULATE	Yageo	RC0402JR-070RL
4	R4, R10, R12, R14	Resistor, Thick Film, 49.9R, 1%, 1/4W, SMD, 0603	Vishay/Dale	CRCW060349R9FKEAHP
2	R5, R6	Resistor, Thick Film, 4.99R, 1%, 1/2W, SMD, 0805, AEC-Q200	Vishay/Dale	CRCW08054R99FKEAHP
4	R7, R11, R13, R15	Resistor, Thick Film, 1k, 1%, 1/4W, SMD, 1206	Yageo Corporation	RC1206FR-071KL
2	R16, R19	Resistor, Thick Film, 4.7K, 1%, 1/10W, 0402 – DO NOT POPULATE	KOA Speer Electronics, Inc.	RK73H1ETTP4701F
2	R17, R18	Resistor, Thick Film, 100R, 1%, 1/10W, SMD, 0402 – DO NOT POPULATE	Panasonic® - BSG	ERJ-2RKF1000X
34	TP1, TP2, TP3, TP4, TP5, TP6, TP7, TP8, TP9, TP10, TP11, TP12, TP14, TP15, TP16, TP17, TP18, TP19, TP20, TP22, TP23, TP24, TP25, TP26, TP27, TP28, TP29, TP30, TP31, TP32, TP33, TP34, TP35, TP36	Connector, TP, Pad, MILL, MAX, 3132, PCB, 1.5x0.9, TH, AU	Mill-Max Mfg. Corporation	3132-0-00-15-00-00-08-0
2	U1, U2	Analog FET Driver, Quad-Two Inverting-Two Non Inverting, 16-Lead QFN	Microchip Technology Inc.	MD1822K6-G
4	U3, U4, U9, U10	IC Special Voltage Level Translator Bidirectional SC70-6	Texas Instruments	SN74LVC1T45MDCKREP

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

Bill of Materials (BOM)

TABLE B-1: HV2925 ANALOG SWITCH EVALUATION BOARD – BILL OF MATERIALS

Qty.	Reference	Description	Manufacturer	Part Number
1	U13	Analog Switch, HV2925 HV 100V BGA-169	Microchip Technology Inc.	HV2925/D8C
1	U14	Memory Serial Flash 16M 104MHz SOIJ-8 – DO NOT POPULATE	Microchip Technology Inc.	SST26VF016B-104I/SM

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

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B.2 HV MUX CONTROLLER BOARD – BOM

TABLE B-2: HV MUX CONTROLLER BOARD – BILL OF MATERIALS

Qty.	Reference	Description	Manufacturer	Part Number
8	C1, C10, C11, C12, C27, C28, C29, C90	Capacitor, Tantalum, 33 μ F, 10V, 10%, 1.4 Ω , Surface Mount B	KEMET	T494B336K010AT
4	C2, C89, C104, C106	Capacitor, Ceramic, 0.1 μ F, 25V, 10%, X7R, Surface Mount, 0603	Murata® Manufacturing Co., Ltd	GRM188R71E104KA01D
1	C3	Capacitor, Tantalum, 100 μ F, 6.3V, 10%, 400 m Ω , Surface Mount B	AVX Corporation	TPSB107K006R0400
7	C4, C13, C17, C21, C30, C33, C37	Capacitor, Ceramic, 47 nF, 16V, 10%, X7R, Surface Mount, 0603	Murata® Manufacturing Co., Ltd	GRM188R71C473KA01D
1	C41	Capacitor, Ceramic, 22000 pF, 50V, 5%, X7R, Surface Mount, 0603	AVX Corporation	06035C223JAT2A
10	C42, C50, C52, C54, C56, C58, C60, C62, C64, C66	Capacitor, Ceramic, 0.1 μ F, 50V, 20%, X7R, Surface Mount, 0603	TDK Corporation	C1608X7R1H104M
12	C43, C44, C45, C46, C51, C53, C55, C57, C59, C61, C63, C65	Capacitor, Ceramic, 10 μ F, 10V, 10%, X7R, Surface Mount, 0805	Murata Manufacturing Co., Ltd.	GRM21BR71A106KE51L
3	C47, C48, C49	Capacitor, Ceramic, 10 μ F, 35V, 10%, X5R, Surface Mount, 1206	Taiyo Yuden Co., Ltd.	GMK316BJ106KL-T
24	C5, C6, C7, C8, C9, C14, C15, C16, C18, C19, C20, C22, C23, C24, C25, C26, C31, C32, C34, C35, C36, C38, C39, C40	Capacitor, Ceramic, 1000 pF, 50V, 10%, X7R, Surface Mount, 0603	NIC Components Corp.	NMC0603X7R102K50TRPF
8	C67, C68, C81, C82, C92, C93, C94, C95	Capacitor, Ceramic, 0.1 μ F, 16V, 10%, X7R, Surface Mount, 0603	Samsung Electro-Mechanics America, Inc.	CL10B104KO8NNNC
6	C69, C70, C83, C84, C96, C97	Capacitor, Ceramic, 4.7 μ F, 16V, 10%, X5R, Surface Mount, 0603	TDK Corporation	C1608X5R1C475K080AC
3	C71, C85, C98	Capacitor, Ceramic, 0.010 μ F, 25V, 10%, X7R, Surface Mount, 0603	Yageo Corporation	CC0603KRX7R8BB103
3	C72, C86, C99	Capacitor, Ceramic, 4700 pF, 50V, 10%, X7R, Surface Mount, 0603	KEMET	C0603C472K5RACTU

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

Bill of Materials (BOM)

TABLE B-2: HV MUX CONTROLLER BOARD – BILL OF MATERIALS (CONTINUED)

Qty.	Reference	Description	Manufacturer	Part Number
15	C73, C74, C75, C76, C77, C78, C87, C88, C91, C100, C101, C102, C107, C108, C109	Capacitor, Ceramic, 10000 pF, 50V, 10%, X7R, 0603	AVX Corporation	06035C103KAT2A
2	C103, C105	Capacitor, Ceramic, 4.7 μ F, 16V, 10%, X7R, Surface Mount, 1206	KEMET	C1206C475K4RACTU
1	D1	Diode, Schottky, 20BQ030, 470 mV, 2A, 30V, DO-214AA_SMB	Vishay	VS-20BQ030-M3/5BT
8	D2, D3, D4, D5, D6, D7, D8, D9	Diode, Schottky, 30V, 200 mA, 40V, SOD-523	Micro Commercial Components	BAT54WX-TP
2	J1, J2	Connector Edge, HMZd, 2.5mm 40P, Female, TH R/A	Tyco Electronics (TE Connectivity, Ltd.)	1469028-1
2	J4, J5	Connector Header-2.54, Male, 1x6, Tin, 5.84 mm, Through-Hole, Vertical	Sullins Connector Solutions	PEC06SAAN
1	J6	Connector, Power, 2.5 mm, 5.5 mm, Switch, Through-Hole, Right Angle	CUI Inc.	PJ-002B
1	J7	Connector, USB, Mini-B, Female, Surface Mount, Right Angle	Hirose Electric Co., Ltd.	UX60SC-MB-5ST(80)
2	J8, J9	Connector, RF Coaxial, SMA, Female, 2P, Through-Hole, Vertical	TE Connectivity, Ltd.	5-1814832-1
4	J10, J11, J12, J13	Connector TP Pad, Mill, Max 3132, PCB, 1.5x0.9, TH, AU	Mill-Max Manufacturing Corp.	3132-0-00-15-00-00-08-0
1	L1	Inductor, 4.7 μ H, 11A 20% SMD L6.56W6.36H6.1	Coilcraft	XAL6060-472MEC
3	LD1, LD2, LD4	Diode, LED, Green, 2.2V, 25 mA, 15 mcd, Clear, Surface Mount, 0603	Kingbright Electronic Co., Ltd.	APT1608SGC
1	LD5	Diode, LED, Red, 2V, 25 mA, 104 mcd, Diffuse, Surface Mount, 0603	OSRAM Opto Semiconductors GmbH.	LS Q976-NR-1-0-20-R18
1	PCB1	HV2925 Analog Switch Evaluation Board – Printed Circuit Board	Microchip Technology Inc.	12014-R1
1	Q1	Transistor, FET N-Channel BSS123, 100V, 170 mA, 300 mW, SOT-23-3	Diodes Incorporated	BSS123-7-F
6	R1, R2, R4, R11, R13, R14	Resistor, Thick Film, 4.7 k Ω , 5%, 1/10W, Surface Mount, 0603	Panasonic® - ECG	ERJ-3GEYJ472V
2	R3, R8	Resistor, Thick Film, 51R, 5%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3GEYJ510V
5	R5, R10, R15, R49, R52	Resistor, Thick Film, 0R, 1/10W, Surface Mount, 0603	NIC Components, Panasonic - ECG	NRC06Z0TRF, ERJ-3GSY0R00V

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

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TABLE B-2: HV MUX CONTROLLER BOARD – BILL OF MATERIALS (CONTINUED)

Qty.	Reference	Description	Manufacturer	Part Number
2	R6, R7	Resistor, Thick Film, 100R, 5%, 1/10W, Surface Mount, 0603	Vishay® Intertechnology, Inc.	CRCW0603100RJNEA
1	R9	Resistor, Thick Film, 22R, 5%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3GSYJ220V
1	R12	Resistor, Metal Film, 330R, 5%, 1/16W, Surface Mount, 0603	Panasonic - ECG	ERA-V33J331V
1	R16	Resistor, Thick Film, 39 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF3902V
1	R17	Resistor, Thick Film, 19.1 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF1912V
1	R18	Resistor, Thick Film, 1 kΩ, 5%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3GEYJ102V
2	R19, R27	Resistor, Thick Film, 390R, 5%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3GEYJ391V
3	R20, R37, R40	Resistor, Thick Film, 100R, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF1000V
1	R21	Resistor, Thick Film, 8.66 kΩ, 1%, 1/10W, Surface Mount, 0603	Yageo Corporation	RC0603FR-078K66L
6	R22, R28, R29, R33, R38, R42	Resistor, Thin Film, 10 kΩ, 1%, 1/8W, Surface Mount, 0603	Vishay Beyschlag	MCT06030C1002FP500
4	R23, R24, R30, R50	Resistor, Thick Film, 10 kΩ, 5%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3GEYJ103V
1	R25	Resistor, Thick Film, 51 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF5102V
1	R26	Resistor, Thick Film, 69.8 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF6982V
1	R31	Resistor, Thick Film, 82 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF8202V
1	R32	Resistor, Thick Film, 10.7 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF1072V
4	R34, R35, R39, R41	Resistor, Thick Film, 150R, 1%, 1/10W, Surface Mount, 0603	Stackpole Electronics, Inc.	RMCF0603FT150R
1	R36	Resistor, Thick Film, 75 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF7502V
3	R43, R45, R47	Resistor, Thick Film, 100k 1% 1/10W SMD 0603	Panasonic - ECG	ERJ-3EKF1003V
3	R44, R46, R48	Resistor, Thick Film, 78.7 kΩ, 1%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3EKF7872V
1	R51	Resistor, Thick Film, 150R, 5%, 1/10W, Surface Mount, 0603	Panasonic - ECG	ERJ-3GEYJ151V

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

Bill of Materials (BOM)

TABLE B-2: HV MUX CONTROLLER BOARD – BILL OF MATERIALS (CONTINUED)

Qty.	Reference	Description	Manufacturer	Part Number
1	SW1	Switch Slide, SPDT, Mini, 50V, 0.5A, Through-Hole	Jameco® Electronics	G4050X-R
1	SW2	Switch Tactical, SPST, 12V, 50 mA, Surface Mount	E-Switch®, Inc.	TL3301NF260QG/TR
1	U1	IC, FPGA, 102I/O LQFP-144	Xilinx Inc.	XC6SLX9-2TQG144C
1	U2	IC, FPGA, TSSOP-20	Xilinx Inc.	XCF04SVOG20C
1	U3	IC Analog Switcher Buck 5V 3A VQFN-16	Semtech Corporation	TS30013-M050QFNR
4	U4, U5, U6, U7	Microchip Analog LDO, 0.8V-5V DFN-8	Microchip Technology Inc.	MCP1727-ADJE/MF
3	U8, U11, U12	Analog LDO, ADJ TDFN-6	Microchip Technology Inc.	MIC94325YMT-TR
1	U9	Interface, USB, SPI MCP2210-I/SS SSOP-20	Microchip Technology Inc.	MCP2210T-I/SS
1	U10	Clock Generation Flexible Ultra-Low Jitter QFN-48	Microchip Technology Inc.	SM803234UMG
1	X1	Resonator, 12 MHz, 0.07%, SMD 3-SMD	Murata Manufacturing Co., Ltd.	CSTNE12M0GH5L000R0
1	X2	Crystal, 40 MHz, 12 pF, SMD L3.2W2.5H0.8	CTS-Frequency Controls	SA324400F35HLR

Note: The components listed in this Bill of Materials are representative of the PCB assembly. The released BOM used in manufacturing uses all RoHS-compliant components.

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NOTES:

Appendix C. Demo Board Waveforms

C.1 BOARD TYPICAL WAVEFORMS

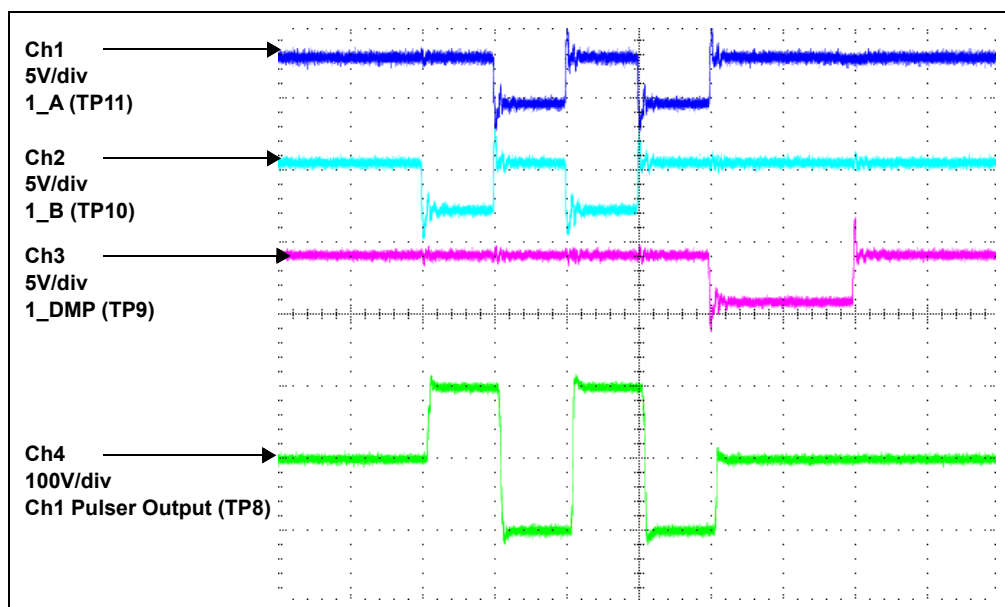


FIGURE C-1: 5 MHz, 4 Pulses, Ch1 Pulser Input and Output when All SW Off.

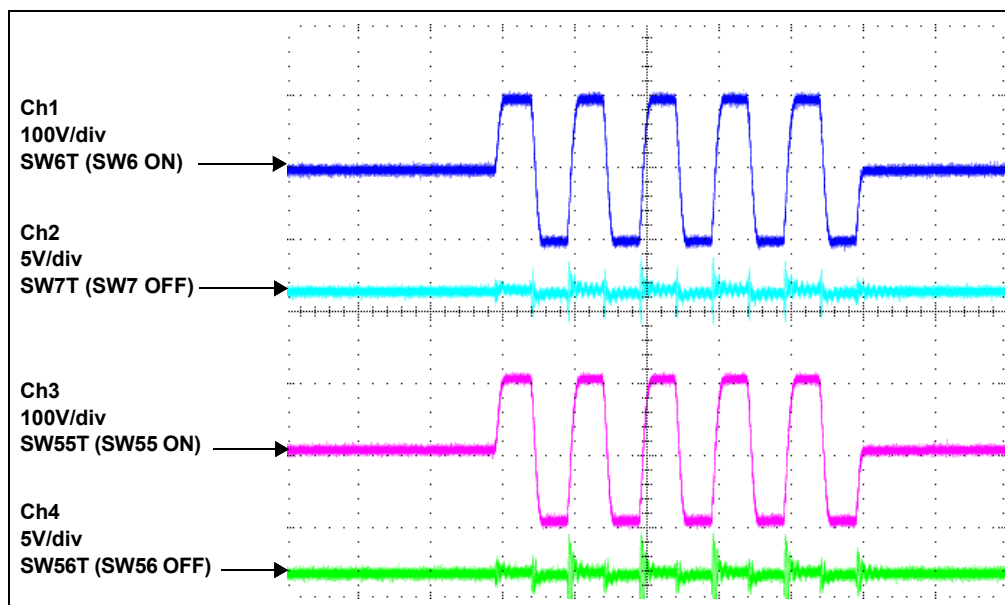


FIGURE C-2: 5 MHz, 10 Pulses, $V_{PP}/V_{NN} = \pm 100V$, $V_{DD} = +5V$, $VGP = 10V$, 330 pF//2.5 kΩ Load.

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