



EVB-LAN8770-RGMII

Evaluation Board

User's Guide

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ISBN: 978-1-6683-3048-7

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Preface

NOTICE TO CUSTOMERS

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Documents are identified with a “DS” number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is “DSXXXXA”, where “XXXX” is the document number and “A” is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the EVB-LAN8770-RGMII Evaluation Board User's Guide. Items discussed in this chapter include:

- [Document Layout](#)
- [Conventions Used in this Guide](#)
- [Warranty Registration](#)
- [The Microchip Web Site](#)
- [Development Systems Customer Change Notification Service](#)
- [Customer Support](#)
- [Document Revision History](#)

DOCUMENT LAYOUT

This document describes how to use the EVB-LAN8770 RGMII Evaluation Board as a development tool for the LAN8770, 100BASE-T1 Ethernet Transceiver. The manual layout is as follows:

- **Chapter 1. “Overview”** – This shows a brief description of the EVB-LAN8770-RGMII Evaluation Board.
- **Chapter 2. “Getting Started”** – This provides information about the setup and operation of the EVB-LAN8770-RGMII Evaluation Board.
- **Chapter 3. “Hardware Configuration”** - This details the hardware configuration information of the EVB-LAN8770-RGMII Evaluation Board.
- **Appendix A. “Schematics”** – This appendix shows the EVB-LAN8770-RGMII Evaluation Board schematics.
- **Appendix B. “Bill of Materials”** – This appendix includes the EVB-LAN8770-RGMII Evaluation Board Bill of Materials.

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	<i>MPLAB® IDE User's Guide</i>
	Emphasized text	...is the <i>only</i> compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	<u><i>File>Save</i></u>
Bold characters	A dialog button	Click OK
	A tab	Click the Power tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	#define START
	Filenames	autoexec.bat
	File paths	c:\mcc18\h
	Keywords	_asm, _endasm, static
	Command-line options	-Opa+, -Opa-
	Bit values	0, 1
	Constants	0xFF, 'A'
Italic Courier New	A variable argument	<i>file.o</i> , where <i>file</i> can be any valid filename
Square brackets []	Optional arguments	mcc18 [options] <i>file</i> [options]
Curly brackets and pipe character: { }	Choice of mutually exclusive arguments; an OR selection	errorlevel {0 1}
Ellipses...	Replaces repeated text	var_name [, var_name...]
	Represents code supplied by user	void main (void) { ... }

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- **Emulators** – The latest information on Microchip in-circuit emulators. This includes the MPLAB REAL ICE and MPLAB ICE 2000 in-circuit emulators.
- **In-Circuit Debuggers** – The latest information on the Microchip in-circuit debuggers. This includes MPLAB ICD 3 in-circuit debuggers and PICkit 3 debug express.
- **MPLAB IDE** – The latest information on Microchip MPLAB IDE, the Windows Integrated Development Environment for development systems tools. This list is focused on the MPLAB IDE, MPLAB IDE Project Manager, MPLAB Editor and MPLAB SIM simulator, as well as general editing and debugging features.
- **Programmers** – The latest information on Microchip programmers. These include production programmers such as MPLAB REAL ICE in-circuit emulator, MPLAB ICD 3 in-circuit debugger and MPLAB PM3 device programmers. Also included are nonproduction development programmers such as PICSTART Plus and PIC-kit 2 and 3.

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- Field Application Engineer (FAE)
- Technical Support

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Technical support is available through the web site at:

<http://www.microchip.com/support>

DOCUMENT REVISION HISTORY

Revisions	Section/Figure/Entry	Correction
DS50003566A (08-30-23)	Initial release	

Chapter 1. Overview

1.1 INTRODUCTION

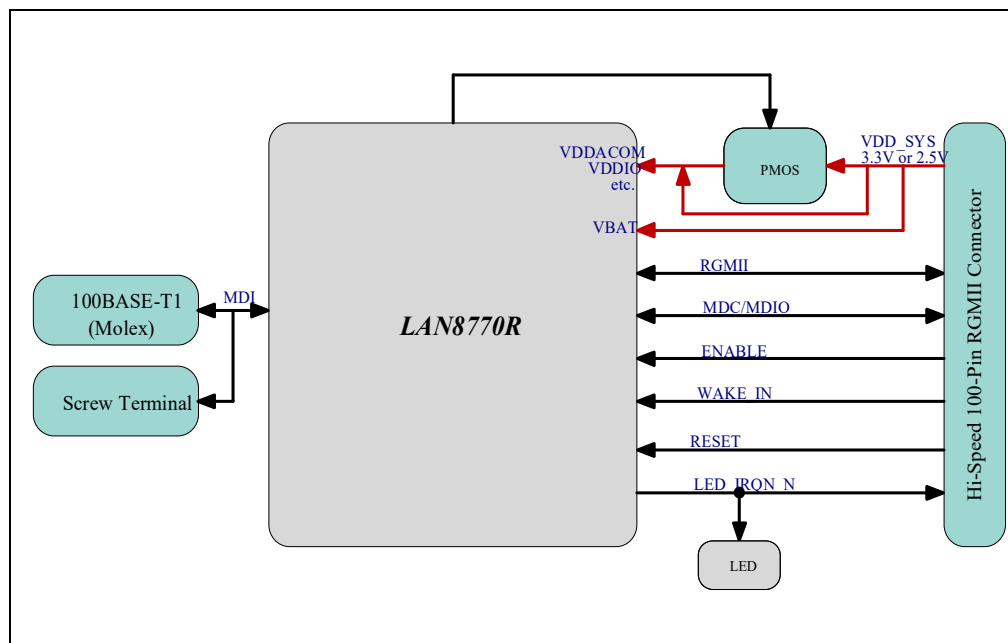
The EVB-LAN8770-RGMII Evaluation Board is a plug-in card that interfaces directly with a mating Microchip host processor controller board, such as the SAMA5D3 Ethernet Development System (EDS) board. It features the EVB-LAN8770, a highly integrated networking device that incorporates a 100BASE-T1 physical layer transceiver (PHY). The board's PHY port is connected to a RJ45 Ethernet jack with integrated magnetics, and the PHY's RGMII connections are brought out to a high-speed multi-pin (HS) connector.

Together, the EVB-LAN8770-RGMII Evaluation Board and the SAMA5D3-EDS provide a highly-flexible platform for evaluation of basic PHY features via static Control-Status Registers (CSR).

The scope of this document is meant to describe the EVB-LAN8770-RGMII Evaluation Board setup and its user interface features. A simplified block diagram of the board is shown in [Figure 1-1](#).

1.2 BLOCK DIAGRAM

FIGURE 1-1: EVB-LAN8770-RGMII BLOCK DIAGRAM



1.3 REFERENCES

Concepts and materials available in the following documents may be helpful when reading this document. Visit www.microchip.com for the latest documentation.

- *LAN8770 Data Sheet*
- LAN8770 Schematics
- SAMA5D3 Ethernet Development System Schematics
- *SAMA5D3 Ethernet Development System Board User's Guide*
- *EVB-LAN7431-EDS Board User's Guide*
- *EVB-LAN7801-EDS Board User's Guide*
- *MIC94052 Data Sheet*
- *VXM7-9013 Data Sheet*

1.4 TERMS AND ABBREVIATIONS

The following are the terms and abbreviations used in this document:

- CSR – Control Status Registers
- DNP – Do Not Populate
- EDS – Ethernet Development System
- EEPROM – Electronically Erasable Programmable Read-Only Memory
- MDC – Management Data Clock
- MDIO – Management Data Input/Output
- PHY – Physical Layer Transceiver
- RGMII – Reduced Gigabit Media Independent Interface
- SMI – Serial Management Interface

Chapter 2. Getting Started

2.1 INTRODUCTION

The EVB-LAN8770-RGMII Evaluation Board is designed as a plug-in card to interface directly with a mating Microchip host processor or controller board, such as the SAMA5D3-EDS Board, that supplies full power and provides full register access and configuration via MDIO/MDC bus management.

2.2 DEFAULT JUMPER SETTINGS

The EVB-LAN8770-RGMII ships with the necessary jumpers installed for basic operation. These are:

- J5: shunt installed between pins [1-2]
- J15: shunt installed between pins [1-2]
- J16: shunt installed between pins [2-3]
- J18: shunt installed between pins [1-2]

See [Figure 3.2](#) for an image of these default shunt installations.

See [Section 3.4 “Connectors”](#) for a full list of connector/header descriptions and directions for use.

2.3 POWER SOURCE

The EVB-LAN8770-RGMII can be completely bus-powered from its mating Microchip host processor or control board. Alternatively, the EVB-LAN8770-RGMII can be powered with an external 3.3V supply.

Refer to [Figure 3.2](#) and the board schematics in [Figure A-1](#) for details.

2.3.1 EDS-Powered Operation

For EDS-powered operation:

- J16 needs a jumper on either pins [1-2] for 2.5V operation or pins [2-3] for 3.3V operation.
- Either one of the following two options but not both options:
 - J15 [1-2] for normal operation
 - J13 [1-2] for TC10 application where INH controls non-VBAT power supplies

2.3.2 External-Powered Operation

For external-powered operation:

- Apply 2.5V or 3.3V to J16, pin 2 (center pin).
- Either one of the following two options but not both options:
 - J15 [1-2] for normal operation
 - J13 [1-2] for TC10 application where INH controls non-VBAT power supplies

2.4 CLOCK

The EVB-LAN8770-RGMII utilizes a 25 MHz crystal to generate input reference clock for the LAN8770 device. Refer to [Figure A-1](#) for details.

2.5 RESET CIRCUIT

2.5.1 Power-On Reset—EDS Reset

The SAMA5D3-EDS can provide the LAN8770 Reset when a jumper is placed on EVB-LAN8770, J18 pins [2-3] (EDS Reset).

2.5.2 Manual Reset

The EVB-LAN8770 SW1 can be pressed and released to provide LAN8770 Reset after device power-up. The EVB-LAN8770-RGMII J18 must have a jumper between pins [1-2] (onboard) to use this manual Reset.

2.6 USING THE EVB-LAN8770-RGMII

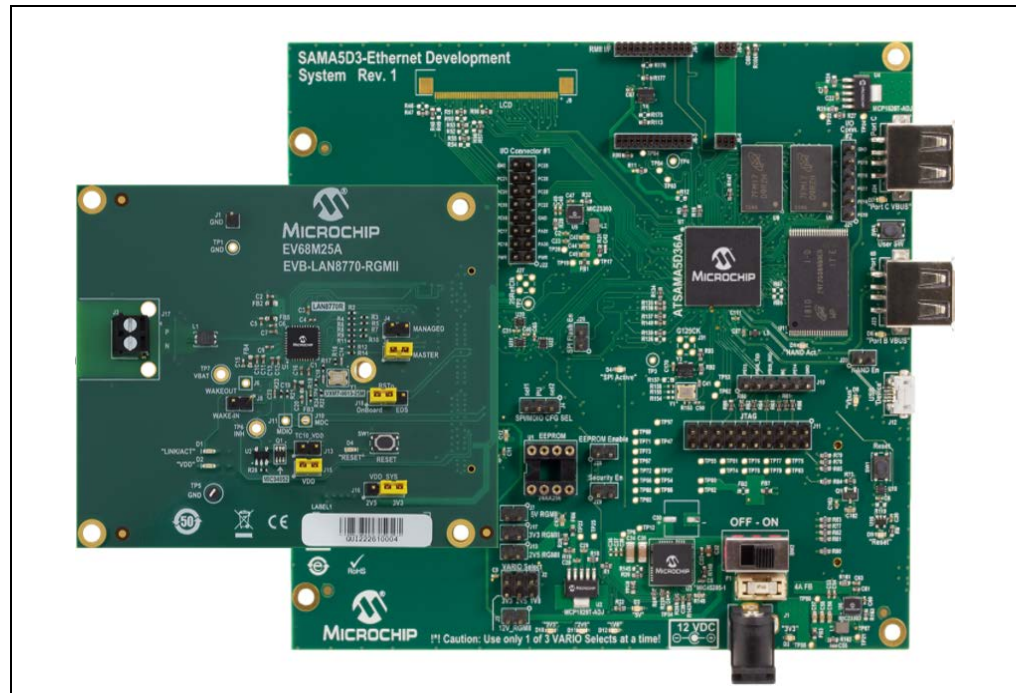
2.6.1 With SAMA5D3-EDS

The EVB-LAN8770-RGMII directly plugs into a mating Microchip host controller or processor board, such as the SAMA5D3-EDS, that can deliver full power and provide full register access and configuration via MDIO/MDC bus management.

Together, the EVB-LAN8770 and the SAMA5D3-EDS enable 100 Mbps Ethernet traffic through RGMII and the PHY port of the EVB-LAN8770 device, with the RGMII port connecting to the SAMA5D3 processor and the PHY port connecting via copper Ethernet cable (CAT-5 UTP or better) to external Ethernet devices.

All LAN8770 registers are accessible via MDIO/MDC bus management from the SAMA5D3-EDS Board, enabling full evaluation and firmware for all LAN8770 features. Refer to the *SAMA5D3 Ethernet Development System Board User's Guide*. [Figure 2-1](#) shows the EVB-LAN8770-RGMII connected to the SAMA5D3-EDS board.

FIGURE 2-1: EVB-LAN8770-RGMII AND SAMA5D3-EDS BOARD (TOP VIEW)



2.6.2 With EVB-LAN7431-EDS

For EVB-LAN7431-EDS to work with the EVB-LAN8770, a specific LAN7431 EEPROM image should be programmed onto the EVB-LAN7431-EDS baseboard. This is necessary to ensure that RGMII TXC and RXC delays settings are appropriately configured, and the LAN7431's 125 MHz clock source is enabled. A `readme` file that describes the detailed configuration and the binary files used to program the EVB-LAN7431-EDS is available on the EVB-LAN8770-RGMII Evaluation Board product page.

All LAN8770 registers are accessible via MDIO/MDC bus management from the EVB-LAN7431-EDS (through both Windows® and Linux®), enabling full evaluation.

2.6.3 With EVB-LAN7801-EDS

For EVB-LAN7801-EDS to work with the EVB-LAN8770, a specific LAN7801 EEPROM image should be programmed onto the EVB-LAN7801-EDS baseboard. This is necessary to ensure that RGMII TXC and RXC delay settings are appropriately configured, and the LAN7801's 125 MHz clock source is enabled.

A `readme` file that describes the detailed configuration and the binary files used to program the EVB-LAN7801-EDS are available on the EVB-LAN8770-RGMII Evaluation Board product page.

All LAN8770 registers are accessible via MDIO/MDC bus management, enabling full LAN8770 evaluation through either of these methods:

- EVB-LAN7801-EDS with Windows or Linux
 - Populate EVB-LAN7801-EDS J15 (MDC).
 - Populate EVB-LAN7801-EDS J16 (MDIO).
- EVB-LAN8770-RGMII
 - DNP EVB-LAN7801-EDS J15 (MDC).

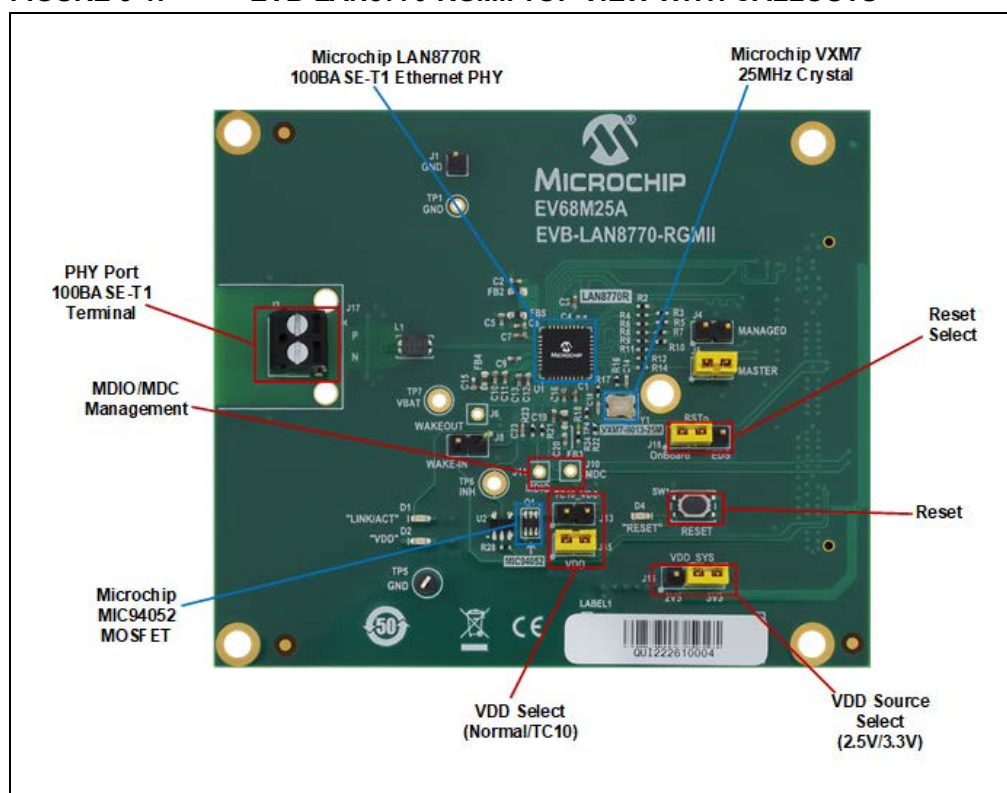
- DNP EVB-LAN7801-EDS J16 (MDIO).
- EVB-LAN8770-RGMII J10 (MDC) and J11 (MDIO) for local LAN8770 evaluation with a Serial Management Interface (SMI) tool

Chapter 3. Hardware Configuration

3.1 HARDWARE CONFIGURATION OPTIONS

Figure 3-1 shows the top view of the EVB-LAN8770-RGMII.

FIGURE 3-1: EVB-LAN8770-RGMII TOP VIEW WITH CALLOUTS



3.2 PHY PORT

PHY port (J17) supports 100BASE-T1 operation.

3.3 TEST POINTS

Table 3-1 lists the test points on the EVB-LAN8770-RGMII:

TABLE 3-1: TEST POINTS

Test Point	Description
J1	GND
TP1	GND
TP5	GND
TP6	INH
TP7	VBAT

3.4 CONNECTORS

Table 3-2 lists the connectors on the EVB-LAN8770-RGMII:

TABLE 3-2: CONNECTORS

Connector Reference Designator	Function	Options
J4	MANAGED mode Selection	Closed: LAN8770 is in Managed mode. Open: LAN8770 is in Autonomous mode.
J5	MASTER mode Selection	Closed: LAN8770 is configured as a leader. Open: LAN8770 is configured as a follower.
J8	WAKEIN Jumper	Closed: Allows LAN8770 WAKE_IN access to MAC through J14 RGMII connector. Open: LAN8770 WAKE_IN is isolated from MAC.
J10	MDC Header	N/A
J11	MDIO Header	N/A
J13	TC10_VDD Select	If populated, VDD power is NOT applied to non-VBAT supplies in TC10 Sleep mode. (See Note 1 .)
J14	RGMII Connector	N/A
J15	VDD Jumper	If populated, VDD power is delivered on non-VBAT supplies in TC10 Sleep mode. (See Note 1 .)
J16	VDD_SYS Select	Shunt pins [1-2]: VDD_SYS is 2.5V. Shunt pins [2-3]: VDD_SYS is 3.3V. (See Note 2 .)
J17	100BASE-T1 Ethernet Connector	N/A
J18	RESET Select	Shunt pins [1-2]: Reset comes from MAC (EDS) device. Shunt pins [2-3]: Reset comes from local Reset button (SW1).

Note 1: Only one of J13 or J15 must be populated for LAN8770 power.

2: A shunt must be placed on either [1-2] or [2-3] for LAN8770 power.



EVB-LAN8770-RGMII EVALUATION BOARD USER'S GUIDE

Appendix A. Schematics

A.1 INTRODUCTION

This appendix shows the EVB-LAN8770-RGMII Evaluation Board schematics.

FIGURE A-1: EVB-LAN8770-RGMII SCHEMATIC 1

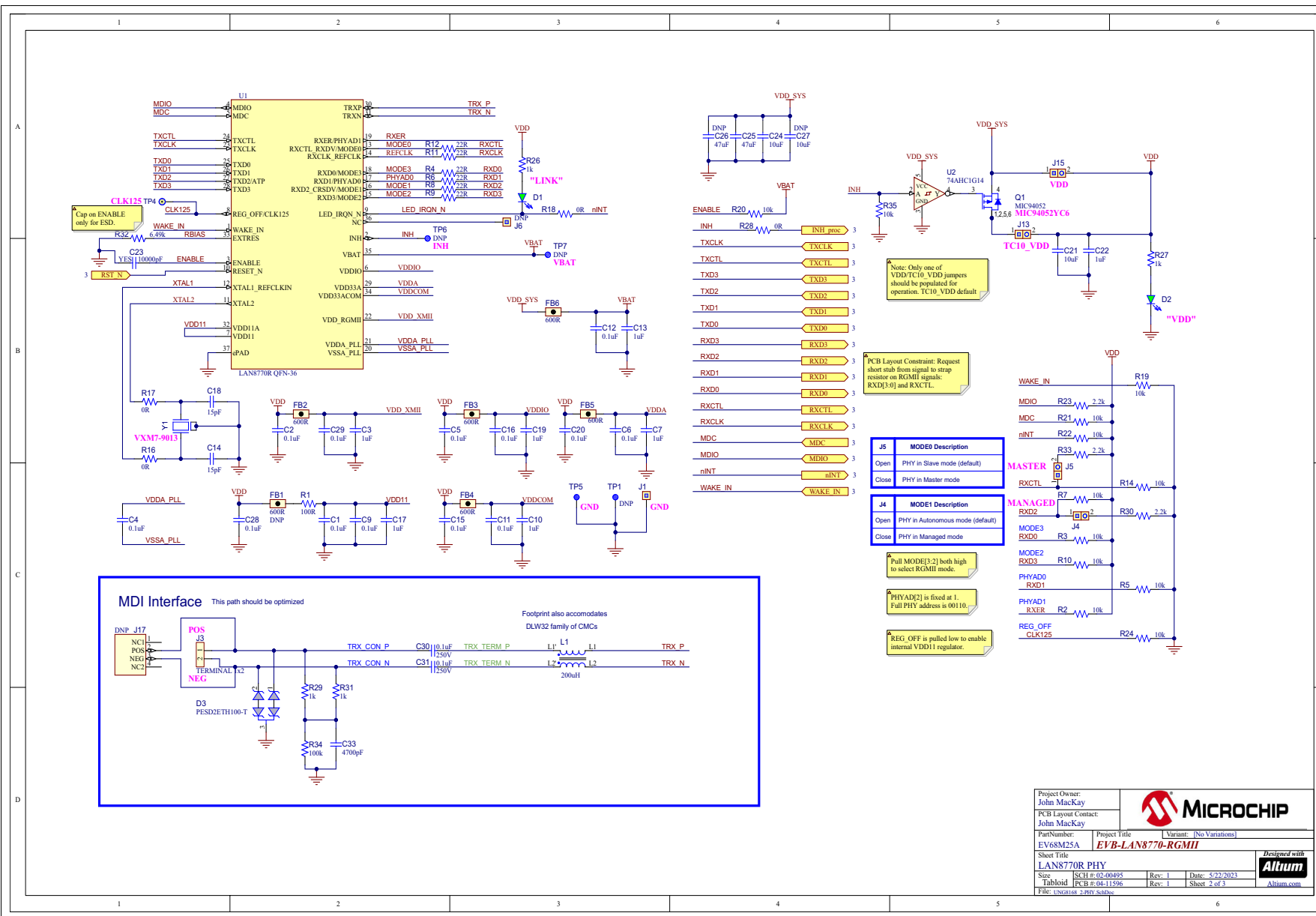
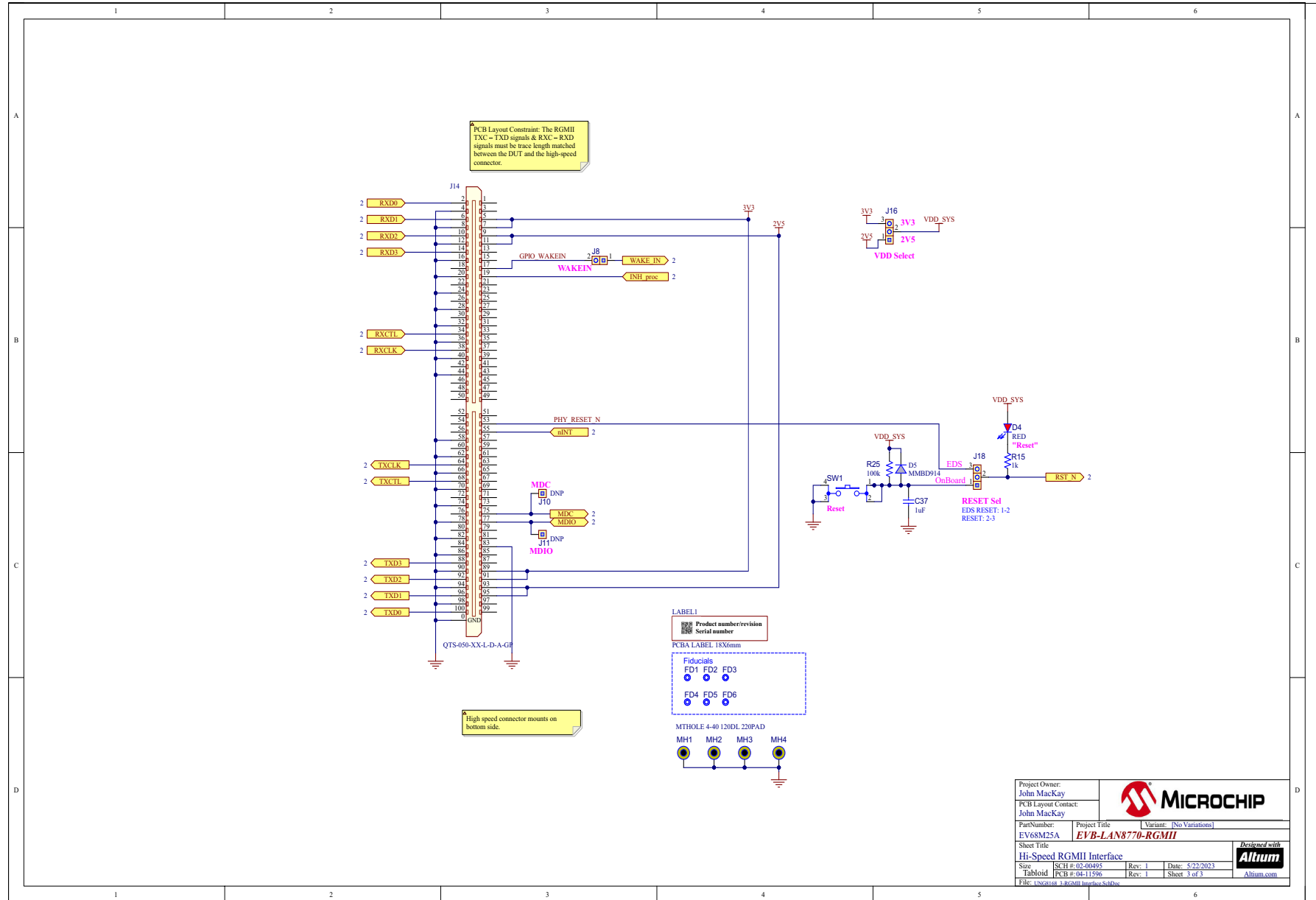


FIGURE A-2: EVB-LAN8770-RGMII SCHEMATIC 2



NOTES:



EVB-LAN8770-RGMII EVALUATION BOARD USER'S GUIDE

Appendix B. Bill of Materials

B.1 INTRODUCTION

This appendix contains the EVB-LAN8770-RGMII Evaluation Board Bill of Materials (BOM).

TABLE B-1: EVB-LAN8770 RGMII BILL OF MATERIALS

Item	Quantity	Designator	Description	Populated	Manufacturer	Manufacturer Part Number
1	13	C1, C2, C4, C5, C6, C9, C11, C12, C15, C16, C20, C28, C29	CAP CER 0.1uF 35V 10% X7R SMD 0402	YES	TDK Corporation	CGA2B3X7R1V104K050BB
2	7	C3, C7, C10, C13, C17, C19, C22	CAP CER 1uF 35V 10% X5R SMD 0402	YES	Murata Electronics North America	GRM155R6YA105KE11D
3	2	C14, C18	CAP HiQ 15pF 50V 5% NP0 2.32GHz SMD 0402	YES	Johanson Technology Inc	500R07S150JV4T
4	2	C21, C24	CAP CER 470pF 25V 5% NP0 SMD 0603	YES	Murata Electronics North America	GRM188R61E106MA73D
5	1	C23	CAP CER 10000pF 16V 10% X7R SMD 0402	YES	KEMET	C0402C103K4RACTU
6	1	C25	CAP CER 47uF 6.3V 20% X5R SMD 0805v	YES	Taiyo Yuden	JMK212BJ476MG-T
7	2	C30, C31	CAP CER 0.1uF 250V 10% X7T SMD 0805	YES	TDK Corporation	C2012X7T2E104K125AA
8	1	C33	CAP CER 4700pF 250V 10% X7R SMD 0805	YES	KEMET	C0805C472KARACAUTO
9	1	C37	CAP CER 1uF 16V 10% X5R SMD 0603	YES	AVX	0603YD105KAT2A
10	1	D1, D2	DIO LED GREEN 2V 30mA 35mcd Clear SMD 0603	YES	Lite-On Inc	LTST-C191KGKT
11	1	D3	DIO TVS ARRAY PESD2ETH100-TR 24V SMD SOT-23-3	YES	Nexperia	PESD2ETH100-TR
12	5	FB2, FB3, FB4, FB5, FB6	FERRITE 600R 500mA SMD 0603	YES	Murata Electronics North America	BLM18AG601SH1D
13	1	J1	CON HDR-2.54 Male 1x1 Gold 5.84MH TH VERT	YES	TE Connectivity	5-146280-1
14	1	J3	CON TERMINAL 3.5mm 1x2 Female 16-26AWG 10A TH R/A	YES	On Shore Technology Inc.	OSTTE020104
15	5	J4, J5, J8, J13, J15	CON HDR-2.54 Male 1x2 Gold 5.84MH TH VERT	YES	FCI	68001-202HLF
16	1	J14	CON STRIP High Speed Stacker 5mm Male 2x50 SMD VERT	YES	Samtec	QTS-050-01-L-D-A-GP
17	2	J16, J18	CON HDR-2.54 Male 1x3 Gold 5.84MH TH VERT	YES	FCI	68000-103HLF
18	1	L1	CM CHOKE 5.5R@100kHz 200UH SMD 3.2X2.5MM	YES	TDJK Electronics	DLW43MH201XK2L
19	1	Q1	MCHP ANALOG MOSFET SINGLE P-CH MIC94052YC6 2A 0.270WSC70-6	YES	Microchip Technology	MIC94052YC6-TR
20	1	R1	RES TKF 100R 1% 1/10W SMD 0402	YES	Panasonic Electronic Components	ERJ-2RKF1000X
21	12	R2, R3, R5, R7, R10, R14, R19, R20, R21, R22, R24, R35	RES TKF 10k 1% 1/10W SMD 0402	YES	Panasonic	ERJ-2RKF1002X
22	6	R4, R6, R8, R9, R11, R12	RES TKF 22R 1% 1/16W SMD 0402	YES	Yageo	RC0402FR-0722RL
23	3	R15, R26, R27	RES TKF 1k 5% 1/16W SMD 0402	YES	Yageo	RC0402JR-071KL
24	4	R16, R17, R18, R28	RES TKF 0R SMD 0402 AEC-Q200	YES	Panasonic	ERJ-2GE0R00X
25	3	R23, R30, R33	RES TKF 2.2k 5% 1/16W SMD 0402	YES	Vishay Dale	CRCW04022K20JNED
26	1	R25	RES TF 100k 1% 1/10W SMD 0603	YES	Yageo	RC0603FR-07100KL

TABLE B-1: EVB-LAN8770 RGMII BILL OF MATERIALS (CONTINUED)

Item	Quantity	Designator	Description	Populated	Manufacturer	Manufacturer Part Number
27	2	R29, R31	RES TF 1k 1% 1/2W SMD 1206	YES	Stackpole Electronics Inc	RNCP1206FTD1K00
28	1	R32	RES TkF 6.49k 0.1% 1/16W SMD 0402	YES	Panasonic Electronics Components	ERA-2ARB6491X
29	1	R34	RES TKF 100k 1% 1/4W SMD 0603	YES	Vishay	CRCW0603100KFKEAHP
30	1	SW1	SWITCH TACT SPST-NO 16V 0.05A PTS810 SMD	YES	C&K Components	PTS810 SJM 250 SMTR LFS
31	1	TP5	CON TP LOOP Black 3.18x5.59 TH	YES	Keystone Electronics	5006
32	1	U1	MCHP INTERFACE T1 ETHERNET LAN8770R QFN-36	YES	Microchip Technology	LAN8770R-E/5KXVAO
33	1	U2	C BUFFER Inverting Schmitt trigger 74AHC1G14GV GATE SGL CMOS SOT-23-5	YES	Nexperia	74AHC1G14GV,125
34	1	Y1	MCHP CRYSTAL 25Mhz 10pF SMD L3.2W2.5H0.8	YES	Microchip Technology	VXM7-9013-25M0000000
35	1	C26	CAP CER 47uF 6.3V 20% X5R SMD 0805	DNP	Taiyo Yuden	JMK212BJ476MG-T
36	1	C27	CAP CER 10uF 25V 20% X5R SMD 0603	DNP	Murata Electronics North America	GRM188R61E106MA73D
37	1	FB1	FERRITE 600R 500mA SMD 0603	DNP	Murata Electronics North America	BLM18AG601SH1D
38	3	J6, J10, J11	CON HDR-2.54 Male 1x1 Gold 5.84MH TH VERT	DNP	TE Connectivity	5-146280-1
39	1	J17	CON JACK MINI50 2MM BLACK MALE TH R/A	DNP	Molex, LLC	0347930040
40	1	TP1	MISC, TEST POINT MULTI PURPOSE MINI BLACK	DNP	Keystone	5001
41	2	TP6, TP7	CON TP LOOP Black 3.18x5.59 TH	DNP	Keystone Electronics	5006

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