DSC8001 Series



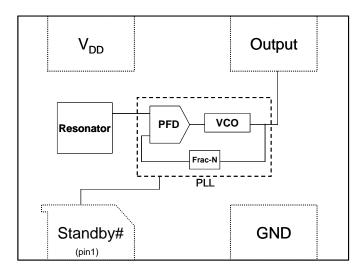
General Description

The DSC8001 is a programmable silicon MEMS based CMOS oscillator offering excellent jitter and stability performance over a wide range of supply voltages and temperatures. The device operates from 1 to 150MHz in increments of 100Hz (up to four decimal point resolution) with supply voltages between 1.8 to 3.3 Volts and extended temperatures from -40°C to 105°C.

The DSC8001 incorporates an all silicon resonator that is extremely robust and nearly immune to stress related fractures, common to crystal based oscillators. Without sacrificing the performance and stability required of today's systems, a crystal-less design allows for a higher level of reliability, making the DSC8001 ideal for rugged, industrial, and portable applications where stress, shock, and vibration can damage quartz crystal based systems.

Available in industry standard packages, the DSC8001 can be "dropped-in" to the same PCB footprint as standard crystal oscillators.

Block Diagram



Features

- Frequency Range: 1 to 150MHz
 - Exceptional Stability over Temperature ±10 PPM, ±25 PPM, ±50 PPM 0

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- Operating voltage o 1.7 to 3.6V
- **Operating Temperature Range**
 - Ext. Industrial -40°C to 105°C 0
 - Industrial -40°C to 85°C 0
 - Ext. Commercial -20°C to 70°C
 - Commercial 0°C to 70°C
- Low Operating and Standby Current
 - 5mA Operating (40MHz)
 - 15uA Standby
- Ultra Miniature Footprint
 - 2.5 x 2.0 x 0.85 mm 0
 - 3.2 x 2.5 x 0.85 mm 0
 - 5.0 x 3.2 x 0.85 mm 0
 - 7.0 x 5.0 x 0.85 mm 0
- MIL-STD 883 Shock and Vibration Resistant
- Pb Free, RoHS, Reach SVHC Compliant
- AEC-Q100 Reliability Qualified

Benefits

- Pin for pin "drop in" replacement for industry standard oscillators
- Semiconductor level reliability, significantly higher than guartz
- Frequency Resolution to 4 decimals
- Short mass production lead-times
- Longer Battery Life / Reduced Power
- **Compact Plastic package**
- Cost Effective

Applications

- Mobile Applications
- **Consumer Electronics**
- Portable Electronics
- DVR, CCTV, Surveillance Cameras
- Low Profile Applications
- Industrial Applications

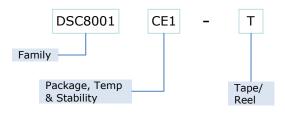
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Absolute Maximum Ratings¹

| Item | Min. | Max | Unit | Condition |
|----------------|------|---------|------|-------------|
| Supply Voltage | -0.3 | +4.0 | V | |
| Input Voltage | -0.3 | VDD+0.3 | V | |
| Junction Temp | - | +150 | °C | |
| Storage Temp | -55 | +150 | °C | |
| Soldering Temp | - | +260 | °C | 40 sec max. |
| ESD | - | | V | |
| НВМ | | 4000 | | |
| ММ | | 200 | | |
| CDM | | 1500 | | |

Ordering Code



* See Ordering Information for details

Recommended Operating Conditions

| Parameter | Symbol | Range |
|---|-----------------|---|
| Supply Voltage | V _{DD} | 1.71 - 3.60V |
| Output Load | ZL | R>10KΩ, C≤15pF |
| Operating Temperature Option 1 Option 2 Option 3 Option 4 | т | -40 to +105 °C -40 to +85 °C -20 to +70 °C 0 to +70 °C |

Specifications (VDD = 1.8 to 3.3v) T_A= $85^{\circ}C$ unless otherwise specified

| Parameter | Symbol | Condition | Min | Тур | Мах | Unit |
|--|------------------------------------|---|---------------------------|-----|----------------------------|-------|
| Frequency | f ₀ | Single Frequency | 1 | | 150 | MHz |
| Frequency Tolerance | Δf | Includes frequency variations due to initial tolerance, temperature and power supply voltage | | | ±10,±25,±50 | ppm |
| Aging | Δf | 1 year @25°C | | | ±5 | ppm |
| Supply Current, standby | I _{DD} | T=25°C | | | 15 | uA |
| Output Logic Levels Output logic high Output logic low | V _{OH} V _{OL} | -4mA 4mA | 0.8*V _{DD} - | | - 0.2*V _{DD} | Volts |
| Output Startup Time ² | t _{su} | T=25°C | | 1.0 | 1.3 | ms |
| Output Disable Time | t _{DA} | | | 20 | 100 | ns |
| Output Duty Cycle | SYM | | 45 | | 55 | % |
| Input Logic Levels Input logic high Input logic low | V _{IH} V _{IL} | | 0.75*V _{DD} - | | - 0.25* V _{DD} | Volts |

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VDD = 1.8v

| Parameter | Symbol | Condition | | Min | Тур | Max | Unit |
|----------------------------|-----------------|-----------------------------|-------------|-----|-----|-----|------|
| | | C _L =0p | 1MHz | | 6.0 | 6.3 | |
| Cumply Current no lood | т | $R_1 = \infty$ | 27MHz | | 6.5 | 6.9 | |
| Supply Current, no load | I_{DD} | к _L =∞ T=25°С | 70MHz | | 7.2 | 7.5 | mA |
| | 1= | 1-25°C | 150MHz | | 8.3 | 9.1 | IIIA |
| Output Transition time | | | | | | | |
| Rise Time | t _R | C _L =1 | 5pF; T=25°C | | 1.8 | 3 | |
| Fall Time | t _F | 20%/80%*V _{DD} | | | 1.0 | 3 | ns |
| Jitter, Max Cycle to Cycle | J _{CC} | $F = 100 MHz^3$ | | | 60 | | Ps |

VDD = 2.5v

| Parameter | Symbol | Condition | | Min | Тур | Max | Unit |
|----------------------------|-----------------|-----------------------------|-------------|-----|-----|------|------|
| | | C _L =0p | 1MHz | | 6.0 | 6.3 | |
| Sumply Current no load | т | $R_1 = \infty$ | 27MHz | | 6.7 | 7.0 | |
| Supply Current, no load | I_{DD} | к _L =∞ T=25°С | 70MHz | | 7.7 | 8.1 | |
| | | 1=25°C | 150MHz | | 9.6 | 10.6 | mA |
| Output Transition time | | | | | | | |
| Rise Time | t _R | C _L =1 | 5pF; T=25°C | | 1.0 | 2 | |
| Fall Time | t _F | 20%/80%*V _{DD} | | | 0.9 | 2 | ns |
| Jitter, Max Cycle to Cycle | J _{CC} | $F = 100 MHz^3$ | | | 50 | | ps |

VDD = 3.3v

| Parameter | Symbol | Condition | | Min | Тур | Max | Unit |
|--|----------------------------------|---|----------------------------------|-----|---------------------------|---------------------------|------|
| Supply Current, no load | I _{DD} | C _L =0p R _L =∞ T=25°C | 1MHz 27MHz 70MHz 150MHz | | 6.0 6.8 8.2 10.8 | 6.3 7.2 8.7 12.2 | mA |
| Output Transition time Rise Time Fall Time | t _R t _F | C _L =15pF; T=25°C 20%/80%*V _{DD} | | | 1.0 0.9 | 2 2 | ns |
| Jitter, Max Cycle to Cycle | J _{CC} | F = | = 100MHz ³ | | 50 | | ps |

Notes:

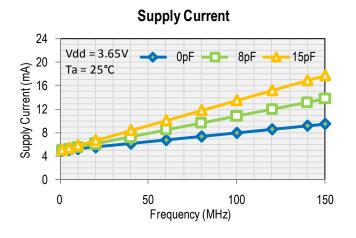
1. Absolute maximum ratings are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated beyond these limits.

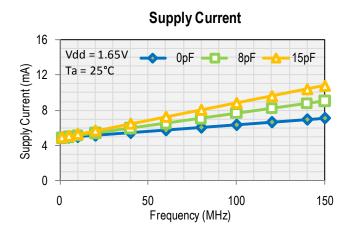
2. t_{SU} is time to stable output frequency after V_{DD} is applied. t_{SU} and t_{EN} (after EN is asserted) are identical values.

3. Measured over 50k clock cycles.

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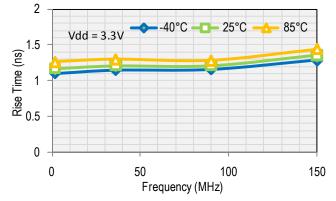
Nominal Performance Characteristics

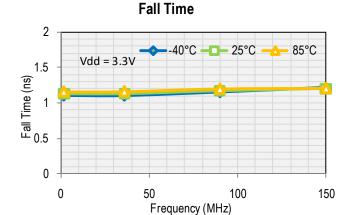


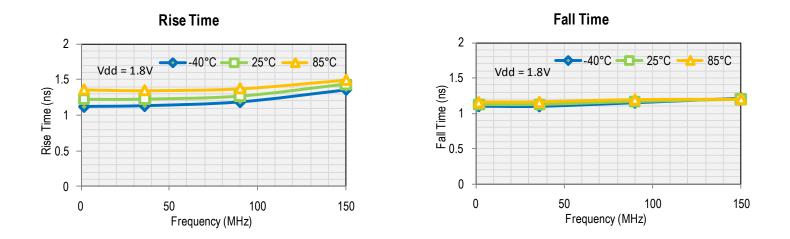


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Rise Time



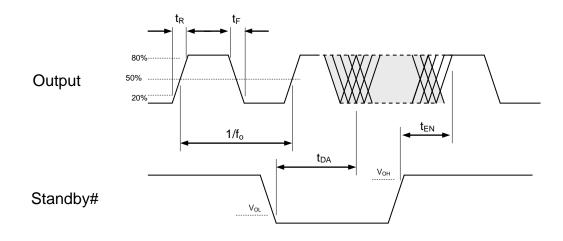




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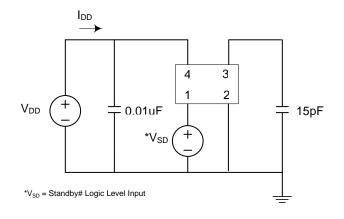
Output Waveform



Standby Function

| Standby# (pin 1) | Output (pin 3) |
|----------------------|-------------------|
| Hi Level | Output ON |
| Open (no connect) | Output ON |
| Low Level | High Impedance |

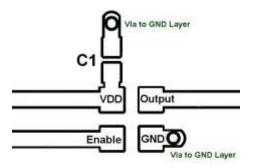
Test Circuit



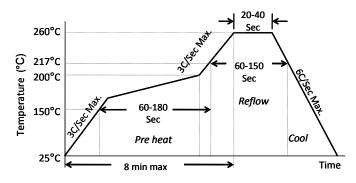
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Board Layout (recommended)



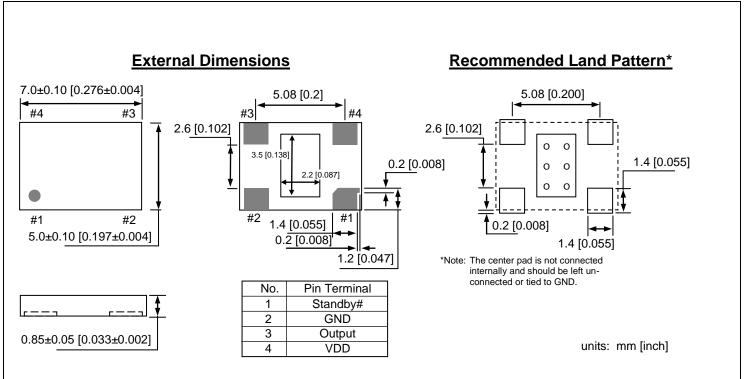
Solder Reflow Profile



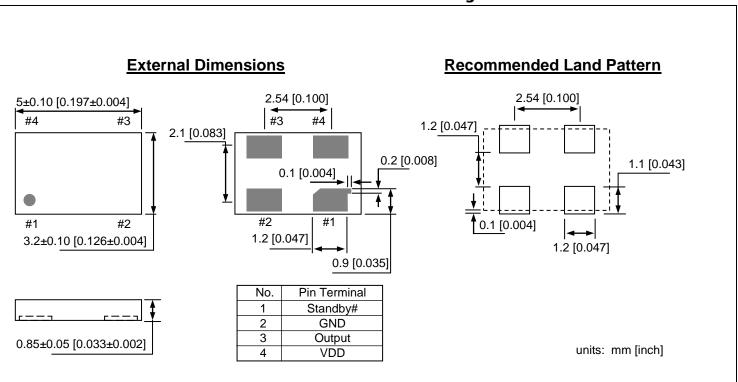
| MSL 1 @ 260°C refer to JSTD-020C | | | | | |
|-----------------------------------|--------------|--|--|--|--|
| Ramp-Up Rate (200°C to Peak Temp) | 3°C/Sec Max. | | | | |
| Preheat Time 150°C to 200°C | 60-180 Sec | | | | |
| Time maintained above 217°C | 60-150 Sec | | | | |
| Peak Temperature | 255-260°C | | | | |
| Time within 5°C of actual Peak | 20-40 Sec | | | | |
| Ramp-Down Rate | 6°C/Sec Max. | | | | |
| Time 25°C to Peak Temperature | 8 min Max. | | | | |

Package Dimensions



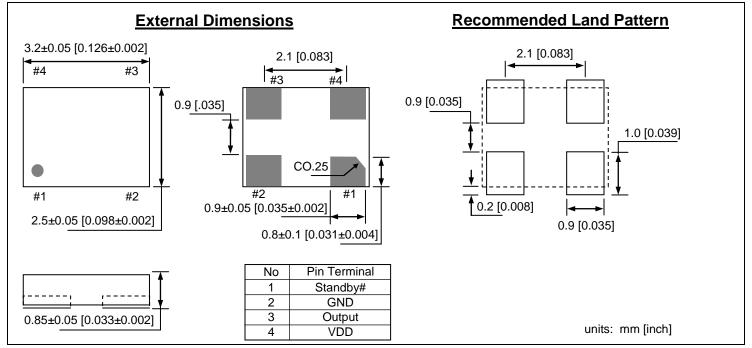


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5.0 x 3.2 mm Plastic Package

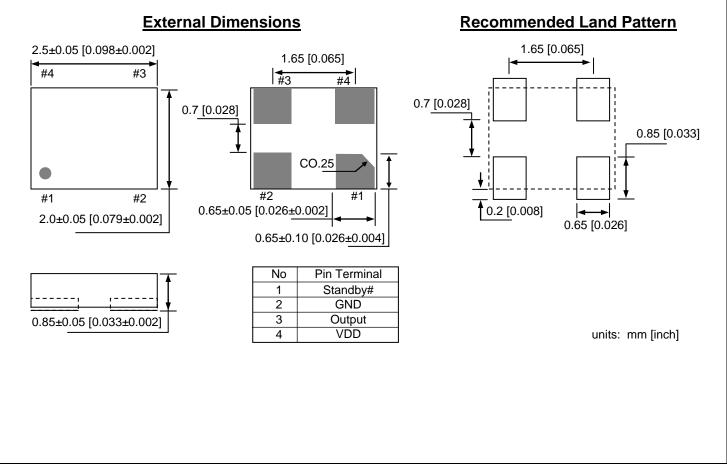




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2.5 x 2.0 mm Plastic Package

Ordering Information

DSC8001 Series

DSC8001 PTS - T

| PART NUMBERING GUIDE | | | | | | | |
|--|---|--|--------------------------------|--|--|--|--|
| Package (Plastic QFN)TemperatureStabilityPacking Option | | | | | | | |
| P=A: 7.0x5.0mm P=B: 5.0x3.2mm P=C: 3.2x2.5mm P=D: 2.5x2.0mm | T=C: $0^{\circ} \sim +70^{\circ} C$ T=E: $-20^{\circ} \sim +70^{\circ} C$ T=I: $-40^{\circ} \sim +85^{\circ} C$ T=L: $-40^{\circ} \sim +105^{\circ} C$ | S=1: ±50ppm S=2: ±25ppm S=5: ±10ppm | Blank: Tubes T: Tape & Reel | | | | |

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