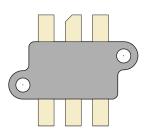


# ARF300

125V, 300W, 45MHz

#### RF POWER MOSFET N-CHANNEL ENHANCEMENT MODE

The ARF300 is a N-CHANNEL RF power transistor in a high efficiency flangeless package. It is designed for high voltage operation in narrow band ISM and MRI power amplifiers at frequencies up to 45MHz. The transistor is well matched to the ARF301 P-CHANNEL RF power transistor making the pair well suited for bridge configurations



- Specified 125 Volt, 27 MHz Characteristics: Output Power = 300 Watts. Gain = 15dB (Class E)
  - Efficiency = 80%
  - RoHS Compliant J

- High Performance
- High Voltage Breakdown and Large SOA
- for Superior Ruggedness
- Low Thermal Resistance.
- Capacitance matched with ARF301 P-Channel

#### All Ratings: T<sub>c</sub>=25°C unless otherwise specified Maximum Ratings Symbol Parameter Ratings Unit V<sub>DSS</sub> Drain-Source Voltage 500 V V<sub>DGO</sub> Drain-Gate Voltage 500 $I_{\rm D}$ Continuous Drain Current @ T<sub>c</sub> = 25°C 24 А $V_{GS}$ Gate-Source Voltage ±30 V $\mathsf{P}_{\mathsf{D}}$ Total Power Dissipation @ T<sub>c</sub> = 25°C 1000 W T<sub>J</sub>, T<sub>STG</sub> -55 to 175 Operating and Storage Junction Temperature Range °C T, Lead Temperature: 0.063" from Case for 10 Sec. 300

#### **Static Electrical Characteristics**

Symbol	Parameter	Min	Тур	Max	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage ( $V_{GS}$ = 0V, $I_{D}$ = 250 µA)	500			V
V <sub>DS(ON)</sub>	On State Drain Voltage <sup>1</sup> (I <sub>D(ON)</sub> = 12A, V <sub>GS</sub> = 10V)		3	4	v
	Zero Gate Voltage Drain Current ( $V_{DS} = V_{DSS}$ , $V_{GS} = 0V$ )			25	
DSS	Zero Gate Voltage Drain Current ( $V_{DS} = 50V_{DSS}$ , $V_{GS} = 0$ , $T_{C} = 125^{\circ}C$ )			250	μA
I <sub>GSS</sub>	Gate-Source Leakage Current ( $V_{DS}$ = ±30V, $V_{DS}$ = 0V)			±100	nA
9 <sub>fs</sub>	Forward Transconductance ( $V_{DS}$ = 15V, $I_{D}$ = 12A)	5	8		mhos
V <sub>GS(TH)</sub>	Gate Threshold Voltage ( $V_{DS} = V_{GS}$ , $I_{D} = 10$ mA)	2.5	4	5	Volts

#### **Thermal Characteristics**

Symbol	Parameter	Min	Тур	Max	Unit
$R_{_{ ext{ extbf{ heta}JC}}}$	Junction to Case			0.15	°C/W
R <sub>ejhs</sub>	Junction to Sink (High Efficiency Thermal Joint Compound and Planar Heat Sink Surface.)			0.27	C/W

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

**Dynamic Characteristics** 

**ARF300** 

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C <sub>ISS</sub>	Input Capacitance	$V_{GS} = 0V$		1890	2100	
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 50V		350	390	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		75	90	

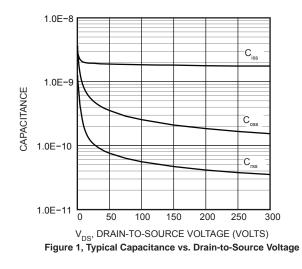
#### **Functional Characteristics**

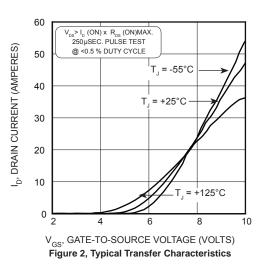
Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
G <sub>PS</sub>	Common Source Amplifier Power Gain	f = 27MHz	15	17		dB
η	Drain Efficiency	I <sub>dq</sub> = 0mA V <sub>DD</sub> = 125V	80	85		%
Ψ	Electrical Ruggedness VSWR 10:1	P <sub>out</sub> = 300W		No Da	mage	

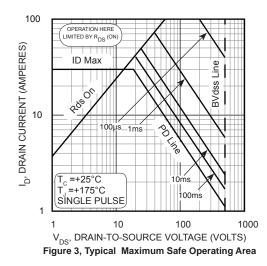
1. Pulse Test: Pulse width < 380 µS, Duty Cycle < 2%.

Microchip reserves the right to change, without notice, the specifications and information contained herein.

#### **Dynamic Characteristics**

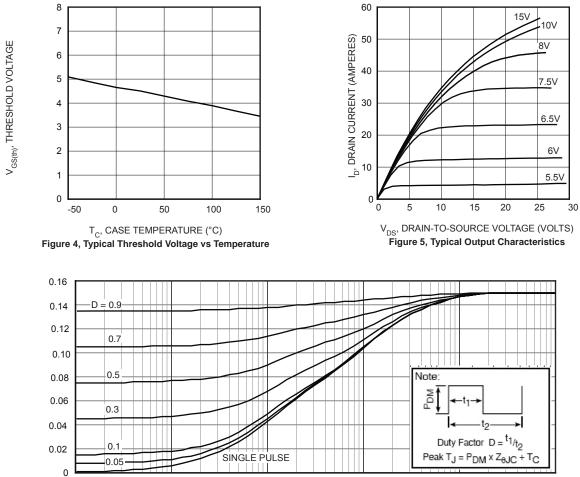






#### **Dynamic Characteristics**







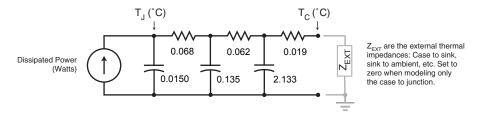


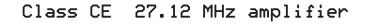
Figure 6b, TRANSIENT THERMAL IMPEDANCE MODEL

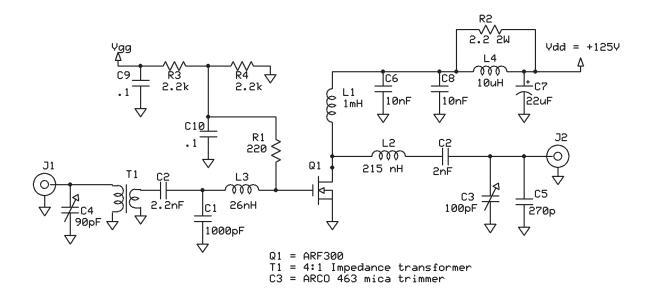
Table 1 - Typical Class AB Large Signal Input - Output Impedance

Freq. (MHz)	Ζ <sub>in</sub> (Ω)	Z <sub>OL</sub> (Ω)
2.0	18 - j 10.5	21 - j 1.4
13.56	2.66 - j 4.6	17.5 - j 7.8
27.12	1.79 - j 1.6	11.7 - j 10.4
40.68	1.68 - j 0.14	7.7 - j 10

 $\mathbf{Z}_{\text{IN}}$  - Gate shunted with 25 $\Omega$   $\mathbf{I}_{\text{dq}}$  = 0

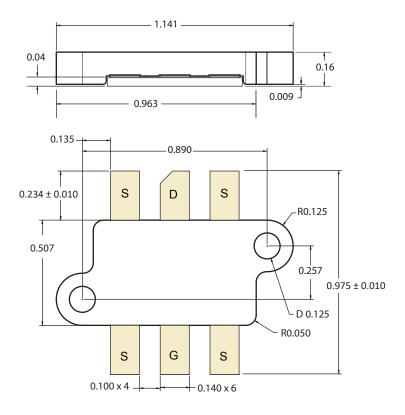
 $Z_{oL}$  - Conjugate of optimum load for 300 Watts output at V<sub>dd</sub>=125V





# T11 Package Outline

Dimensions are in inches (± 0.005" unless otherwise specified)



Use 4-40 (M3) screws for mounting. Torque = 4-6 in-lb (0.45- 0.7 Nm).



ATTENTION: This is a high power device. Special considerations must be followed in mounting to ensure proper operation of these devices. Incorrect mounting can cause internal temperatures to exceed the maximum allowable operating junction temperature.

Refer to Microchip Application Note #1810 before starting system design.

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