Fast Diode Phase-Leg Power Module

APTDF200A120D16AG

Product Overview

The APTDF200A120D16AG device is a 1200V, 200A fast diode phase-leg power module.

The following figures show the electrical diagram and pinout location of the device.

Figure 1. Electrical Diagram

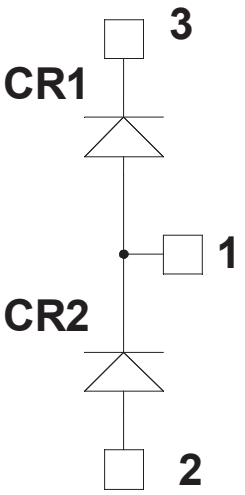
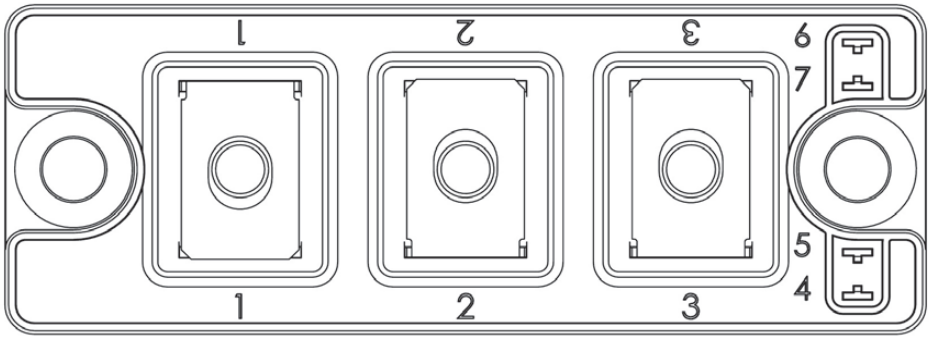


Figure 2. Pinout Location



Note: All ratings are at T_J = 25 °C, unless otherwise specified.

CAUTION These devices are sensitive to electrostatic discharge. Proper handling procedures must be followed.

Features

The APTDF200A120D16AG device has the following key features:

- Fast-recovery times
- Soft-recovery characteristics
- High-blocking voltage
- High current
- Low-leakage current
- M6 power connectors
- Aluminum Nitride (AlN) substrate for improved thermal performance

Benefits

The APTDF200A120D16AG device has the following benefits:

- Outstanding performance at high frequency operation
- Low losses
- Low-noise switching
- Direct mounting to heatsink (isolated package)
- Low junction-to-case thermal resistance
- RoHS Compliant

Application

The APTDF200A120D16AG device has the following applications:

- Uninterruptible Power Supply (UPS)
- Induction heating
- Welding equipment
- High-speed rectifiers

1. Electrical Specification

The following sections describe the electrical specifications of the APTDF200A120D16AG device.

1.1 Diode Characteristics (Per Diode)

The following table lists the absolute maximum ratings of the APTDF200A120D16AG device.

Table 1-1. Absolute Maximum Ratings

Symbol	Parameter			Maximum Ratings	Unit
V_{RRM}	Peak repetitive reverse voltage			1200	V
I_F	DC forward current		T_C = 25 °C	410	A
			T_C = 115 °C	200	
I_{FSM}	Non-repetitive forward surge current	t_P = 8.3 ms	T_C = 45 °C	1000	
I_{FRM}	Repetitive forward current	t_P = 1 ms	—	400	

The following table lists the electrical characteristics of the APTDF200A120D16AG device.

Table 1-2. Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
V_F	Diode forward voltage	I_F = 200A		—	2.4	3.5	V
		I_F = 300A		—	2.7	—	
		I_F = 200A	T_J = 125 °C	—	1.8	—	
I_{RRM}	Reverse leakage current	V_R = 1200V		—	—	200	µA
C_T	Junction capacitance	V_R = 200V		—	220	—	pF

The following table lists the dynamic characteristics of the APTDF200A120D16AG device.

Table 1-3. Dynamic Characteristics

Symbol	Characteristic	Test Conditions		Min.	Typ.	Max.	Unit
t_{rr}	Reverse recovery time	I_F = 200A V_R = 800V di/dt = 400 A/µs	T_J = 25 °C	—	385	—	ns
			T_J = 125 °C	—	480	—	
Q_{rr}	Reverse recovery charge		T_J = 25 °C	—	2.1	—	µC
			T_J = 125 °C	—	10.5	—	
I_{rm}	Reverse recovery current		T_J = 25 °C	—	12	—	A
			T_J = 125 °C	—	38	—	
t_{rr}	Reverse recovery time	I_F = 200A V_R = 800V di/dt = 2000 A/µs	T_J = 125 °C	—	210	—	ns
Q_{rr}	Reverse recovery charge			—	19	—	µC
I_{rm}	Reverse recovery current			—	140	—	A
R_{thJC}	Junction-to-case thermal resistance			—	—	0.135	°C/W

1.2 Thermal and Package Characteristics

The following table lists the thermal and package characteristics of the APTDF200A120D16AG device.

Table 1-4. Thermal and Package Characteristics

Symbol	Characteristic			Min.	Max.	Unit
V_{ISOL}	RMS isolation voltage, any terminal to case, t = 1 min, 50/60Hz			4000	—	V
T_J	Operating junction temperature range			–40	175	°C
T_{JOP}	Recommended junction temperature under switching conditions			–40	T_{Jmax} –25	
T_{STG}	Storage temperature range			–40	125	
T_C	Operating case temperature			–40	125	
Torque	Mounting torque	For terminals	M6	3	5	N.m
		To heatsink				
Wt	Package weight			—	160	g

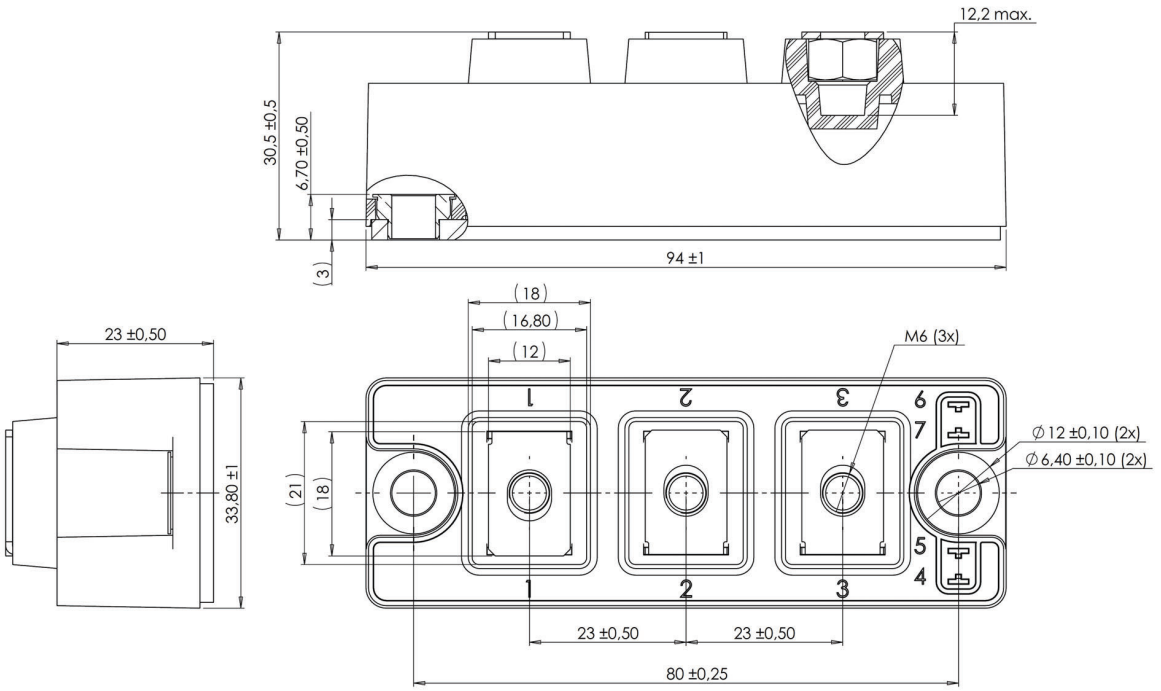
2. Package Specifications

The following section describes the package specification of the APTDF200A120D16AG device.

2.1 Package Outline

The following figure shows the package outline drawing of the APTDF200A120D16AG device. The dimensions in the following figure are in millimeters.

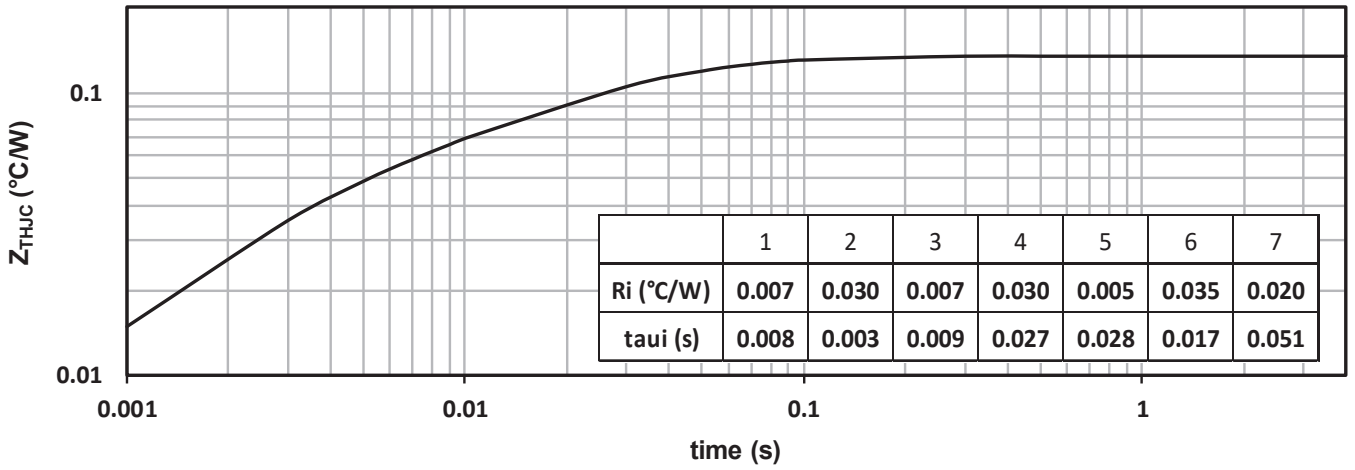
Figure 2-1. Package Outline Drawing



3. Typical Performance Curve

The following figures show the performance curves of the APTDF200A120D16AG device.

Figure 3-1. Maximum Thermal Impedance



	1	2	3	4	5	6	7
Ri (°C/W)	0.007	0.030	0.007	0.030	0.005	0.035	0.020
taui (s)	0.008	0.003	0.009	0.027	0.028	0.017	0.051

Figure 3-2. Forward Current vs. Forward Voltage

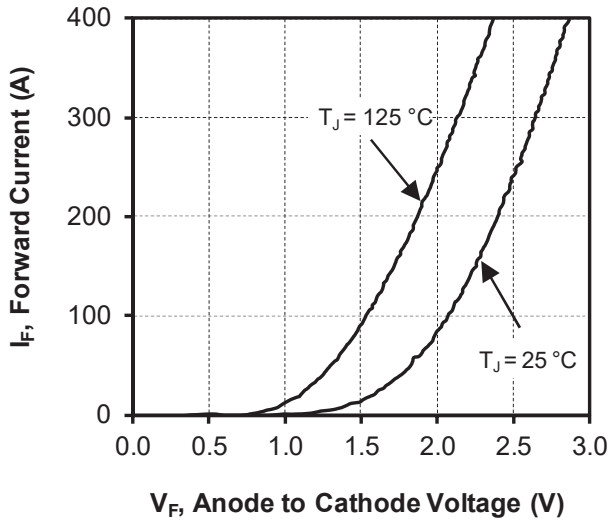


Figure 3-3. t_{rr} vs. Current Rate of Charge

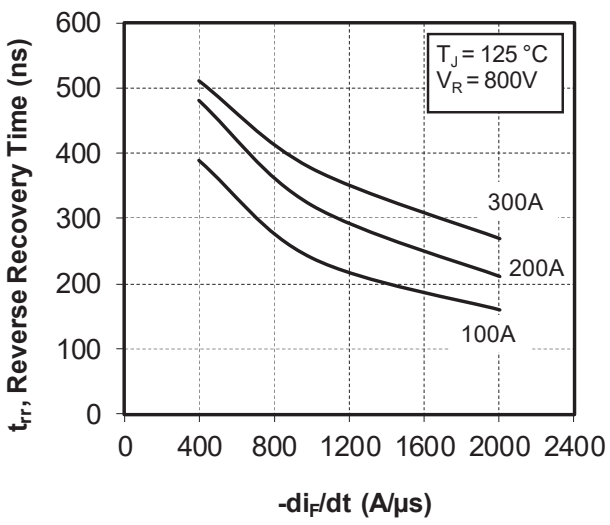


Figure 3-4. Q_{rr} vs. Current Rate Charge

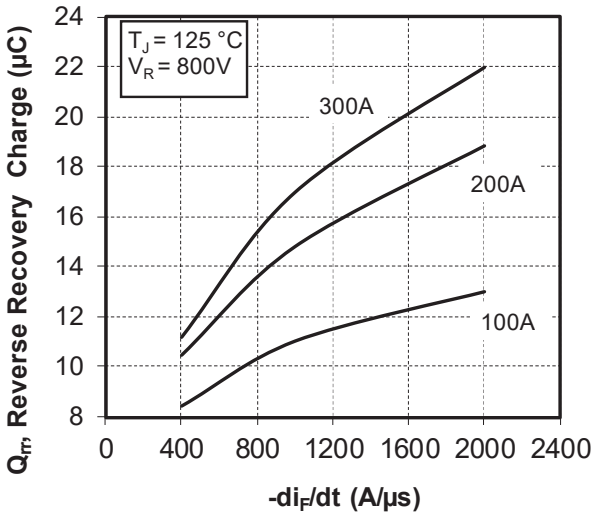


Figure 3-5. I_{RRM} vs. Current Rate of Charge

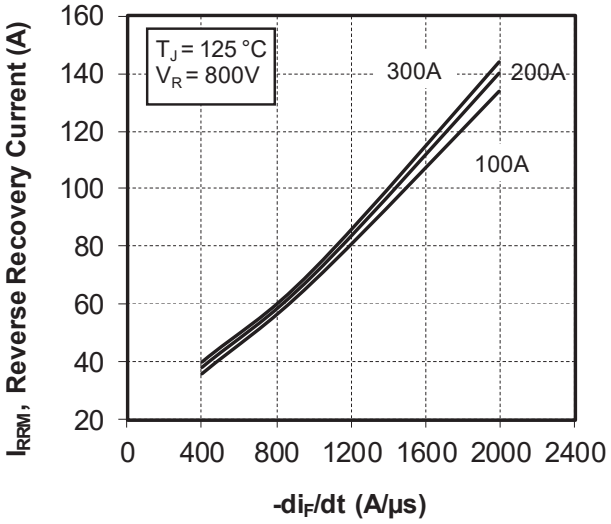


Figure 3-6. Capacitance vs. Reverse Voltage

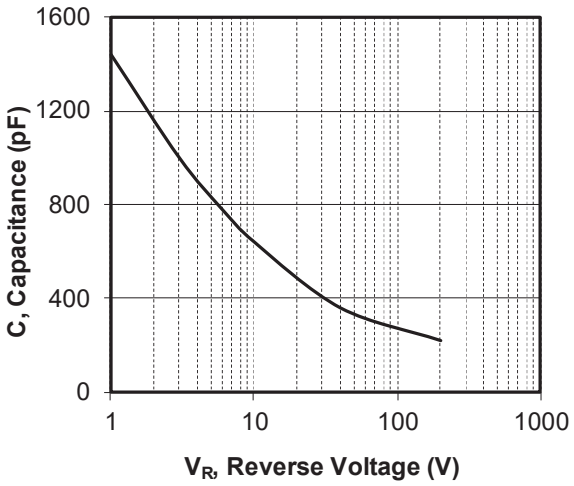
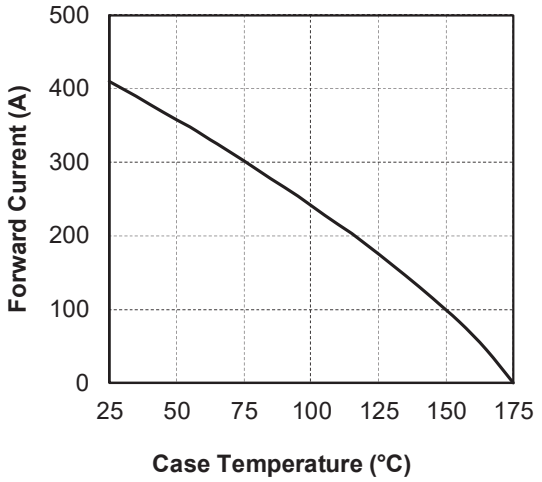


Figure 3-7. Forward Current vs. Case Temperature



4. Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

Revision	Date	Description
A	02/2024	Initial revision

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