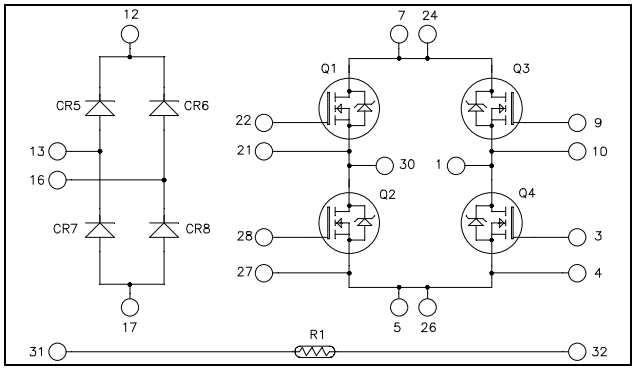
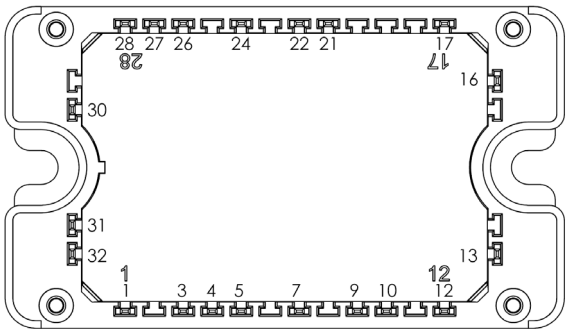
APTC60HM70RT3G

Full bridge + rectifier bridge CoolMOS Power module

CoolMOS™ :
V_{DSS} = 600V
R_{DSon} = 70mΩ max @ Tj = 25°C





All multiple inputs and outputs must be shorted together
7/24 ; 5/26

Application
- Solar converter

Features
- ***CoolMOS™***
  - Ultra low R_{DSon}
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
- Very low stray inductance
- Kelvin source for easy drive
- Internal thermistor for temperature monitoring
- High level of integration

Benefits
- Optimized conduction & switching losses
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- Easy paralleling due to positive T_C of V_{CEsat}
- RoHS Compliant

All ratings @ T_j = 25°C unless otherwise specified

1. Full bridge

Absolute maximum ratings (Per CoolMOS™)

Symbol	*Parameter*		*Max ratings*	*Unit*
V_{DSS}	Drain - Source Breakdown Voltage		600	V
I_D	Continuous Drain Current	T_c = 25°C	39	A
		T_c = 80°C	29	
I_{DM}	Pulsed Drain current		160	
V_{GS}	Gate - Source Voltage		±20	V
R_{DSon}	Drain - Source ON Resistance		70	mΩ
P_D	Maximum Power Dissipation	T_c = 25°C	250	W
I_{AR}	Avalanche current (repetitive and non repetitive)		20	A
E_{AR}	Repetitive Avalanche Energy		1	mJ
E_{AS}	Single Pulse Avalanche Energy		1800	

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handing Procedures Should Be Followed. See application note APT0502 on www.microsemi.com

Electrical Characteristics (Per CoolMOS™)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_{DSS}	Zero Gate Voltage Drain Current	V_{GS} = 0V, V_{DS} = 600V	T_j = 25°C			25	µA
		V_{GS} = 0V, V_{DS} = 600V	T_j = 125°C			250	
$R_{DS(on)}$	Drain – Source on Resistance	V_{GS} = 10V, I_D = 39A				70	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS} = V_{DS}, I_D = 2.7mA		2.1	3	3.9	V
I_{GSS}	Gate – Source Leakage Current	V_{GS} = ±20 V, V_{DS} = 0V				±100	nA

Dynamic Characteristics (Per CoolMOS™)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
C_{iss}	Input Capacitance	V_{GS} = 0V V_{DS} = 25V f = 1MHz			7		nF
C_{oss}	Output Capacitance				2.56		
C_{rss}	Reverse Transfer Capacitance				0.21		
Q_g	Total gate Charge	V_{GS} = 10V V_{Bus} = 300V I_D = 39A			259		nC
Q_{gs}	Gate – Source Charge				29		
Q_{gd}	Gate – Drain Charge				111		
$T_{d(on)}$	Turn-on Delay Time	**Inductive Switching @ 125°C** V_{GS} = 15V V_{Bus} = 400V I_D = 39A R_G = 5Ω			21		ns
T_r	Rise Time				30		
$T_{d(off)}$	Turn-off Delay Time				283		
T_f	Fall Time				84		
E_{off}	Turn-off Switching Energy	V_{GS} = 15V V_{Bus} = 400V I_D = 39A R_G = 5Ω	T_j = 25°C		980		µJ
E_{off}	Turn-off Switching Energy		T_j = 125°C		1206		
R_{thJC}	Junction to Case Thermal resistance					0.5	°C/W

Source - Drain diode ratings and characteristics (Per CoolMOS™)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
I_S	Continuous Source current (Body diode)		Tc = 25°C		39		A
			Tc = 80°C		29		
V_{SD}	Diode Forward Voltage	V_{GS} = 0V, I_S = - 39A				1.2	V
dv/dt	Peak Diode Recovery ❶					6	V/ns
t_{rr}	Reverse Recovery Time	I_S = - 39A V_R = 350V di_S/dt = 100A/µs	T_j = 25°C		580		ns
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		23		µC

❶ dv/dt numbers reflect the limitations of the circuit rather than the device itself.
$I_S \le$ - 39A di/dt ≤ 100A/µs $V_R \le V_{DSS}$ $T_j \le$ 150°C

2. Rectifier bridge

Absolute maximum ratings (per diode)

Symbol	*Parameter*			*Max ratings*	*Unit*
V_R	Maximum DC reverse Voltage			600	V
V_{RRM}	Maximum Peak Repetitive Reverse Voltage				
$I_{F(AV)}$	Maximum Average Forward Current	Duty cycle = 50%	T_C = 80°C	40	A
I_{FSM}	Non-Repetitive Forward Surge Current	8.3ms	T_J = 45°C	320	

Electrical Characteristics (per diode)

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
V_F	Diode Forward Voltage	I_F = 30A			1.8	2.2	V
		I_F = 60A			2.2		
		I_F = 30A	T_j = 125°C		1.5		
I_{RM}	Maximum Reverse Leakage Current	V_R = 600V	T_j = 25°C			250	µA
			T_j = 125°C			500	

Dynamic Characteristics (per diode)

Symbol	*Characteristic*	*Test Conditions*		*Min*	*Typ*	*Max*	*Unit*
t_{rr}	Reverse Recovery Time	I_F=1A,V_R=30V di/dt = 100A/µs	T_j = 25°C		22		ns
t_{rr}	Reverse Recovery Time	I_F = 30A V_R = 400V di/dt = 200A/µs	T_j = 25°C		25		ns
			T_j = 125°C		160		
Q_{rr}	Reverse Recovery Charge		T_j = 25°C		35		nC
			T_j = 125°C		480		
I_{RRM}	Reverse Recovery Current		T_j = 25°C		3		A
			T_j = 125°C		6		
t_{rr}	Reverse Recovery Time	I_F = 30A V_R = 400V di/dt = 1000A/µs	T_j = 125°C		85		ns
Q_{rr}	Reverse Recovery Charge				920		µC
I_{RRM}	Reverse Recovery Current				20		A
R_{thJC}	Junction to Case Thermal Resistance					1.2	°C/W

3. Thermal and package characteristics

Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	*Characteristic*		*Min*	*Typ*	*Max*	*Unit*
R_{25}	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
$B_{25/85}$	T_{25} = 298.15 K			3952		K
$\Delta B/B$		T_C=100°C		4		%

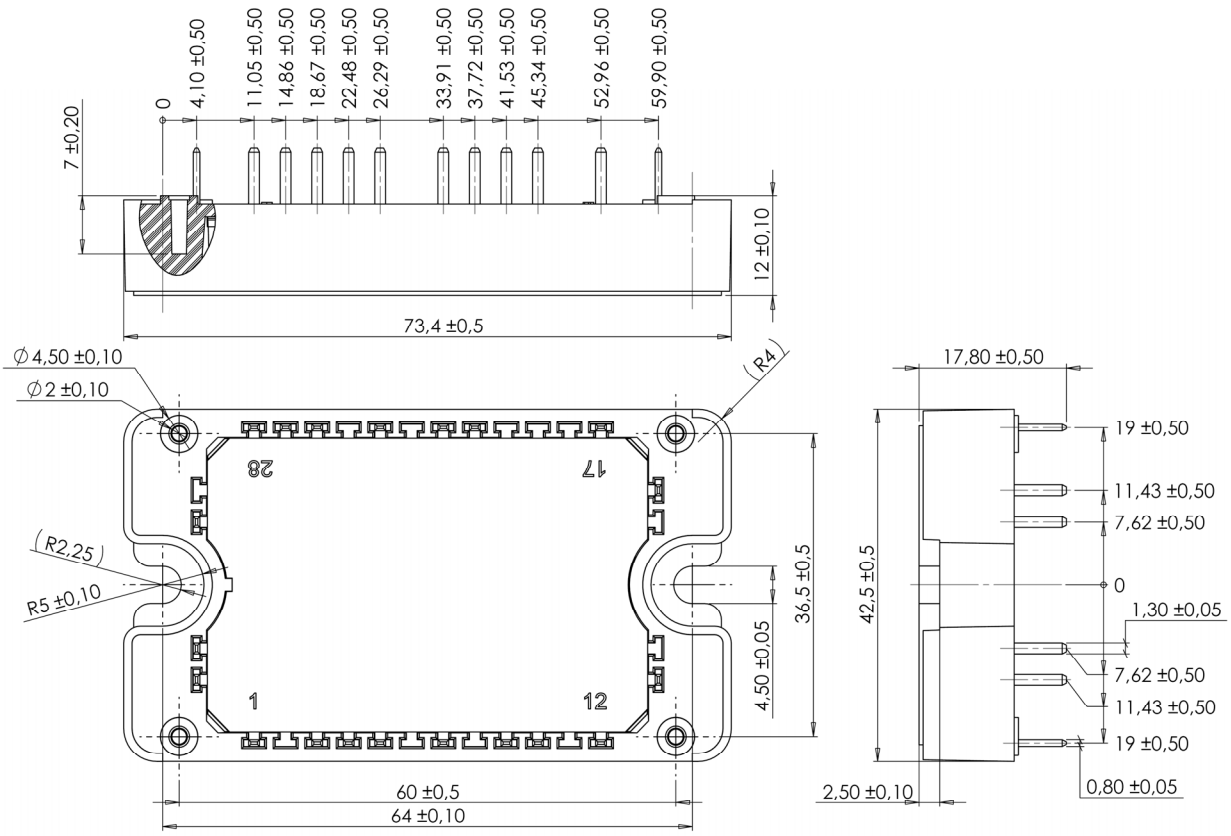
$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

T: Thermistor temperature
R_T: Thermistor value at T

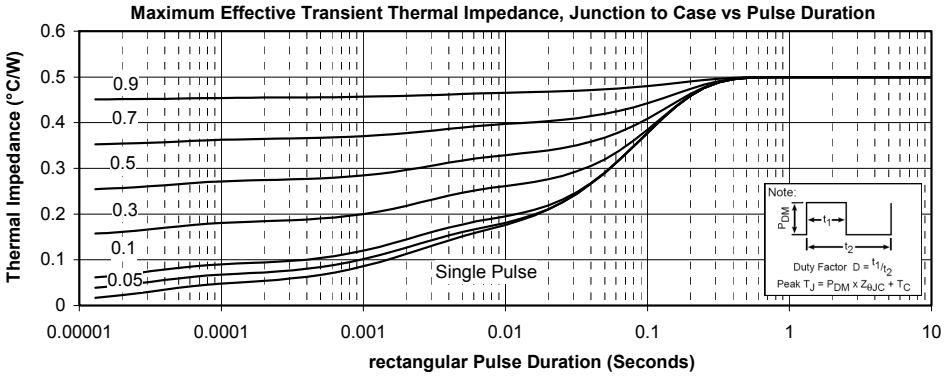
Package characteristics

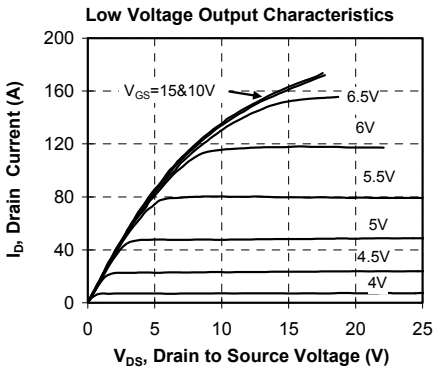
Symbol	*Characteristic*			*Min*	*Typ*	*Max*	*Unit*
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_J	Operating junction temperature range			-40		150	°C
T_{STG}	Storage Temperature Range			-40		125	
T_C	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M4	2		3	N.m
Wt	Package Weight					110	g

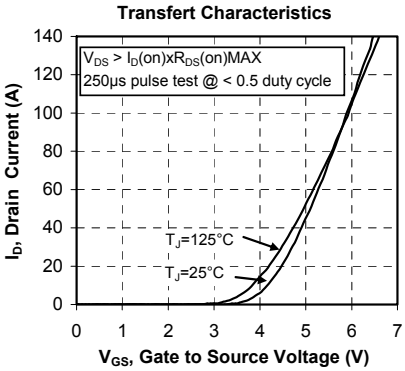
4. SP3F Package outline (dimensions in mm)

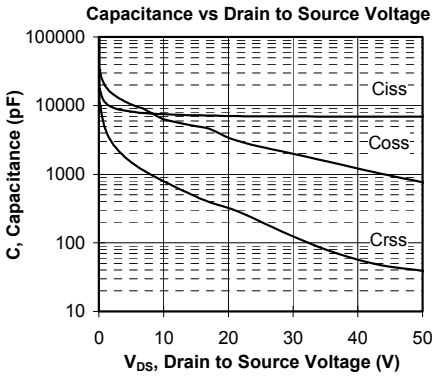


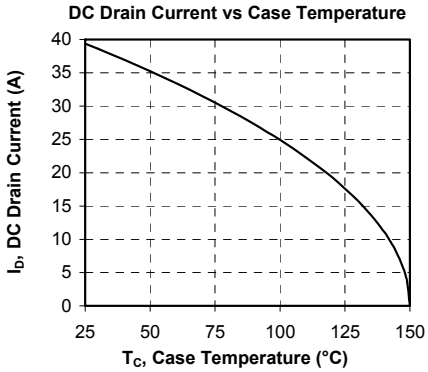
5. Full bridge switches curves (Per CoolMOS™)

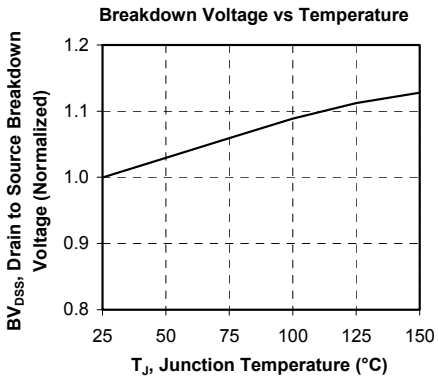


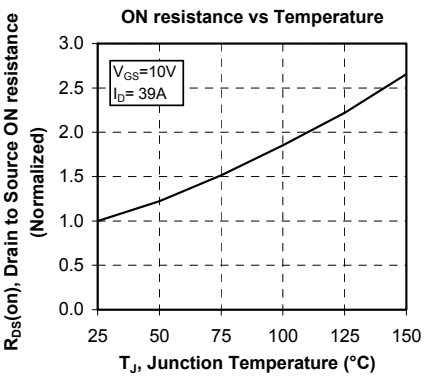




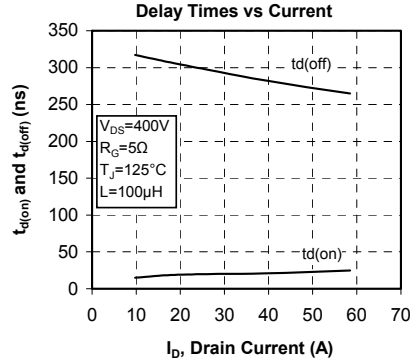




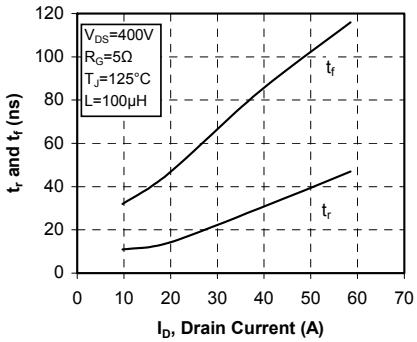




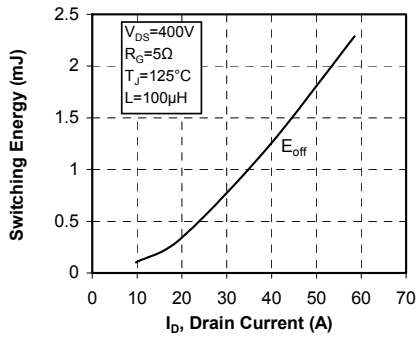
Delay Times vs Current



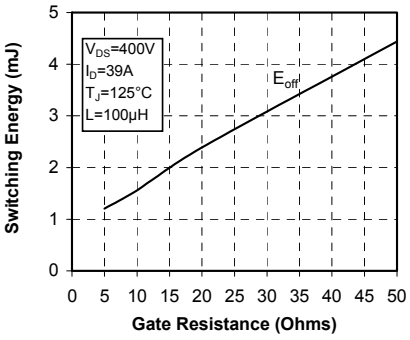
Rise and Fall times vs Current



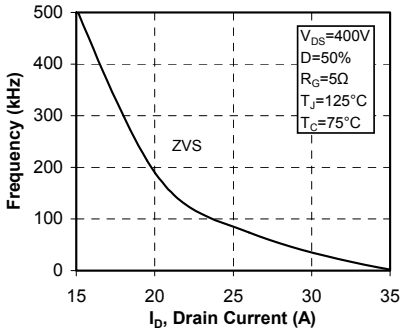
Switching Energy vs Current



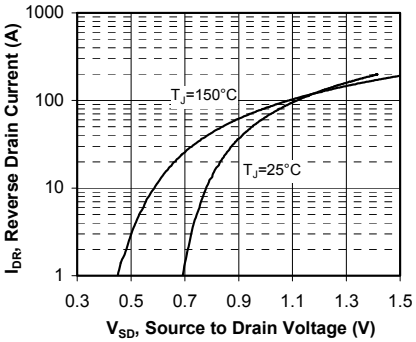
Switching Energy vs Gate Resistance



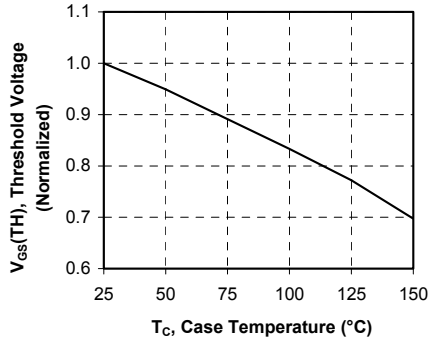
Operating Frequency vs Drain Current



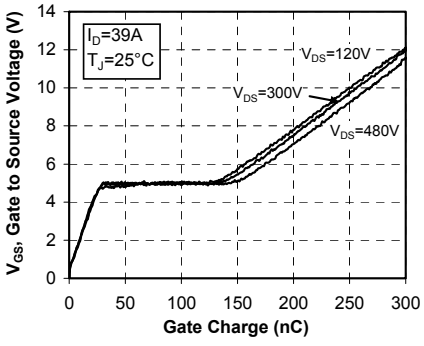
Source to Drain Diode Forward Voltage



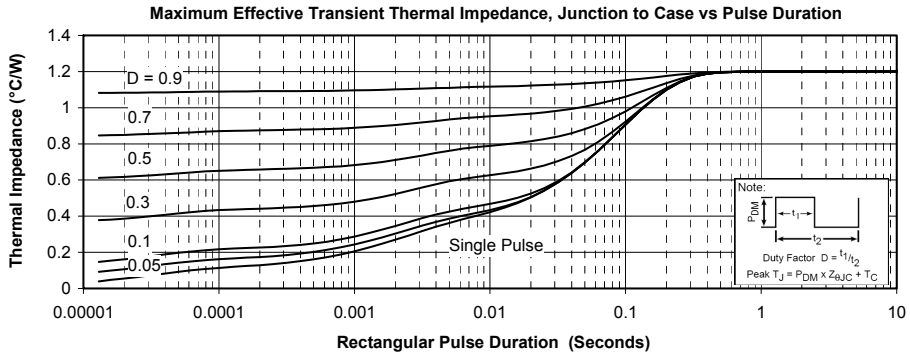
Threshold Voltage vs Temperature

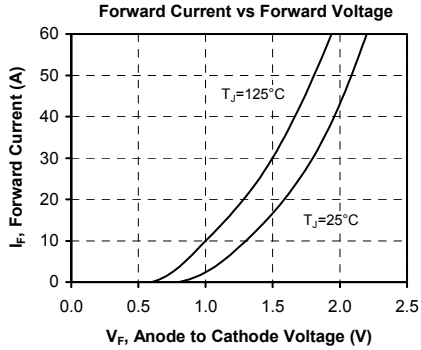


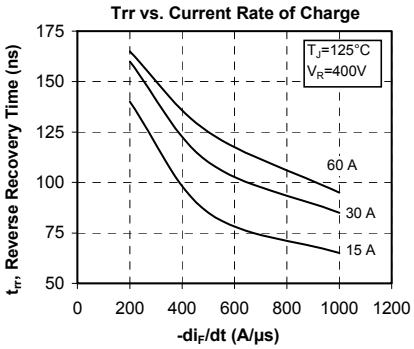
Gate Charge vs Gate to Source Voltage

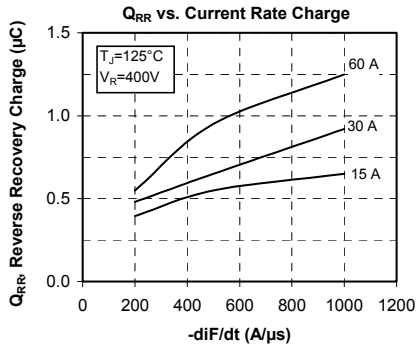


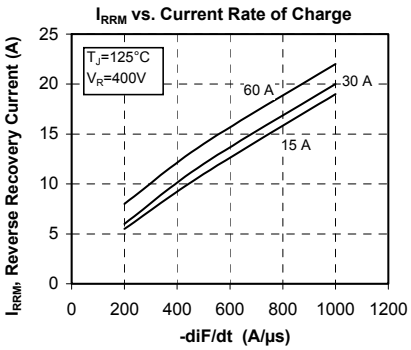
6. Typical rectifier bridge Performance Curve (per diode)

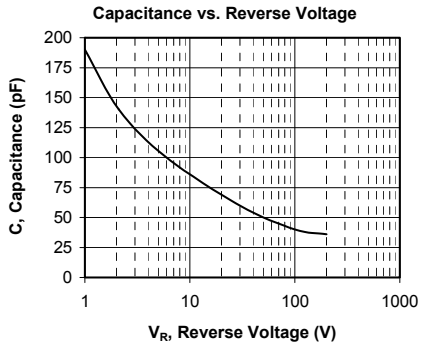












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