

1200V, 70A, $V_{ce(on)} = 2.5V$ Typical

Ultra Fast NPT - IGBT®

The Ultra Fast NPT - IGBT® is a new generation of high voltage power IGBTs. Using Non-Punch-Through Technology, the Ultra Fast NPT-IGBT® offers superior ruggedness and ultrafast switching speed.

Features

- Low Saturation Voltage
- Low Tail Current
- RoHS Compliant

- Short Circuit Withstand Rated
- · High Frequency Switching
- Ultra Low Leakage Current

Unless stated otherwise, Microsemi discrete IGBTs contain a single IGBT die. This device is recommended for applications such as induction heating (IH), motor control, general purpose inverters and uninterruptible power supplies (UPS).



MAXIMUM RATINGS

All Ratings: $T_C = 25$ °C unless otherwise specified.

Symbol	Parameter	Ratings	Unit
V _{ces}	Collector Emitter Voltage	1200	V
$V_{\rm GE}$	Gate-Emitter Voltage	±30	V
I _{C1}	Continuous Collector Current @ T _c = 25°C	112	
I _{C2}	Continuous Collector Current @ T _C = 86°C	70	Α
I _{CM}	Pulsed Collector Current ①	280	
SCWT	Short Circuit Withstand Time: V _{CE} = 600V, V _{GE} = 15V, T _C =125°C	10	μs
P _D	Total Power Dissipation @ T _c = 25°C	543	W
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	

STATIC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Min	Тур	Max	Unit
V _{(BR)CES}	Collector-Emitter Breakdown Voltage (V _{GE} = 0V, I _C = 1.0mA)	1200			
V _{GE(TH)}	Gate Threshold Voltage $(V_{CE} = V_{GE}, I_{C} = 2.5 \text{mA}, T_{j} = 25 ^{\circ}\text{C})$	3.5	5.0	6.5	\
V _{CE(ON)}	Collector-Emitter On Voltage (V _{GE} = 15V, I _C = 70A, T _j = 25°C)		2.5	3.2	Volts
	Collector-Emitter On Voltage ($V_{GE} = 15V$, $I_{C} = 70A$, $T_{j} = 125$ °C)		3.3		
	Collector-Emitter On Voltage $(V_{GE} = 15V, I_{C} = 140A, T_{j} = 25^{\circ}C)$		3.5		
I _{CES}	Collector Cut-off Current $(V_{CE} = 1200V, V_{GE} = 0V, T_j = 25^{\circ}C)$ ②		20	1100	μA
	Collector Cut-off Current (V _{CE} = 1200V, V _{GE} = 0V, T _j = 125°C) ②		200		
I _{GES}	Gate-Emitter Leakage Current (V _{GE} = ±20V)			±250	nA



CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

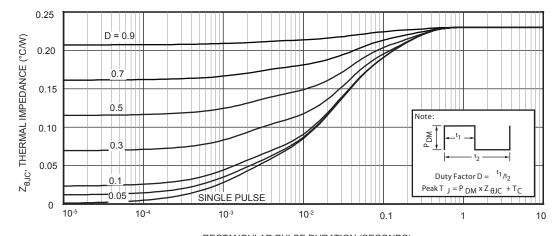
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C _{ies}	Input Capacitance	Capacitance		7260		
C _{oes}	Output Capacitance	$V_{GE} = 0V, V_{CE} = 25V$		643		pF
C _{res}	Reverse Transfer Capacitance	f = 1MHz		199		
V _{GEP}	Gate to Emitter Plateau Voltage	Cata Charra		7.5		V
Qg3	Total Gate Charge	Gate Charge		412	544	
Q_{ge}	Gate-Emitter Charge	V _{GE} = 15V		48	62	"
Q_{gc}	Gate- Collector Charge	$V_{CE} = 600V$ $I_{C} = 70A$		204	275	nC
t _{d(on)}	Turn-On Delay Time	Inductive Switching (25°C)		33		
t,	Current Rise Time	V _{CC} = 600V		48		20
t _{d(off)}	Turn-Off Delay Time	V _{GE} = 15V		278		ns
t _f	Current Fall Time	I _C = 70A		64		
E _{on2} 5	Turn-On Switching Energy	$R_{_{\rm G}} = 4.3 \Omega^{\textcircled{4}}$		3816	5720	1
E _{off}	Turn-Off Switching Energy	T _J = +25°C		2582	3870	μJ
t _{d(on)}	Turn-On Delay Time	Inductive Switching (125°C)		33		
t,	Current Rise Time	V _{CC} = 600V		48		20
$t_{d(off)}$	Turn-Off Delay Time	V _{GE} = 15V		320		ns
t _f	Current Fall Time	I _C = 70A		74		
E _{on2} 5	Turn-On Switching Energy	$R_{_{\rm G}} = 4.3 \ \Omega^{\textcircled{4}}$		5651	8475	1
E _{off}	Turn-Off Switching Energy	T _J = +125°C		3323	4980	μJ

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions	Min	Тур	Max	Unit	
$R_{\theta JC}$	Junction to Case Thermal Resistance (IGBT)	-	-	0.23	°C/\\/	
R _{eJC}	Junction to Case Thermal Resistance (Diode)	-	-	0.56	°C/W	
W _T	Package Weight	-	1.03	-	OZ	
Torque	Terminals and Maunting Caraus	-	-	10	in·lbf	
Torque	Terminals and Mounting Screws.	-	-	1.1	N·m	
V _{Isolation}	RMS Voltage (50-60Hz Sinusoidal Waveform from Terminals to Mounting Base for 1 Min.)	2500	-	-	Volts	

- 1 Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature.
- 2 Pulse test: Pulse Width < $380\mu s$, duty cycle < 2%.
- 3 See Mil-Std-750 Method 3471.
- 4 $R_{\rm g}$ is external gate resistance, not including internal gate resistance or gate driver impedance. (MIC4452)
- 5 E_{on2} is the clamped inductive turn on energy that includes a commutating diode reverse recovery current in the IGBT turn on energy loss. A combi device is used for the clamping diode
- $6~~{\rm E}_{\rm off}$ is the clamped inductive turn-off energy measured in accordance with JEDEC standard JESD24-1.

Microsemi reserves the right to change, without notice, the specifications and information contained herein.



RECTANGULAR PULSE DURATION (SECONDS)
Figure 1, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

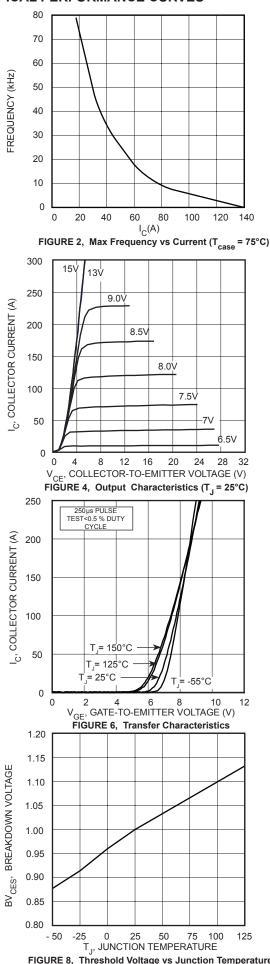


FIGURE 8, Threshold Voltage vs Junction Temperature

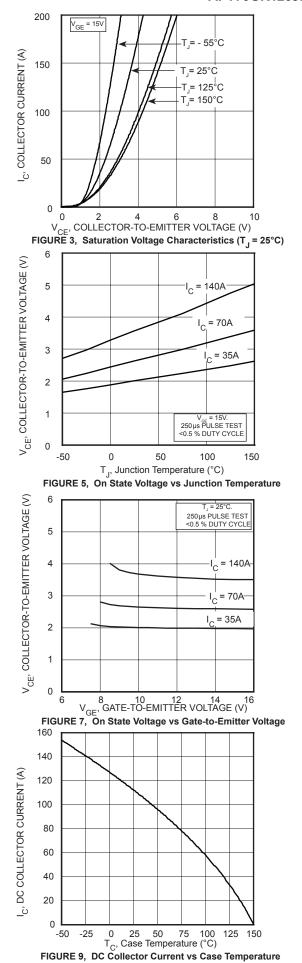


FIGURE 16, Swiitching Energy vs Junction Temperature

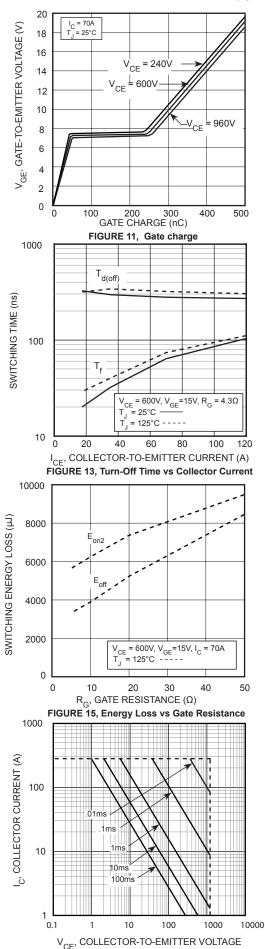


FIGURE 17, Minimum Switching Safe Operating Area

ULTRAFAST SOFT RECOVERY RECTIFIER DIODE

MAXIMUM RATINGS

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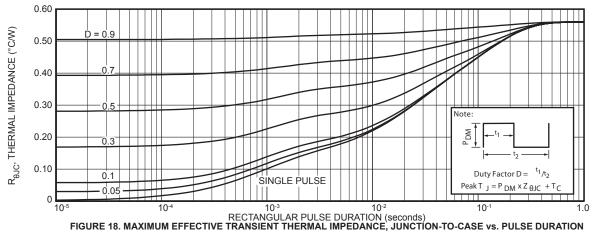
Symbol	Characteristic / Test Conditions	APT70GR120JD60	Unit
I _{F(AV)}	Maximum Average Forward Current (T _C = 92°C, Duty Cycle = 0.5)	60	
I _{F(RMS)}	RMS Forward Current (Square wave, 50% duty)	73	Amps
I _{FSM}	Non-Repetitive Forward Surge Current (T _J = 45°C, 8.3 ms)	540	

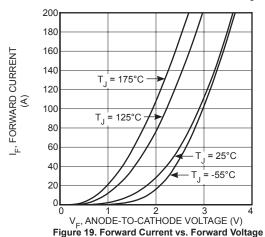
STATIC ELECTRICAL CHARACTERISTICS

Symbol	Characteristic / Test Conditions		Min	Type	Max	Unit
		I _F = 60A		2.5		
V _F	Forward Voltage	I _F = 120A		3.07		Volts
'		I _F = 60A, T _J = 125°C		1.82		

DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
t _{rr}	Reverse Recovery Time	$I_F = 1A$, $di_F/dt = -100A/\mu s$, $V_R = 30V$, $T_J = 25^{\circ}C$	-	60	-	ns
t _{rr}	Reverse Recovery Time	$I_F = 60A$, $di_F/dt = -200A/\mu s$ $V_R = 800V$, $T_C = 25^{\circ}C$	-	265	-	
Q _{rr}	Reverse Recovery Charge		-	560	-	nC
I _{RRM}	Maximum Reverse Recovery Current		-	5	-	Amps
t _{rr}	Reverse Recovery Time	$I_F = 60A$, $di_F/dt = -200A/\mu s$ $V_R = 800V$, $T_C = 125^{\circ}C$	-	350	-	ns
Q _{rr}	Reverse Recovery Charge		-	2890	-	nC
I _{RRM}	Maximum Reverse Recovery Current		-	13	-	Amps
t _{rr}	Reverse Recovery Time	$I_F = 60A$, $di_F/dt = -1000A/\mu s$ $V_R = 800V$, $T_C = 125°C$	-	150	-	ns
Q _{rr}	Reverse Recovery Charge		-	4720	-	nC
I _{RRM}	Maximum Reverse Recovery Current		-	40	-	Amps





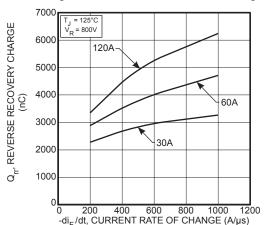


Figure 21. Reverse Recovery Charge vs. Current Rate of Change

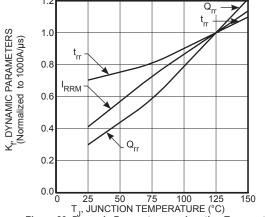
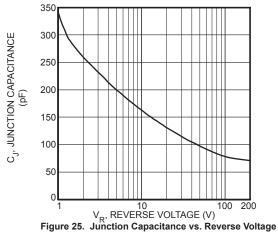


Figure 23. Dynamic Parameters vs. Junction Temperature



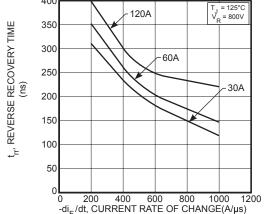


Figure 20. Reverse Recovery Time vs. Current Rate of Change

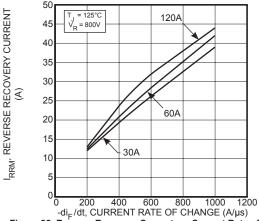


Figure 22. Reverse Recovery Current vs. Current Rate of Change

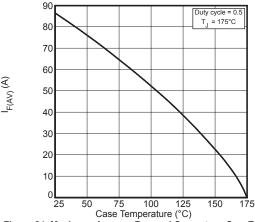


Figure 24. Maximum Average Forward Current vs. CaseTemperature

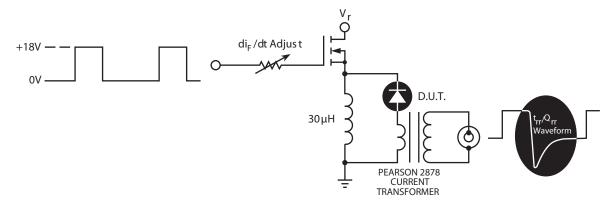
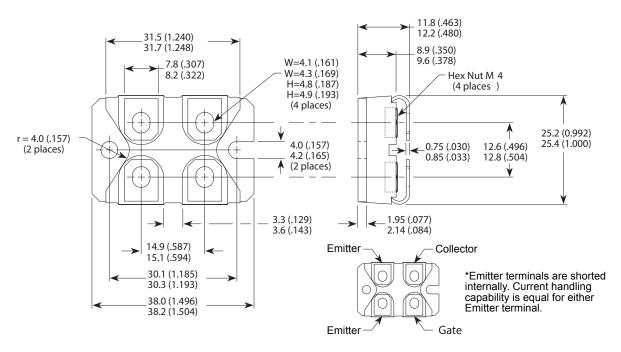


Figure 26. Diode Test Circuit

1 I_F - Forward Conduction Current
 2 di_F/dt - Rate of Diode Current Change Through Zero Crossing.
 3 I_{RRM} - Maximum Reverse Recovery Current
 4 t_{rr} - Reverse Recovery Time measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through I_{RRM} and 0.25, I_{RRM} passes through zero.
 5 Q_{rr} - Area Under the Curve Defined by I_{RRM} and t_{RR}.

Figure 27. Diode Reverse Recovery Waveform Definition

SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters and (Inches)

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