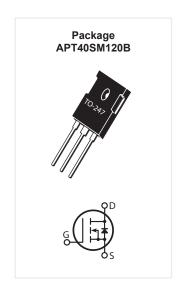


PRELIMINARY 1200V, 41A, 80mΩ

Silicon Carbide N-Channel Power MOSFET

DESCRIPTION

Silicon carbide (SiC) power MOSFET product line from Microsemi increase your performance over silicon MOSFET and silicon IGBT solutions while lowering your total cost of ownership for high-voltage applications.



FEATURES / TYPICAL APPLICATIONS

SiC MOSFET Features:

- · Low capacitances and low gate charge
- Fast switching speed due to low internal gate resistance (ESR)
- Stable operation at high junction temperature, Tj(max) = +175C
- · Fast and reliable body diode
- · Superior avalanche ruggedness

SiC MOSFET Benefits:

- High efficiency to enable lighter/compact system
- · Simple to drive and easy to parallel
- Improved thermal capabilities and lower switching losses
- Eliminates the need of external Free Wheeling Diode
- · Lower system cost of ownership

Applications:

- PV inverter, converter and industrial motor drives
- · Smart grid transmission & distribution
- · Induction heating, and welding
- · H/EV powertrain and EV charger
- · Power supply and distribution

MAXIMUM RATINGS

Symbol	Parameter	Ratings	Unit
V _{DSS}	Drain Source Voltage	1200	V
	Continuous Drain Current @ T _c = 25°C	41	
l _D	Continuous Drain Current @ T _c = 100°C	29	А
I _{DM}	Pulsed Drain Current ^①	100	
V _{GS}	Gate-Source Voltage	-10 to +25	V
P _D	Total Power Dissipation @ T _c = 25°C	273	W
	Linear Derating Factor	1.82	W/°C

THERMAL AND MECHANICAL CHARACTERISTICS

Symbol	Characteristic	Min	Тур	Max	Unit
$R_{\theta JC}$	Junction to Case Thermal Resistance		0.36	0.55	°C/W
T _i	Operating Junction Temperature	-55		175	
T _{stg}	Storage Junction Temperature Range	-55		150	°C
T _L	Soldering Temperature for 10 Seconds (1.6mm from case)			260	
Torque	Mounting Torque (TO-247 Package), 6-32 or M3 screw			10	in·lbf
				1.1	N·m

050-7698 Rev C 09/2016

STATIC CHARACTERISTICS

Symbol	Parameter	Test Condi	tions	Min	Тур	Max	Unit
V _{(BR)DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_{D} = 1mA$		1200			V
R _{DS(on)}	Drain-Source On Resistance@	V _{GS} = 20V, I _D = 20A			80	100	mΩ
V _{GS(th)}	Gate-Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 1mA$		1.7	3.0		V
$\Delta V_{GS(th)}/\Delta T_{J}$	Threshold Voltage Temperature Coefficient				-4.8		mV/°C
	Zero Gate Voltage Drain Current	V _{DS} = 1200V	T _J = 25°C			100	
DSS		$V_{GS} = 0V$	T _J = 125°C			500	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = +20V / -10V				±100	nA

 $T_J = 25$ °C unless otherwise specified

DYNAMIC CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input Capacitance	V = 0V V = 1000V		2085		
C _{rss}	Reverse Transfer Capacitance	$V_{GS} = 0V, V_{DD} = 1000V$ f = 1MHz		25		pF
C _{oss}	Output Capacitance	T = TMHZ		115		
Q_g	Total Gate Charge	V _{GS} = 0/20V		130		nC
Q _{gs}	Gate-Source Charge	V _{DD} = 800V		19		
Q_{gd}	Gate-Drain Charge	I _D = 20A		35		
t _{d(on)}	Turn-On Delay Time	V _{DD} = 800V		10		- ns
t _r	Current Rise Time	V _{GS} = 0/20V		6		
t _{d(off)}	Turn-Off Delay Time	I _D = 20A		32		
t _f	Current Fall Time	$R_{_{\rm G}}$ = 0.7 Ω^{\bigodot} L = 115 μH		16		
E _{on2}	Turn-On Switching Energy ^④	Т _с = 25°С		225		
E _{off}	Turn-Off Switching Energy	Freewheeling Diode = APT10SCE120B		50		μJ
t _{d(on)}	Turn-On Delay Time	V _{DD} = 800V		8		
t,	Current Rise Time	V _{GS} = 0/20V		6		
t _{d(off)}	Turn-Off Delay Time	I _D = 20A		36		ns
t _f	Current Fall Time	$R_{\rm G} = 0.7 \Omega^{\circ}$		17		
E _{on2}	Turn-On Switching Energy ^④	L = 115 μH		225		1
E _{off}	Turn-Off Switching Energy			60		μJ
ESR	Equivalent Series Resistance	f = 1MHz, 25mV, Drain Short		1.2	ĺ	Ω
SCWT	Short Circuit Withstand Time	V _{DS} = 960V, V _{GS} = 20V, T _C = 25°C	İ	5	ĺ	μS
E _{AS}	Avalanche Energy, Single Pulse	$V_{DS} = 145V, V_{GS} = 20V, I_{D} = 20A, T_{C} = 25^{\circ}C$		2500	ĺ	mJ

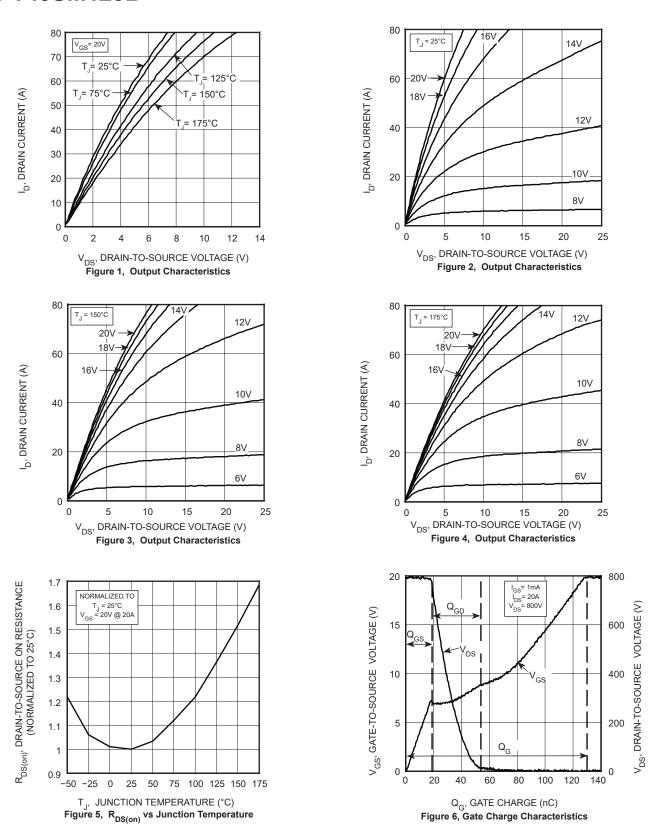
Source-Drain Diode Characteristics

	2.000 2.000 2.000						
Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit	
V _{SD}	Diode Forward Voltage	I _{SD} = 20A, V _{GS} = 0V		3.8		V	
t _{rr}	Reverse Recovery Time	I _{SD} = 20A, V _{DD} = 800V dI/dt = -1000A/μs		90		ns	
Q _{rr}	Reverse Recovery Charge			265		nC	
I	Reverse Recovery Current			7.8		Α	

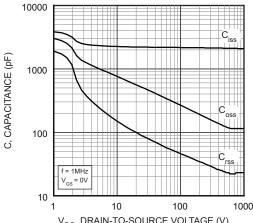
T_J = 25°C unless otherwise specified

- ① Repetitive Rating: Pulse width and case temperature limited by maximum junction temperature
- $\ensuremath{\mathfrak{J}}$ R $_{\ensuremath{\mathsf{G}}}$ is total external gate resistance including internal gate driver impedance.
- 4 E_{on2} includes energy of APT10SCE120B free wheeling diode.

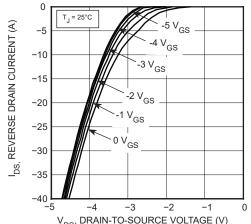




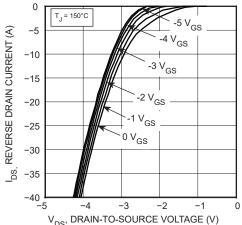
050-7698 Rev C 09/2016 3



 V_{DS} , DRAIN-TO-SOURCE VOLTAGE (V) Figure 7, Capacitance vs Drain-to-Source Voltage

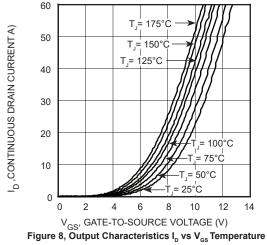


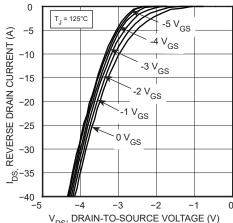
 ${\rm V_{DS},\, DRAIN\text{-}TO\text{-}SOURCE\,\, VOLTAGE\,\, (V)}$ Figure 9, Reverse Drain Current vs Drain-to-Source Voltage **Third Quadrant Conduction**



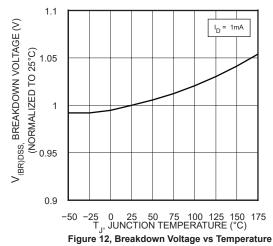
V_{DS}, DRAIN-TO-SOURCE VOLTAGE (V)

Figure 11, Reverse Drain Current vs Drain-to-Source Voltage **Third Quadrant Conduction**



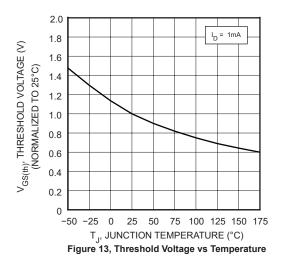


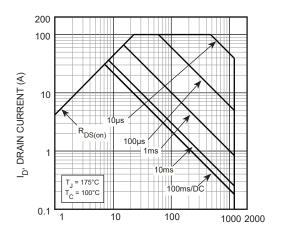
 ${\rm V_{DS'}, DRAIN\text{-}TO\text{-}SOURCE\ VOLTAGE\ (V)}$ Figure 10, Reverse Drain Current vs Drain-to-Source Voltage **Third Quadrant Conduction**



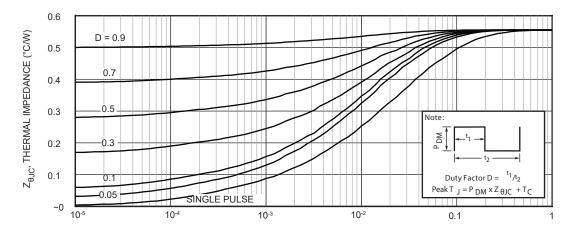
050-7698 Rev C 09/2016 4





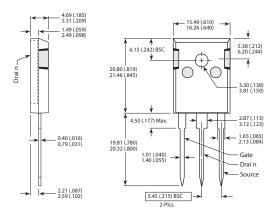


 $\rm V_{DS}$, DRAIN-TO-SOURCE VOLTAGE (V) Figure 14, Forward Safe Operating Area



RECTANGULAR PULSE DURATION (SECONDS)
Figure 15, Maximum Effective Transient Thermal Impedance, Junction-To-Case vs Pulse Duration

TO-247 (B) Package Outline



Dimensions in Millimeters (Inches)

050-7698 Rev C 09/2016 5

Microsemi makes no warranty, representation, or guarantee regarding the information contained herein or the suitability of its products and services for any particular purpose, nor does Microsemi assume any liability whatsoever arising out of the application or use of any product or circuit. The products sold hereunder and any other products sold by Microsemi have been subject to limited testing and should not be used in conjunction with mission-critical equipment or applications. Any performance specifications are believed to be reliable but are not verified, and Buyer must conduct and complete all performance and other testing of the products, alone and together with, or installed in, any end-products. Buyer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the Buyer's responsibility to independently determine suitability of any products and to test and verify the same. The information provided by Microsemi hereunder is provided "as is, where is" and with all faults, and the entire risk associated with such information is entirely with the Buyer. Microsemi does not grant, explicitly or implicitly, to any party any patent rights, licenses, or any other IP rights, whether with regard to such information itself or anything described by such information. Information provided in this document is proprietary to Microsemi, and Microsemi reserves the right to make any changes to the information in this document or to any products and services at any time without notice.



Microsemi Corporate Headquarters One Enterprise, Aliso Viejo, CA 92656 USA Within the USA: +1 (800) 713-4113 Outside the USA: +1 (949) 380-6100 Sales: +1 (949) 380-6136

Fax: +1 (949) 215-4996 email: sales.support@microsemi.com www.microsemi.com able anti-tamper products; Ethernet Solutions; Power-over-Ethernet ICs and midspans; as well as custom design capabilities and services. Microsemi is headquartered in Aliso Viejo, Calif., and has approximately 4,800 employees globally. Learn more at www.microsemi.com.

Microsemi Corporation (Nasdaq: MSCC) offers a comprehensive portfolio of semiconductor and system solutions for communications, defense & security, aerospace and industrial markets. Products include high-performance and radiation-hardened analog mixed-signal integrated circuits, FPGAs, SoCs and ASICs; power management products; timing and synchronization devices and precise time solutions, setting the world's standard for time; voice processing devices; RF solutions; discrete components; security technologies and scal-

©2016 Microsemi Corporation. All rights reserved. Microsemi and the Microsemi logo are registered trademarks of Microsemi Corporation. All other trademarks and service marks are the property of their respective owners.

050-7698 Rev C 09/2016 6

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Microchip:

APT40SM120B