# AAP661X Electret Microphone (ECM) Pre-Amplifier w/Programmable Filter



# PRELIMINARY DATA

### DESCRIPTION

The AAP661X ECM Pre Amplifiers were designed for high end audio headset microphone applications. The performance of this Pre-Amplifier is such that it enables design of enhanced end system products, due to its various gain options, ultra-low noise and other high performance features.

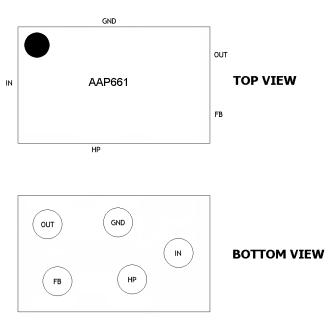
The AAP661X ECM Pre-Amplifier provides a number of performance advantages over prior ECM Pre-Amplifier products. Key features include ultra low input capacitance (0.35pF typical) and quiescent current (250 $\mu$ A typical), with ultra low equivalent input noise (1.9  $\mu$ V RMS to 2.5  $\mu$ V RMS, A-Weighted, with the microphone capacitor short circuited, gain version dependent). Additionally, the Pre-Amplifier supports a programmable high pass filter and DC output operation down to 1.23V. Other key features include THD performance 0.4% typical, output impedance of 25 $\Omega$  typical, with exceptionally high tolerance to RF interference and ESD tolerance (8kV).

The AAP661X is offered with a fixed gain of 16dB, 19dB and 30dB. Packaging is bumped chip scale SMD configuration with a size of 930 $\mu$ m x 580 $\mu$ m and an overall thickness of 320 $\mu$ m (including solder bumps). Optimum for small diameter microphones, the die is RoHS compliant, with lead free solder pads of 118 $\mu$ m diameter. Packing styles available are 2" x 2" Waffle Pack or Tape and Reel.

- Selectable Gain Configuration 16dB, 19dB and 30dB
- Ultra Low Input Capacitance—0.35pF Typ
- Ultra Low Equivalent Input Noise Performance— 1.9  $\mu$ V RMS to 2.5 $\mu$ V RMS, Cmic = SC, Varies with Gain
- 8kV ESD Tolerance
- High RFI Tolerance, Low Output Impedance (25 $\Omega$ )
- Excellent THD Performance (< 0.5%)
- Ultra Low Quiescent Current (250µA Typical)
- Chip-Scale SMD Bumped Packaging (930µm x 580µm, 320µm thick)

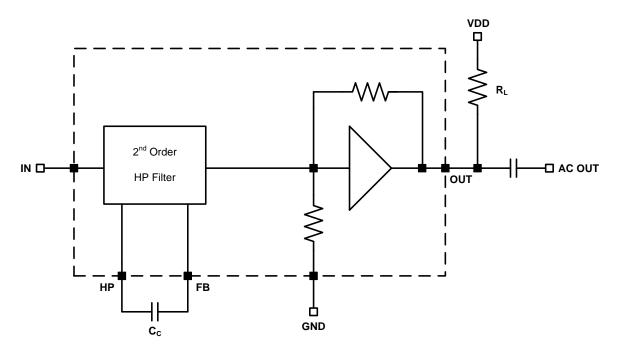
### PIN CONFIGURATION: 5-Lead Micro SMD

AAP661X shown from the top and bottom.



#### **FEATURES**

# Functional Block Diagram



### MAXIMUM RATINGS

PARAMETER	SYMBOL	PARAMETERS		UNITS	CONDITIONS	
		MIN.	MAX.			
Applied Voltage (all pins)		-0.5	2.5	V	Max voltage between pin and GND	
Supply Current	IDD		2	mA		
ESD	V <sub>esd,out</sub>	8000		- V	OUT terminal	
	Vesd	2000		v	Other terminals	
Operating Ambient Temp		-40	85	°C		
Storage Temp Range		-40	100	°C		
Performance Operating Temp Range		-5	55	°C		

## **ELECTRICAL CHARACTERISTICS**

Unless otherwise stated: T=25°C, VDD=1.8V, V<sub>in</sub>=-40dBVrms for AAP661A and AAP661B, V<sub>in</sub>=-60dBVrms for AAP661C  $R_L$ =2.2k $\Omega$ ,  $C_c$ =100nF,  $C_{mic}$ =short

PARAMETER	SYMBOL	PARAMETERS			UNITS	CONDITIONS		
		MIN	TYP	MAX				
OPERATING SUPPLY								
Supply Voltage	VDD	1.6	1.8	5.5	V	R∟=3.3kΩ		
Operating Output Voltage	V <sub>op</sub>	1.18	1.23	1.3	V			
Supply Current	IDD		250		μA	Note 1		

Note 1: IDD= (VDD – Vop)/RL

# Portable Electronics AAP661X

PSRR			60		dB	
AC CHARACTERISTICS						
Transfer Function (AAP661A)	TF	14	15	16	dB	
Transfer Function (AAP661B)	TF	18.5	19	19.5	dB	
Transfer Function (AAP661C)	TF	29	30	30.5	dB	
Gain Variation over Supply	ΔAv			0.1	dB	1.6V < VDD < 3.5V
Gain Variation over Temp (AAP661A & AAP661B)	ΔAv			0.2	dB	-5ºC < T < 55 ºC
Gain Variation over Temp (AAP661C)	ΔAv			0.5	dB	-5ºC < T < 55 ºC
Input Referred Noise	en		2	2.5	μV RMS	Input shorted to GND, A-weighted values
Overload Margin (AAP661A & AAP661B)	V <sub>outmax</sub>			825	mVpp	5% distortion, TF=11dB
Overload Margin (AAP661C)	V <sub>outmax</sub>			422	mVpp	5% distortion, TF=11dB
LF Cutoff (AAP661A & AAP661B)	f∟ow		200		Hz	
LF Cutoff (AAP661C)	fLOW	180	265	360	Hz	
HF Cutoff (AAP661A & AAP661B)	fнigh	20	85		kHz	
HF Cutoff (AAP661C)	fнigн	16	24		kHz	
Total Harmonic Distortion	THD		0.4		%	Vout=-23dBVrms
Input Capacitance	CIN		0.35		pF	
Input Impedance	Z <sub>IN</sub>	10			GΩ	
Output Impedance	Z <sub>OUT</sub>		25	70	Ω	
Input Impedance of FB (AAP661A)	ZINFB		17		kΩ	
Input Impedance of FB (AAP661B)	ZINFB		12.1		kΩ	
Input Impedance of FB (AAP661C)	ZINFB		6		kΩ	

## APPLICATION

Use the following equation to calculate the capacitor ( $C_c$ ) value to program the low frequency cutoff of the high-pass filter:

Example1: for a cutoff of f = 100Hz using AAP661A

$$C_c = 1 = 1$$
  $Z_{\Pi} * Z_{IN}FB * f$   $Z_{\Pi} * 17k * 100 = 94nF$ 

Example2: for a cutoff of f = 100Hz using AAP661B

$$C_{c} = \underbrace{1}{2\pi * Z_{IN}FB * f} = \underbrace{1}{2\pi * 12.1k * 100} = 44nF$$

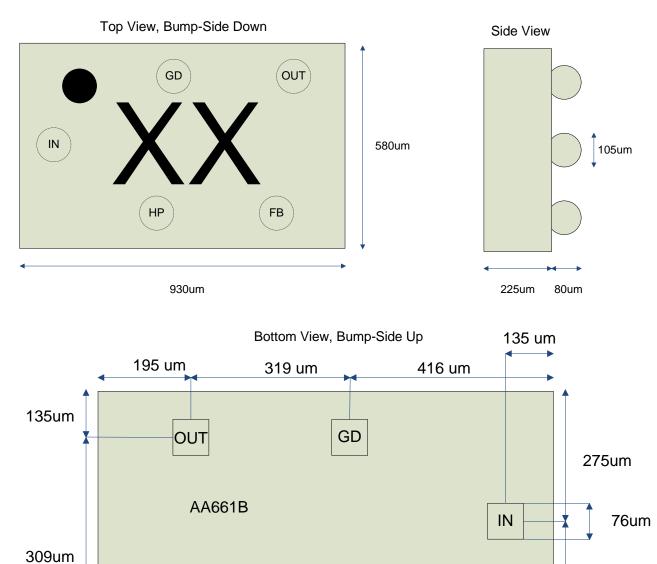
Example3: for a cutoff of f = 100Hz using AAP661C

$$C_c = 1 = 1$$
 = 1 = 265nF  
 $2\pi * Z_{IN}FB * f$   $2\pi * 6k * 100$ 

### ORDERING INFORMATION

Ordering PN	Subgroup	Description	Temp. Range	Package	Packing Type	Packing Qty
AAP661A S-M5A-G-LF-W	Microphone ECM Interface	Pre-Amplifier, 16dB gain	S - Special -5°C to +55°C	5-pin Micro SMD	Waffle-Pack	400
AAP661A S-M5A-G-LF-TR	Microphone ECM Interface	Pre-Amplifier, 16dB gain	S - Special -5°C to +55°C	5-pin Micro SMD	T&R	3500
AAP661B S-M5A-G-LF-W	Microphone ECM Interface	Pre-Amplifier, 19dB gain	S - Special -5°C to +55°C	5-pin Micro SMD	Waffle-Pack	400
AAP661B S-M5A-G-LF-TR	Microphone ECM Interface	Pre-Amplifier, 19dB gain	S - Special -5°C to +55°C	5-pin Micro SMD	T&R	3500
AAP661C S-M5A-G-LF-W	Microphone ECM Interface	Pre-Amplifier, 30dB gain	S - Special -5°C to +55°C	5-pin Micro SMD	Waffle-Pack	400
AAP661C S-M5A-G-LF-TR	Microphone ECM Interface	Pre-Amplifier, 30dB gain	S - Special -5°C to +55°C	5-pin Micro SMD	T&R	3500

# PACKAGE DIMENSIONS AND MARKING



HP

M

403 um

283 um

FΒ

76um

244 um

136um

305um

The following is a brief overview of certain terms and conditions of sale of product. For a full and complete copy of all the General Terms and Conditions of Sale, visit our webpage http://www.microsemi.com.

#### LIMITED WARRANTY

The product is warranted that it will conform to the applicable specifications and be free of defects for one year. Buyer is responsible for selection of, use of and results obtained from use of the product. Buyer indemnifies and holds ASIC Advantage, Inc. harmless for claims arising out of the application of ASIC Advantage, Inc.'s products to Buyer's designs. Applications described herein or in any catalogs, advertisements or other documents are for illustrative purposes only.

#### CRITICAL APPLICATIONS

Products are not authorized for use in critical applications including aerospace and life support applications. Use of products in these applications is fully at the risk of the Buyer. Critical applications include any system or device whose failure to perform can result in significant injury to the user.

#### LETHAL VOLTAGES

Lethal voltages could be present in the applications. Please comply with all applicable safety regulations.

#### INTELLECTUAL PROPERTY RIGHTS AND PROPRIETARY DATA

ASIC Advantage, Inc. retains all intellectual property rights in the products. Sale of products does not confer on Buyer any license to the intellectual property. ASIC Advantage, Inc. reserves the right to make changes without notice to the products at any time. Buyer agrees not to use or disclose ASIC Advantage Inc.'s proprietary information without written consent.

#### TRADEMARKS AND PATENTS

- IN-PLUG® is a registered trademark of ASIC Advantage, Inc.

- AAI's modified snubber network is patented under the US Patent # 6,233,165

#### PROTECTION FOR CUSTOM SOLUTIONS

When AAI accepts to design and manufacture products to Buyer's designs or specifications, buyer has certain obligations to provide defense in a suit or proceeding claiming infringement of a patent, copyright or trademark or for misappropriation of use of any trade secrets or for unfair competition.

#### COMPLIANCE WITH LAWS

Buyer agrees that at all times it will comply with all applicable federal, state, municipal, and local laws, orders and regulations. Buyer agrees to comply with all applicable restrictions on exports and re-exports including obtaining any required U.S. Government license, authorization, or approval. Buyer shall pay any duties, levies, taxes, brokerage fees, or customs fees imposed on the products.

#### TITLE AND DELIVERY

All shipments of goods shall be delivered ExWorks, Sunnyvale, CA, U.S.A. Title in the goods shall not pass to Buyer until ASIC Advantage, Inc. has received in full all amounts owed by Buyer.

#### LATEST DATASHEET UPDATES

For the latest datasheet updates, visit our web page: http://www.asicadvantage.com.

#### WORLDWIDE REPRESENTATIVES

To access AAI's list of worldwide representatives, visit our web page http://www.asicadvantage.com.

#### **COPYRIGHTS**

Copyrights and all other proprietary rights in the Content rests with ASIC Advantage Inc. (AAI) or its licensors. All rights in the Content not expressly granted herein are reserved. Except as otherwise provided, the Content published on this document may be reproduced or distributed in unmodified form for personal non-commercial use only. Any other use of the Content, including without limitation distribution, reproduction, modification, display or transmission without the prior written consent of AAI is strictly prohibited. All copyright and other proprietary notices shall be retained on all reproductions.

Microsemi Corp. 1290-B Reamwood Ave, Sunnyvale California 94089, USA Tel: (1) 408-541-8686 Fax: (1) 408-541-8675 Website: <u>http://www.microsemi.com</u> <u>http://www.asicadvantage.com</u>

# **Mouser Electronics**

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Microchip:

AAP661AS-M5A-GLF-TR AAP661BS-M5A-GLF-TR AAP661CS-M5A-GLF-TR