

### Features

- Advanced Trench MOSFET Process Technology
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

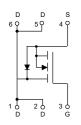
### **Maximum Ratings**

- Operating Junction Temperature Range: -55°C to +150°C
- \* Storage Temperature Range: -55°C to +150°C
- \* Thermal Resistance: 73.5°C/W Junction to Ambient<sup>(Note2)</sup>

Parameter	Symbol	Rating	Unit	
Drain -Source Voltage	V <sub>DS</sub>	60	V	
Gate -Source Volltage	V <sub>GS</sub>	±20	V	
Drain Current-Continuous	I <sub>D</sub>	5	А	
Pulsed Drain Current <sup>(Note1)</sup>	I <sub>DM</sub>	30	А	
Power Dissipation	P <sub>D</sub>	1.7	W	

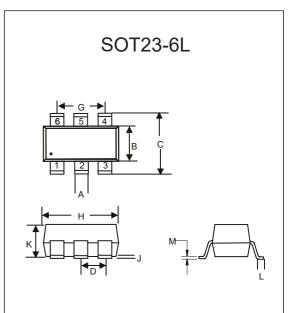
Noet:1.Pulse Width Limited by Maximum Junction Temperature. 2.Surface Mounted on FR4 Board, t  $\leq$  10 sec.

### **Internal Structure**



Marking: 5N06

# N-Channel Power MOSFET



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	NOTE
А	0.012	0.020	0.30	0.50	
В	0.051	0.070	1.30	1.80	
С	0.087	0.126	2.20	3.20	
D	0.037		0.95		TYP.
G	0.074		1.90		TYP.
Н	0.106	0.122	2.70	3.10	
J	0.002	0.006	0.05	0.15	
Κ	0.030	0.051	0.75	1.30	
L	0.012	0.024	0.30	0.60	
Μ	0.003	0.008	0.08	0.22	



### ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Тур	Мах	Unit
Static Characteristics	1					
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS}$ =0V, I <sub>D</sub> =250 $\mu$ A	60			V
Gate-Threshold Voltage <sup>(Note3)</sup>	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.0		3.0	V
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =± 20V, V <sub>DS</sub> =0V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	μA
Drain-Source On-Resistance <sup>(Note3)</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A		37	45	mΩ
Forward Transconductance <sup>(Note3)</sup>	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =4.5A	11			S
Dynamic Characteristics <sup>(Note4)</sup>						
Input Capacitance	C <sub>iss</sub>			500		
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V, f=1MHz		60		pF
Reverse Transfer Capacitance	C <sub>rss</sub>			25		
Switching Characteristics <sup>(Note4</sup>	L)			I	L	
Total Gate Charge	Qg			12		nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =48V,V <sub>GS</sub> =10V,I <sub>D</sub> =15A		4.1		
Gate-Drain Charge	Q <sub>gd</sub>			4.5		
Turn-on Delay Time	t <sub>d(on)</sub>			5.0		
Turn-on Rise Time	t <sub>r</sub>	V <sub>DD</sub> =30V,V <sub>GS</sub> =10V,I <sub>D</sub> =2A,R <sub>G</sub>		2.6		
Turn-off Delay Time	t <sub>d(off)</sub>	=3Ω, R <sub>L</sub> =6.7Ω		16.1		ns
Turn-off Fall Time	t <sub>f</sub>			2.3		
Drain-Source Diode Character	ristics		ł	I		
Diode Forward Voltage <sup>(Note3)</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>s</sub> =20A			1.2	V
Diode Forward Current <sup>(Note2)</sup>	Is				20	А
Reverse Recovery Time	t <sub>rr</sub>			35		nS
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =20A,di/dt=100A/us <sup>(Note4)</sup>		53		μC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)			)	

Note: 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%. 4.Guaranteed by design, not subject to production.



# **Curve Characteristics**

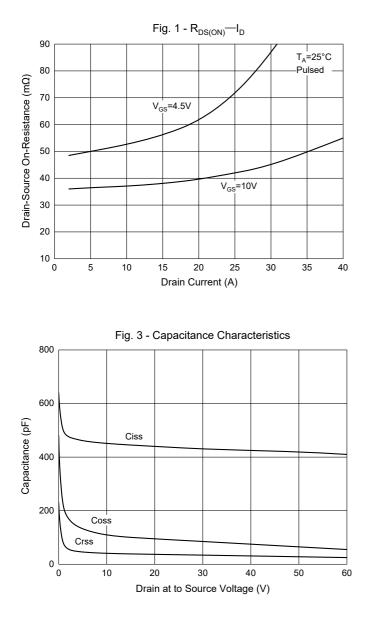
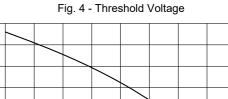
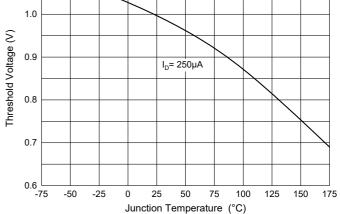


Fig. 2 - Gate Charge 10 V<sub>DS</sub>= 30V I<sub>D</sub>=4.5A 8 Gate to Source Voltage (V) 6 4 2 0 2 0 4 6 8 10 Gate Charge (nC)



1.1





## **Ordering Information**

Device	Packing	
Part Number-TP	Tape&Reel:3Kpcs/Reel	

Note : Adding "-HF" Suffix For Halogen Free, eg. Part Number-TP-HF

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