

Features

- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)

N-Channel Enhancement Mode Field Effect Transistor

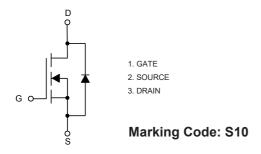
Maximum Ratings

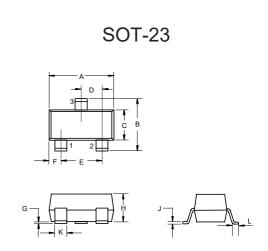
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 105°C/W Junction to Ambient(Note 3)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Volltage	V _{GS}	±20	V
Drain Current	I _D	3.0	Α
Pulsed Drain Current ^(Note 2)	I _{DM}	10	Α
Total Power Dissipation	P_D	1.2	W

Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

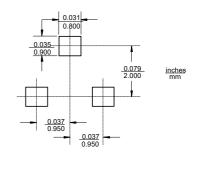
Internal Structure:





DIMENSIONS					
DIM	INCHES		MM		NOTE
DIIVI	MIN	MAX	MIN	MAX	INOTE
Α	0.110	0.120	2.80	3.04	
В	0.083	0.104	2.10	2.64	
С	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
Н	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout





Electrical Characteristics @ 25°C (Unless Otherwise Specified)

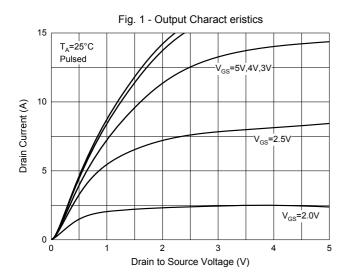
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μΑ	
Gate-Threshold Voltage(Note 4)	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.5		2.0	V	
Drain-Source On-Resistance(Note 4)	R _{DS(on)}	V _{GS} =10V, I _D =3A			105	mΩ	
		V _{GS} =4.5V, I _D =3A			125		
Diode Forward Voltage ^(Note 4)	V _{SD}	V _{GS} =0V, I _S =3A			1.2	V	
Forward Tranconductance(Note 4)	9 FS	V _{DS} =15V, I _D =2A	1.4			S	
Dynamic Characteristics(Note 5)	ı			1	1		
Input Capacitance	C _{iss}			247			
Output Capacitance	C _{oss}	V_{DS} =30V, V_{GS} =0V,f=1MHz		34		pF	
Reverse Transfer Capacitance	C _{rss}			19.5			
Switching Characteristics(Note	5)		·				
Total Gate Charge	Q_g			6			
Gate-Source Charge	Q_{gs}	V_{DS} =30V, V_{GS} =4.5V, I_{D} =3A		1		nC	
Gate-Drain Charge	Q_{gd}			1.3			
Turn-On Delay Time	t _{d(on)}			6			
Turn-On Rise Time	t _r	V_{GS} =10V, V_{DD} =30V, I_{D} =1.5A,		15		20	
Turn-Off Delay Time	t _{d(off)}	R_{GEN} =1 Ω		15		ns	
Turn-Off Fall Time	t _f			10			

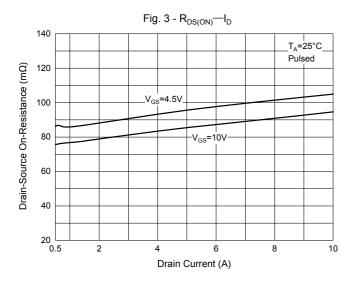
Note:

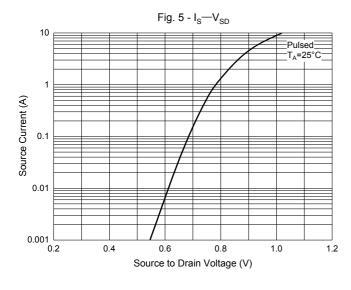
- 2. Repetitive rating : Pulse width limited by junction temperature.
- 3. Surface mounted on FR4 board , t≤10s.
- 4. Pulse Test : Pulse Width≤300μs, Duty Cycle≤0.5%.
- 5. Guaranteed by design, not subject to producting.

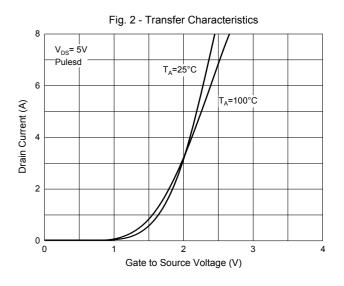


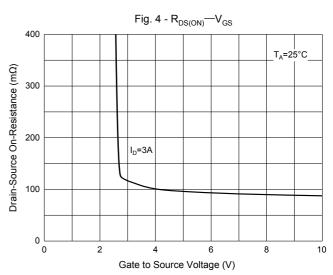
Curve Characteristics

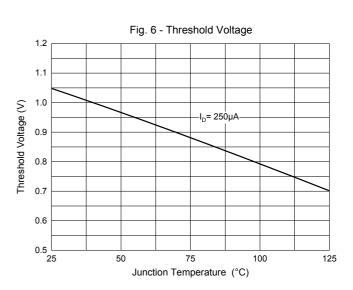














Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 3Kpcs/Reel	

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