

Features

- AEC-Q101 Qualified
- Split Gate Trench MOSFET Technology
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low $R_{DS(ON)}$
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 3

Maximum Ratings

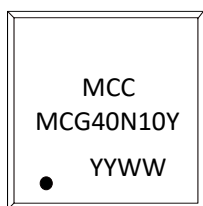
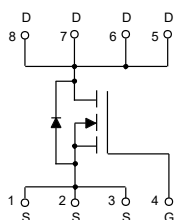
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 55°C/W Junction to Ambient⁽²⁾
- Thermal Resistance: 2.9°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	40	A
		25	
			$T_C=25^\circ\text{C}$
			$T_C=100^\circ\text{C}$
Pulsed Drain Current ⁽³⁾	I_{DM}	110	A
Total Power Dissipation ⁽⁴⁾	P_D	43	W
Single Pulsed Avalanche Energy ⁽⁵⁾	E_{AS}	81	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$. The Power dissipation PDSM is based on $R_{\theta JA} \leq 10\text{s}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_d is based on max. junction temperature, using junction-case thermal resistance.
5. $V_{DD}=40\text{V}$, $V_G=10\text{V}$, $R_G=25\Omega$, $L=2\text{mH}$, $I_{AS}=9\text{A}$.

Internal Structure and Marking Code

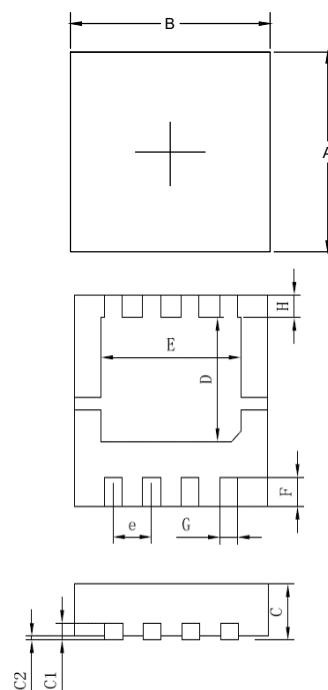


pin1

4 codes in total
YY is the year
WW is the week

N-CHANNEL MOSFET

DFN3333



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.126	0.130	3.20	3.30	
B	0.126	0.130	3.20	3.30	
C	0.030	0.033	0.75	0.85	
C1	0.007	0.009	0.18	0.22	
C2	---	0.002	---	0.05	
D	0.071	0.079	1.80	2.00	
E	0.087	0.098	2.20	2.50	
F	0.016	0.020	0.40	0.50	
G	0.010	0.014	0.25	0.35	
H	0.012	0.016	0.30	0.40	
e	0.024	0.028	0.60	0.70	

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	100			V
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.0	1.8	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		14	18.5	mΩ
		V _{GS} =4.5V, I _D =10A		17	22.5	
Diode Characteristics						
Continuous Body Diode Current	I _S				40	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A		0.9	1.2	V
Reverse Recovery Time	t _{rr}	I _F =20A, dI _F /dt=100A/μs		39.8		ns
Reverse Recovery Charge	Q _{rr}			42		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =10V, f=1MHz		1220		pF
Output Capacitance	C _{oss}			657		
Reverse Transfer Capacitance	C _{rss}			22		
Total Gate Charge	Q _g	V _{DS} =50V, V _{GS} =10V, I _D =25A		16		nC
Gate-Source Charge	Q _{gs}			5.6		
Gate-Drain Charge	Q _{gd}			2.4		
Turn-On Delay Time	t _{d(on)}	V _{DS} =50V, V _{GEN} =10V, R _G =2.2Ω, I _{DS} =25A		39.2		ns
Turn-On Rise Time	t _r			11		
Turn-Off Delay Time	t _{d(off)}			53.2		
Turn-Off Fall Time	t _f			15.8		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

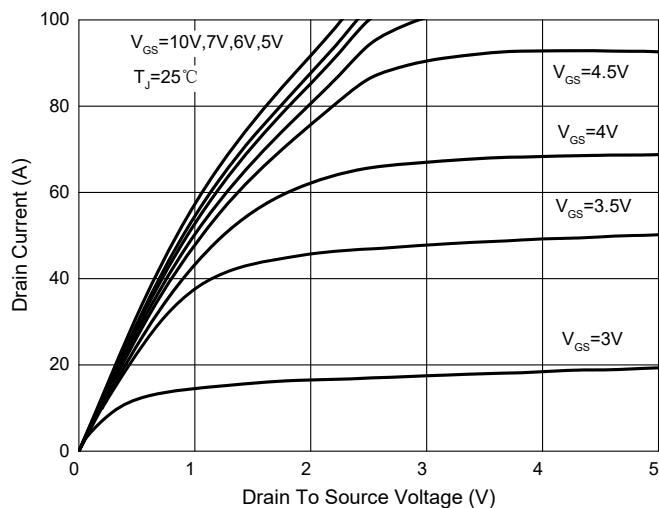


Fig. 2 - Transfer Characteristics

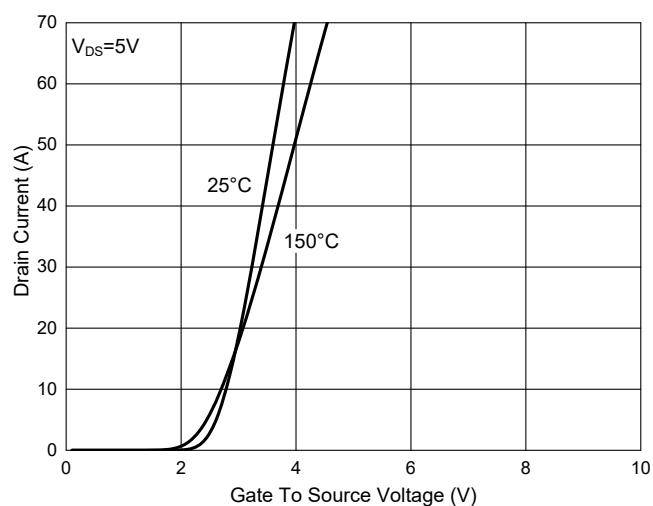


Fig. 3 - $R_{DS(ON)} - V_{GS}$

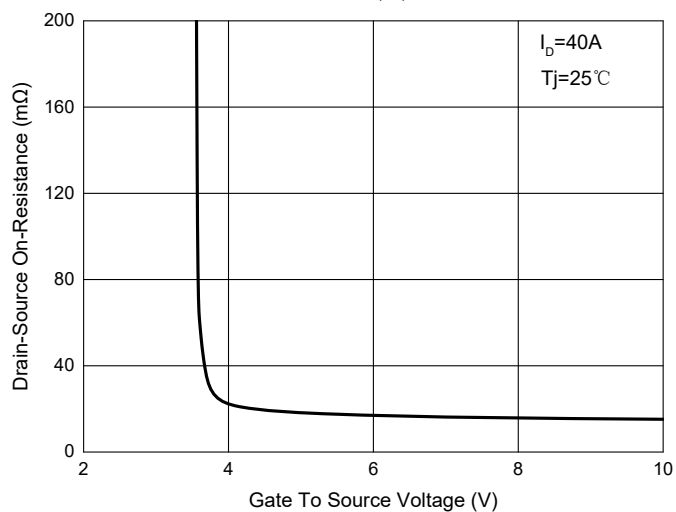


Fig. 4 - Normalized On Resistance Characteristics

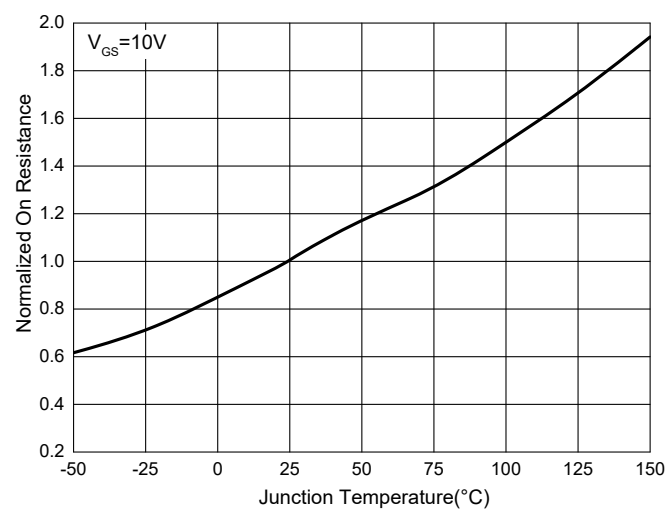


Fig. 5 - Capacitance Characteristics

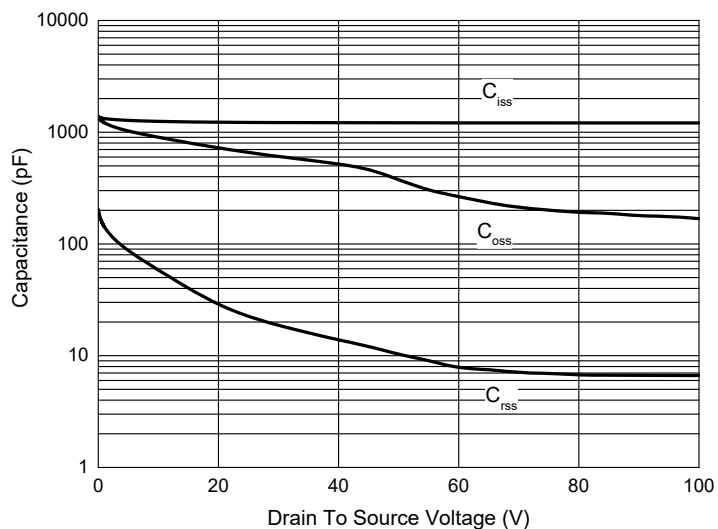
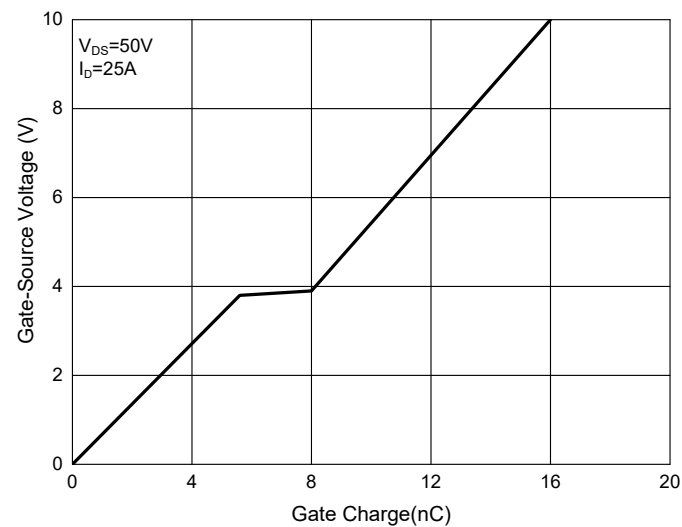


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - $R_{DS(ON)} - I_D$

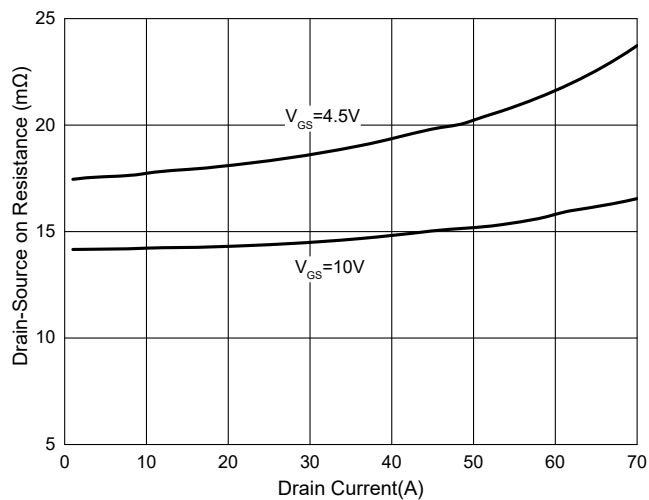


Fig. 8 - Normalized Threshold voltage

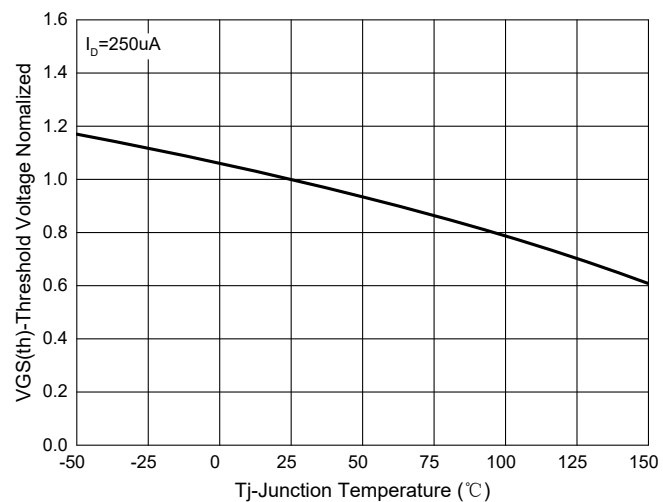


Fig. 9 - $I_S - V_{SD}$

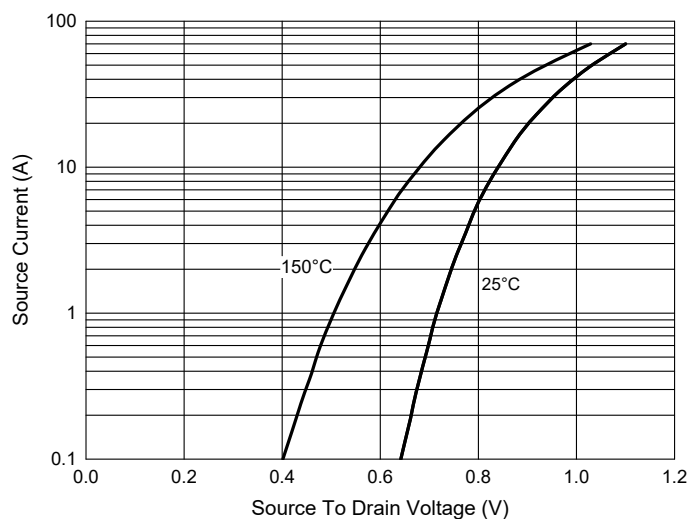


Fig. 10 - Current dissipation

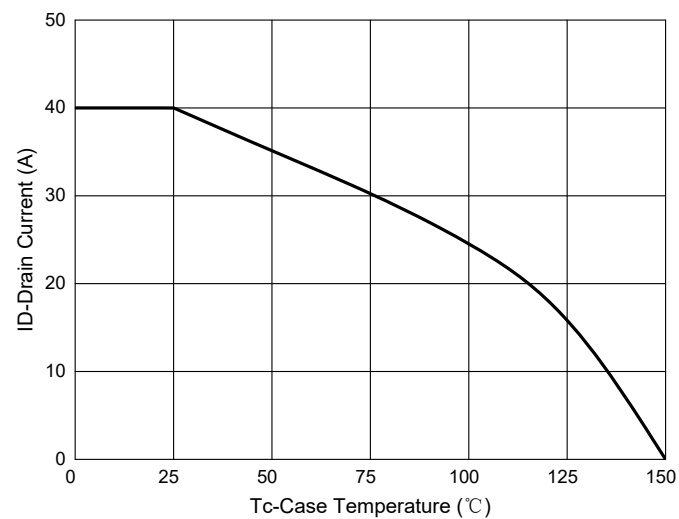
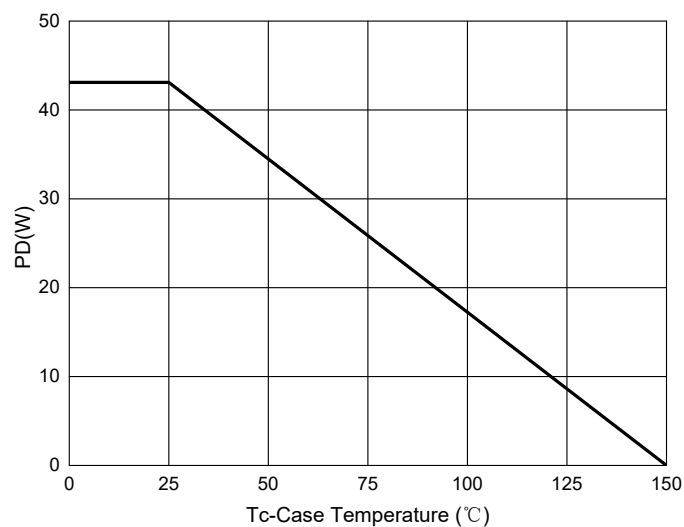


Fig. 11 - Power Dissipation



Curve Characteristics

Fig. 12- Safe Operation Area

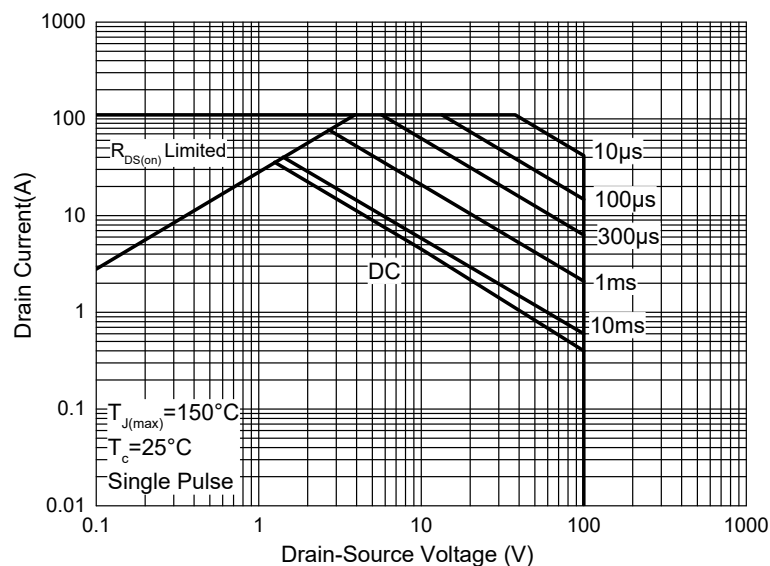
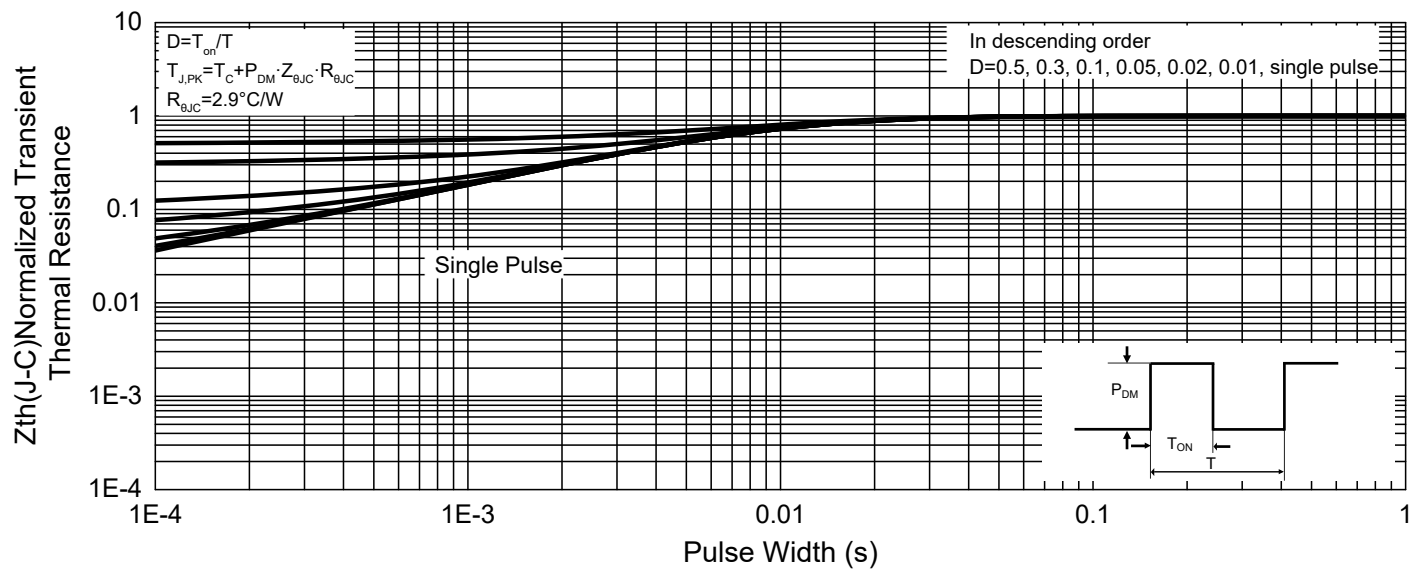


Fig. 13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

Revision History

Datasheet status	Version No	Release date	Update content
New product datasheet	Rev4-1	20230104	

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