

## Features

- Trench Power LV MOSFET Technology
- Halogen Free, "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 3

## Maximum Ratings

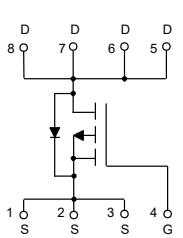
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient<sup>(Note 2)</sup>
- Thermal Resistance: 5°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DS</sub>	-60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Continuous Drain Current  T <sub>J</sub> =25°C	I <sub>D</sub>	-20	A
T <sub>J</sub> =100°C	I <sub>D</sub>	-12	A
Pulsed Drain Current <sup>(Note 3)</sup>	I <sub>DM</sub>	-80	A
Total Power Dissipation <sup>(Note 4)</sup>	P <sub>D</sub>	25	W
Single Pulsed Avalanche Energy <sup>(Note 5)</sup>	E <sub>AS</sub>	100	mJ

Note:

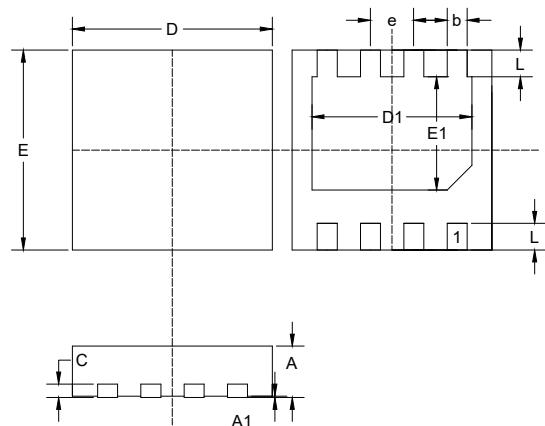
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25°C. The Power dissipation P<sub>DSM</sub> is based on R<sub>θJA</sub> t≤ 10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P<sub>D</sub> is based on max. junction temperature, using junction-case thermal resistance.
5. T<sub>J</sub>=25°C, V<sub>DD</sub>= -60V, V<sub>GS</sub>= -10V, L= 0.5mH.

## Internal Structure and Marking Code



## P-CHANNEL MOSFET

### DFN3333-8



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.028	0.031	0.70	0.80	
A1	0.000	0.002	0.00	0.05	
C	0.008		0.20		TYP.
b	0.010	0.014	0.25	0.35	
D	0.130		3.30		TYP.
E	0.130		3.30		TYP.
e	0.026		0.65		TYP.
D1	0.100	0.110	2.55	2.80	
E1	0.065	0.074	1.64	1.89	
L	0.013	0.021	0.325	0.525	

**Electrical Characteristics @ 25°C (Unless Otherwise Specified)**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=-250\mu A$	-60			V
Gate-Source Leakage Current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 20V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=-48V, V_{GS}=0V$			-1	$\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-1.9	-3	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-20A$		17.5	25	$m\Omega$
		$V_{GS}=-4.5V, I_D=-10A$		23	34	
Gate Resistance	$R_g$	$f=1MHz, Open Drain$		5		$\Omega$
<b>Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$				-8	A
Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=-3A$			-1.3	V
Reverse Recovery Time	$t_{rr}$	$I_F=-4A, dI_F/dt=100A/\mu s$		31		ns
Reverse Recovery Charge	$Q_{rr}$			26		nC
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=-25V, V_{GS}=0V, f=1MHz$		1533		$pF$
Output Capacitance	$C_{oss}$			272		
Reverse Transfer Capacitance	$C_{rss}$			13		
Total Gate Charge	$Q_g$	$V_{DS}=-30V, V_{GS}=-10V, I_D=-4A$		23		$nC$
Gate-Source Charge	$Q_{gs}$			4		
Gate-Drain Charge	$Q_{gd}$			3		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS}=-30V, V_{GEN}=-10V, R_G=6\Omega, I_{DS}=-4A$		7		$ns$
Turn-On Rise Time	$t_r$			5		
Turn-Off Delay Time	$t_{d(off)}$			65		
Turn-Off Fall Time	$t_f$			20		

## Curve Characteristics

Fig. 1 - Typical Output Characteristics

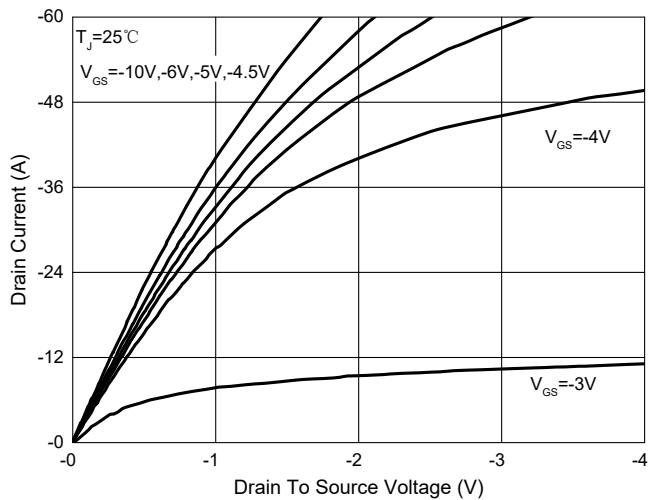


Fig. 2 -  $V_{GS} = -10V$  @  $I_D = 10A$

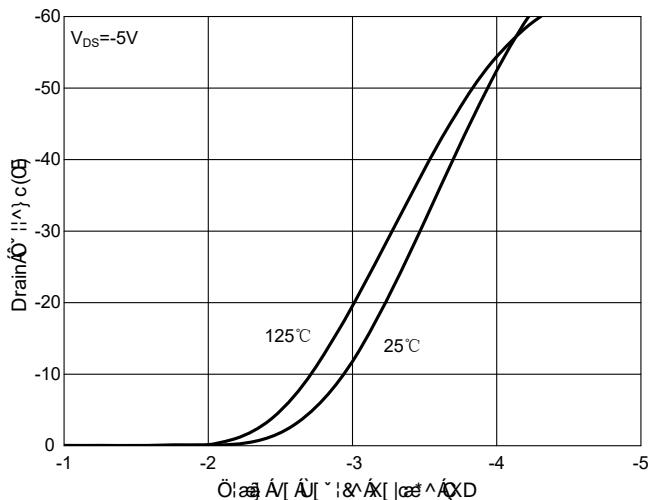


Fig. 3 -  $R_{DS(ON)}$  —  $V_{GS}$

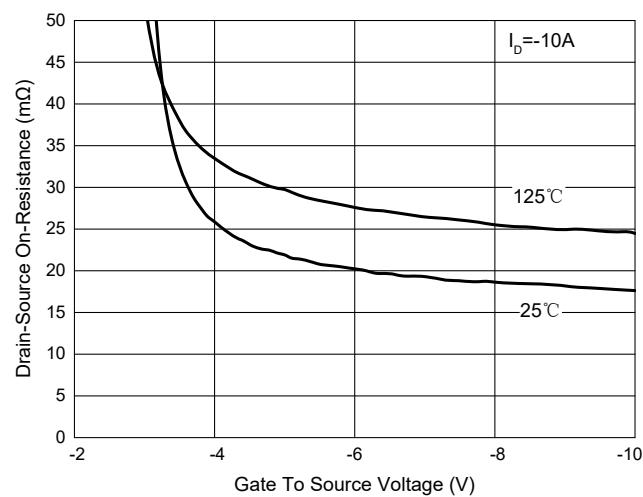


Fig. 4 - Normalized On Resistance Characteristics

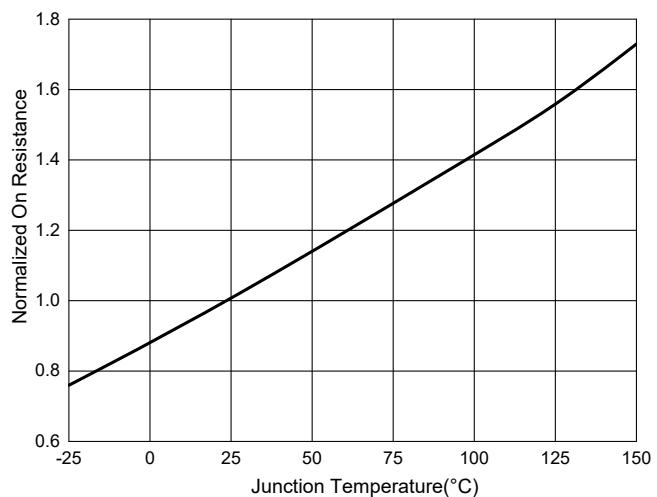


Fig. 5 - Capacitance Characteristics

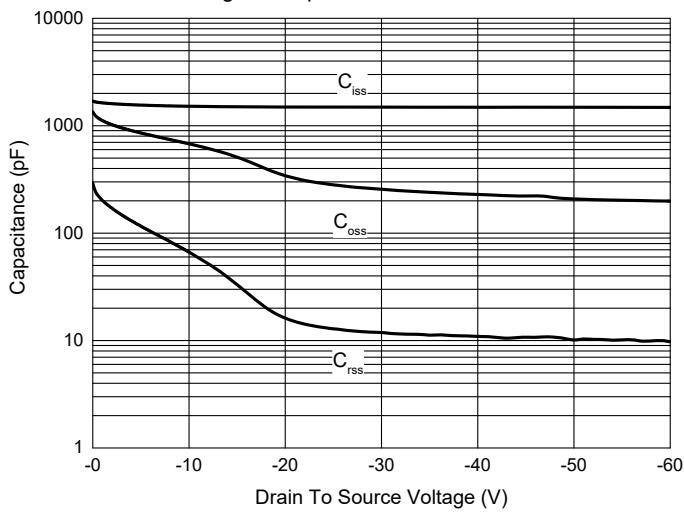
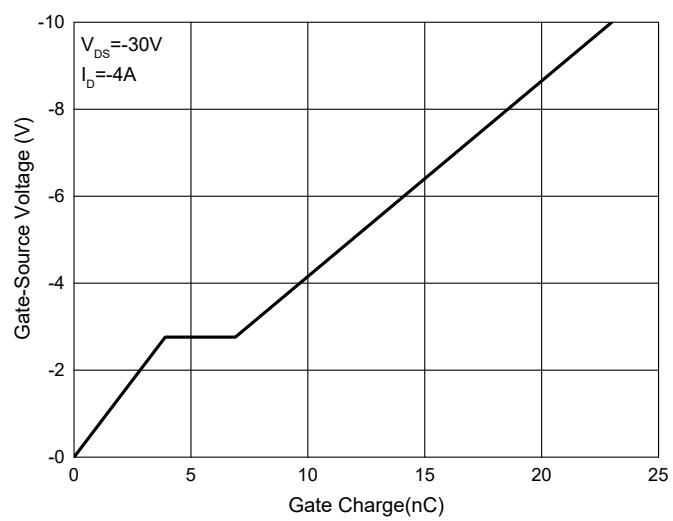
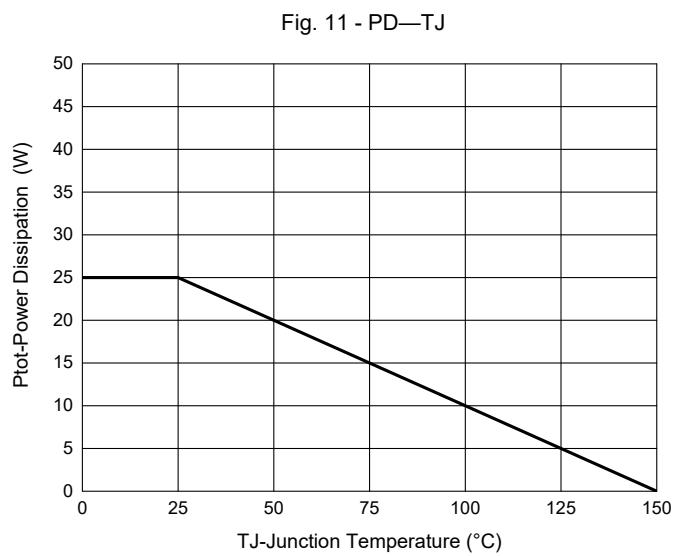
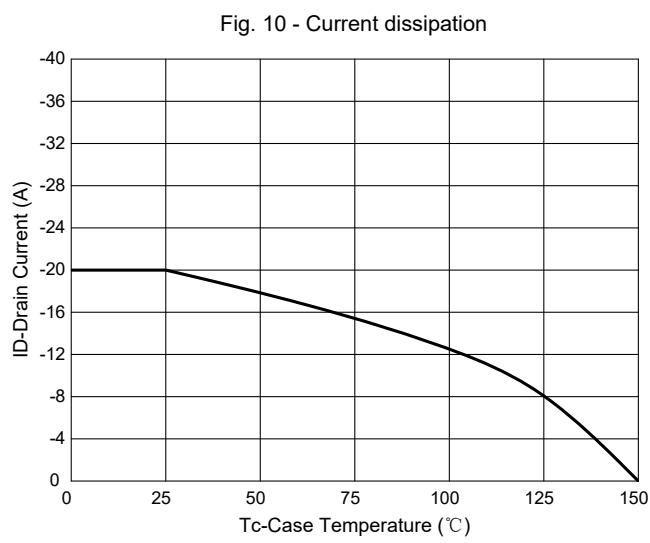
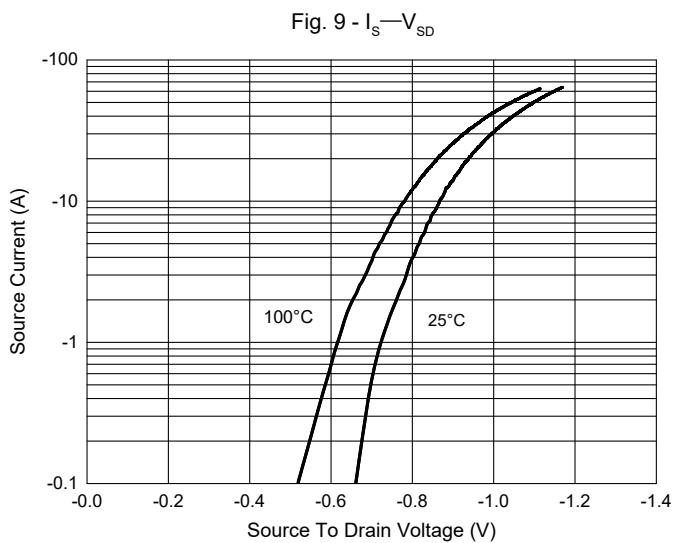
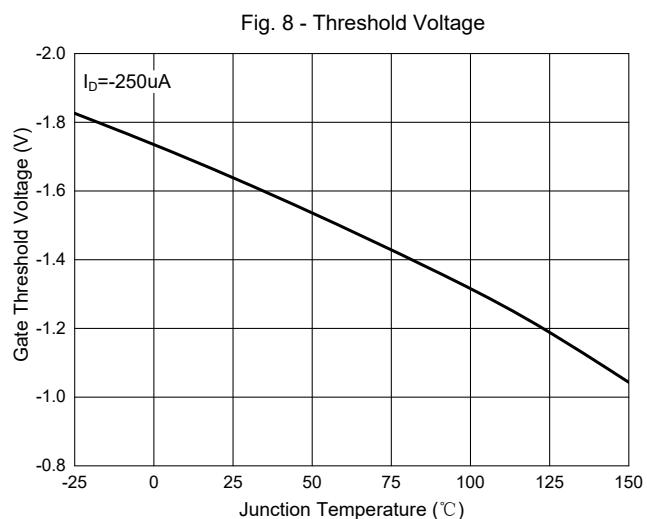
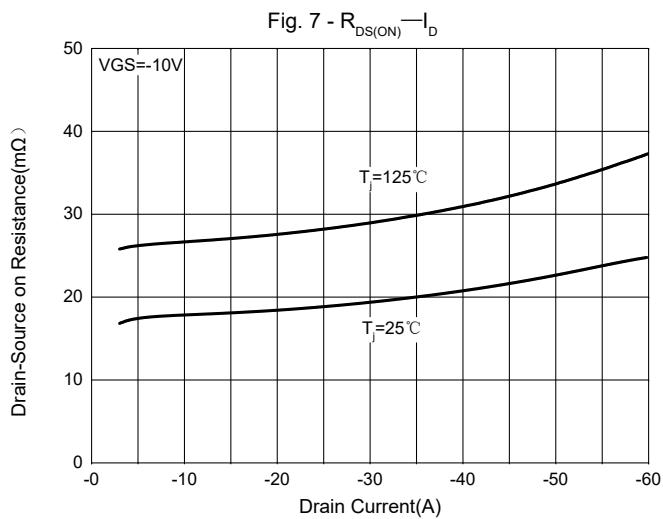


Fig. 8 - Gate Charge



## Curve Characteristics



## Curve Characteristics

Fig. 12 - Safe Operation Area

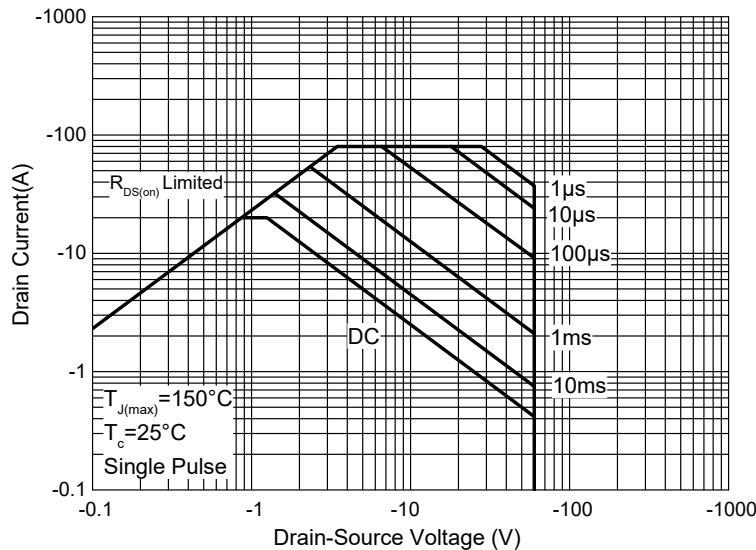
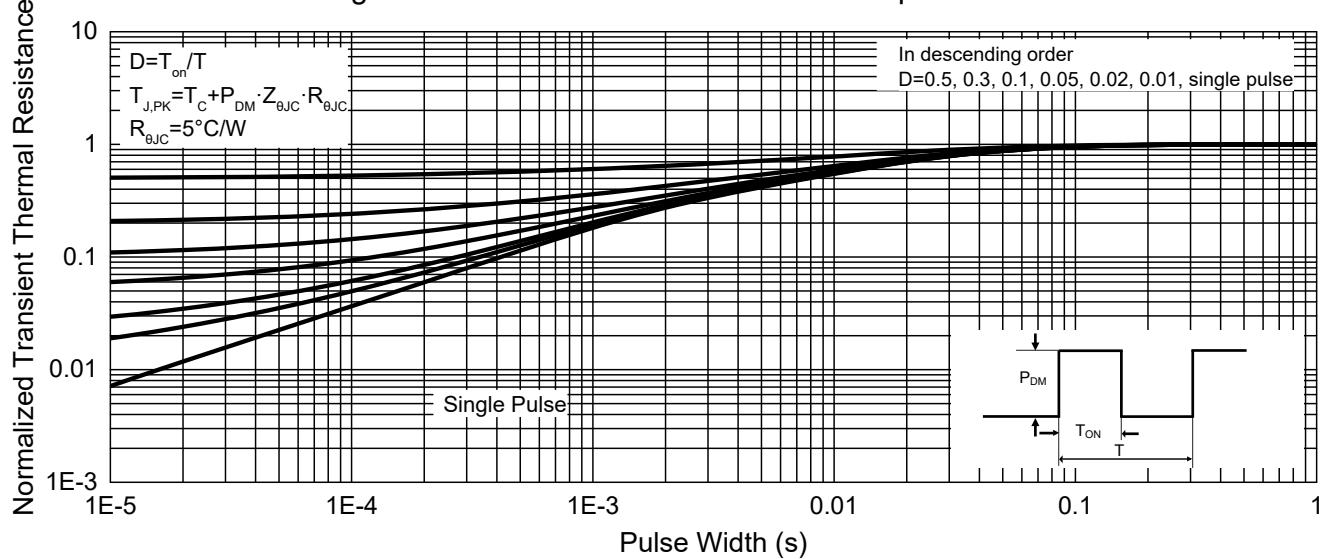


Fig. 13 - Normalized Transient Thermal Impedance



## Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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