

Features

- Split Gate Trench MOSFET Technology
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device^(Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

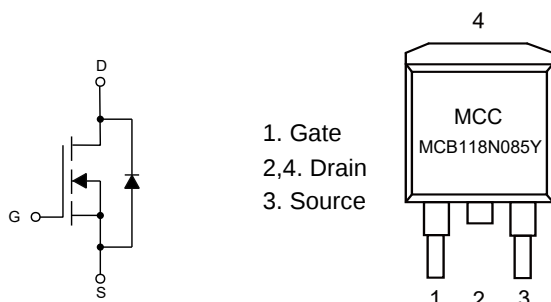
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 40°C/W Junction to Ambient^(Note 2)
- Thermal Resistance: 0.8°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	85	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	118	A
		75	
		$T_C=25^\circ\text{C}$	
		$T_C=100^\circ\text{C}$	
Pulsed Drain Current ^(Note 3)	I_{DM}	472	A
Total Power Dissipation ^(Note 4)	P_D	156	W
Single Pulsed Avalanche Energy ^(Note 5)	E_{AS}	380	mJ

Note:

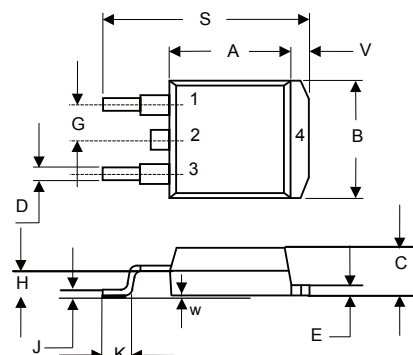
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-case thermal resistance.
5. $T_J=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_{GS}=10\text{V}$, $R_G=25\Omega$, $L=0.5\text{mH}$

Internal Structure and Marking Code



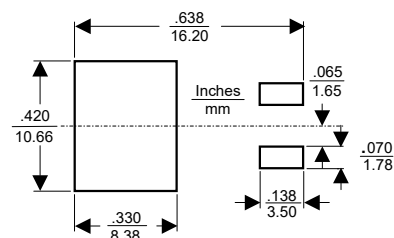
N-Channel MOSFET

D2-PAK



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.331	0.370	8.40	9.40	
B	0.378	0.417	9.60	10.60	
C	0.165	0.189	4.20	4.80	
D	0.027	0.037	0.68	0.94	
E	0.045	0.055	1.14	1.40	
G	0.1		2.54		TYP.
H	0.096	0.134	2.43	3.40	
J	0.011	0.025	0.28	0.64	
K	0.071	0.131	1.80	3.32	
S	0.575	0.625	14.60	15.87	
V	0.042	0.058	1.07	1.47	
W	0.000	0.010	0.00	0.25	

Suggested Solder Pad Layout



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	85			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =85V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2	3	4	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =59A		4.5	6	mΩ
		V _{GS} =6V, I _D =20A		7	9	
Gate Resistance	R _g	f=1 MHz, Open drain		1.6		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				118	A
Body Diode Voltage	V _{SD}	I _S =59A, V _{GS} =0V			1.2	V
Reverse Recovery Time	t _{rr}	I _F =59A di/dt=300A/μs		39		ns
Reverse Recovery Charge	Q _{rr}			138		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=0.5MHz		4140		pF
Output Capacitance	C _{oss}			1492		
Reverse Transfer Capacitance	C _{rss}			103		
Total Gate Charge	Q _g	V _{DS} =40V, I _D =59A V _{GS} =10V		57		nC
Gate-Source Charge	Q _{gs}			20		
Gate-Drain Charge	Q _{gd}			13		
Turn-On Delay Time	t _{d(on)}	V _{DD} =40V, V _{GS} =10V, I _D =59A R _G =2.2Ω		17		ns
Turn-On Rise Time	t _r			84		
Turn-Off Delay Time	t _{d(off)}			26		
Turn-Off Fall Time	t _f			13		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

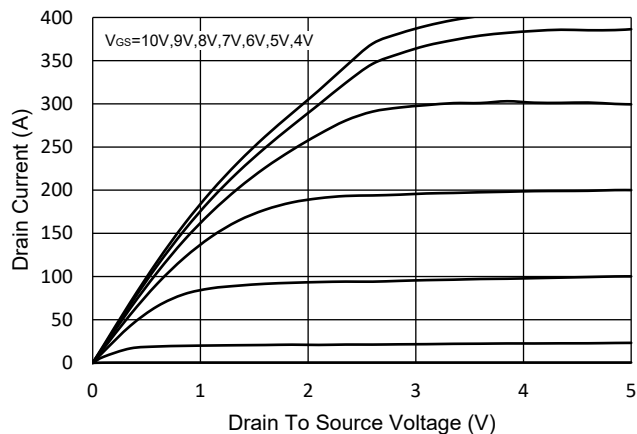


Fig.2 - Transfer Characteristic

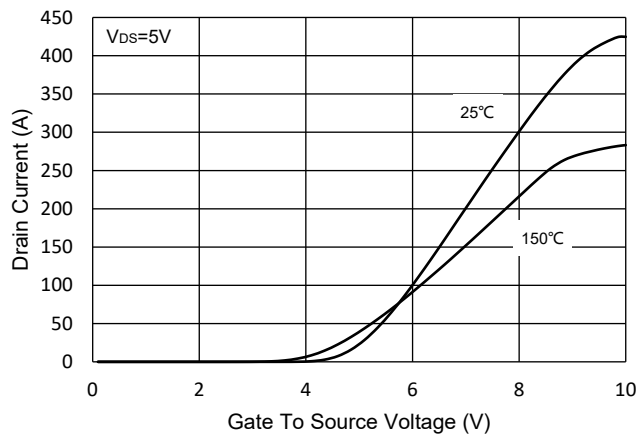


Fig.3 - $R_{DS(ON)} - V_{GS}$

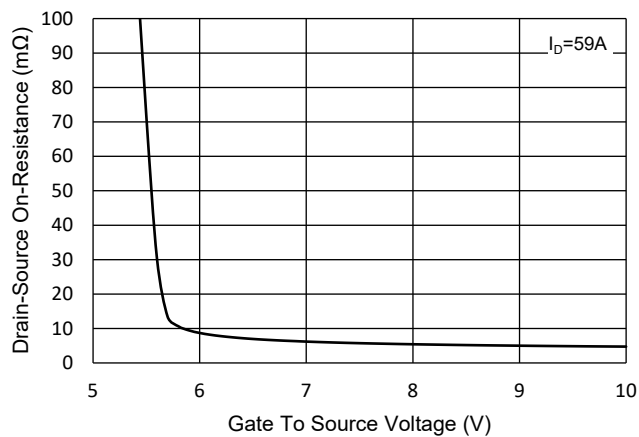


Fig. 4 - $R_{DS(ON)} - I_D$

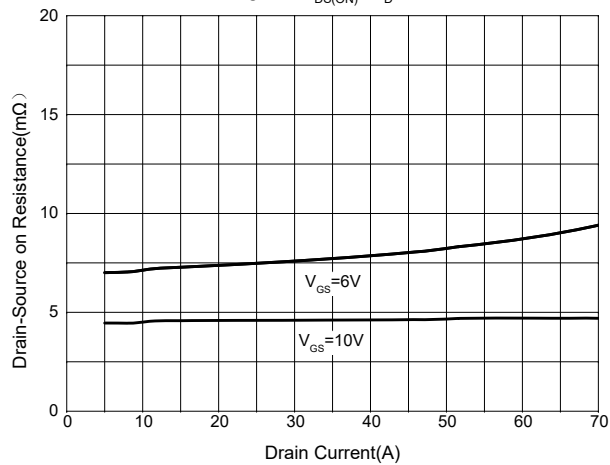


Fig.5 - Capacitance Characteristics

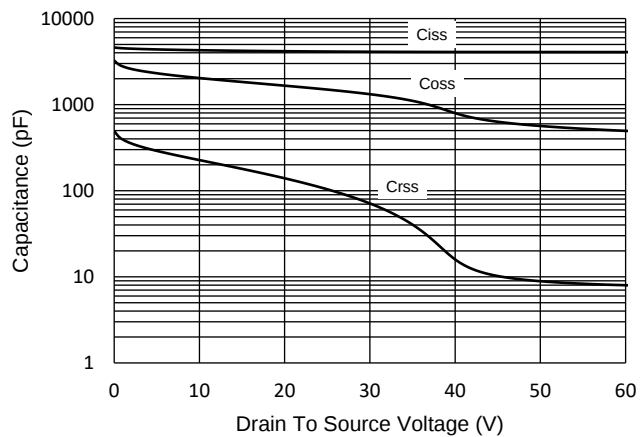
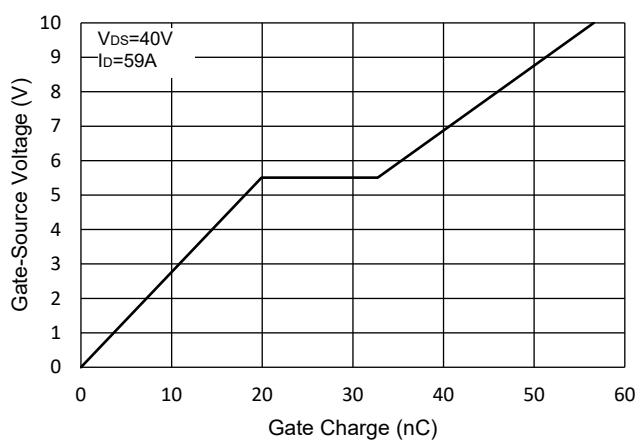


Fig.6 - Gate Charge



Curve Characteristics

Fig.7 - Normalized Threshold Voltage

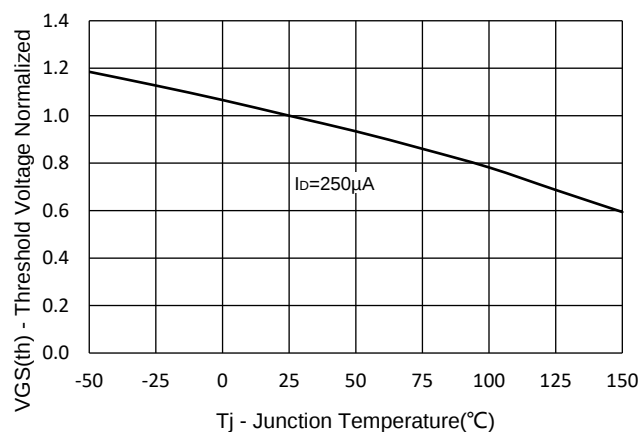


Fig.8 - Normalized On Resistance Characteristics

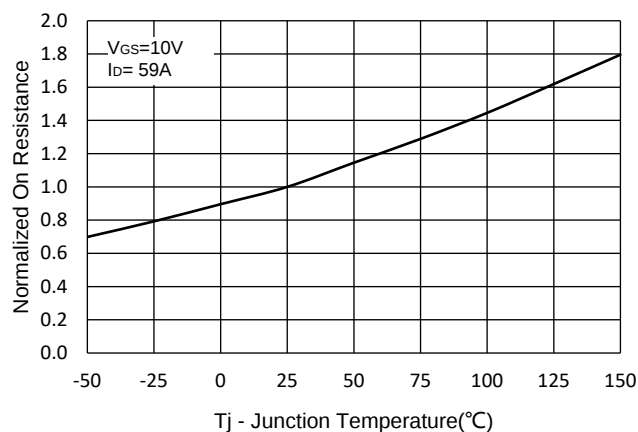


Fig.9 - $I_S - V_{SD}$

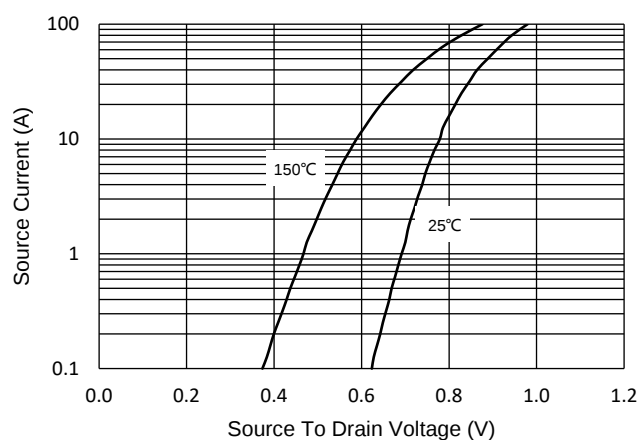


Fig.10 - Drain Current

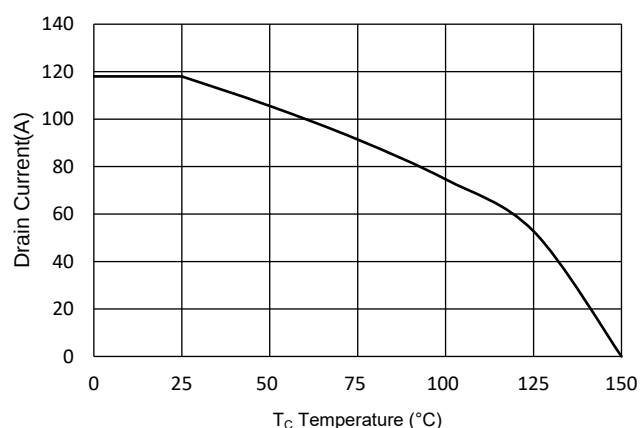
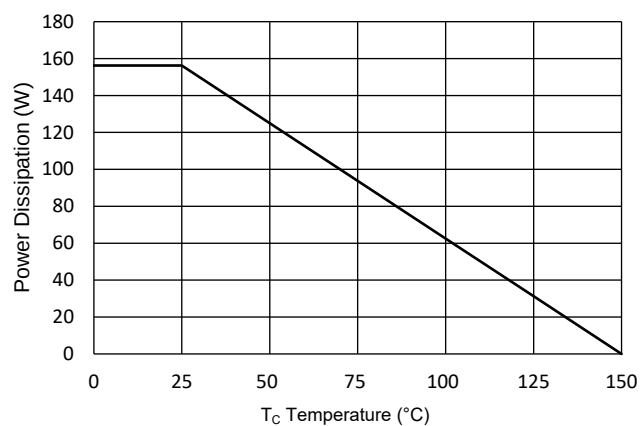


Fig.11 - PD Dissipation



Curve Characteristics

Fig.12 - Safe Operation Area

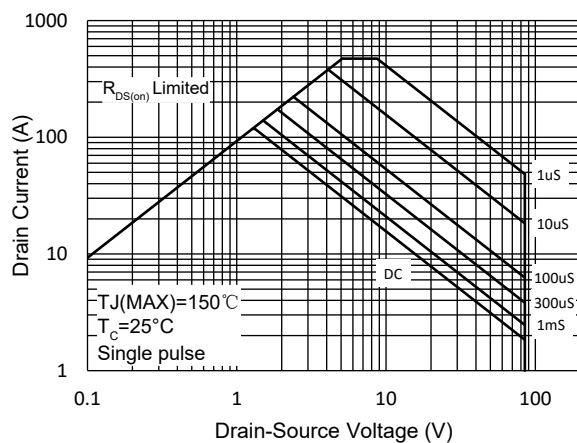
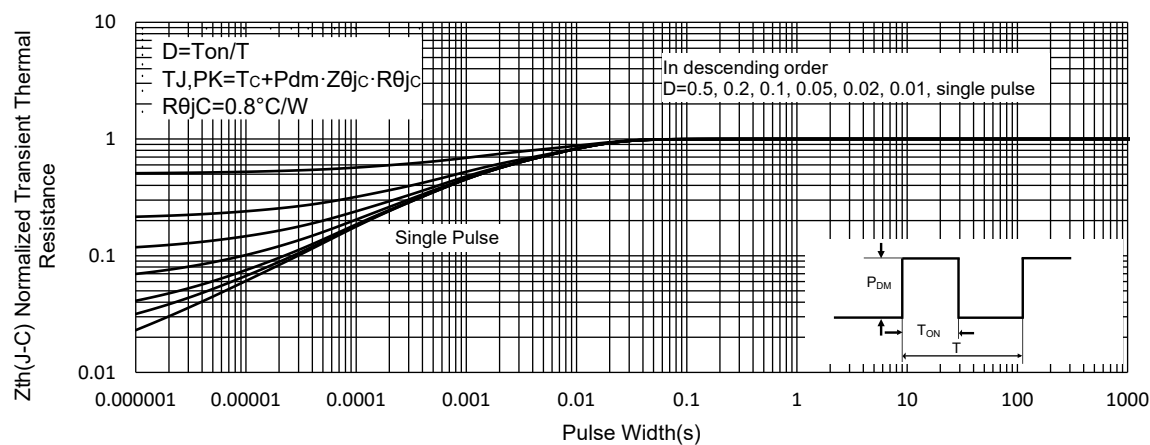


Fig.13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 800pcs/Reel

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