

Features

- AEC-Q101 Qualified
- Split Gate Trench MOSFET Technology
- High Density Cell Design For Low $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

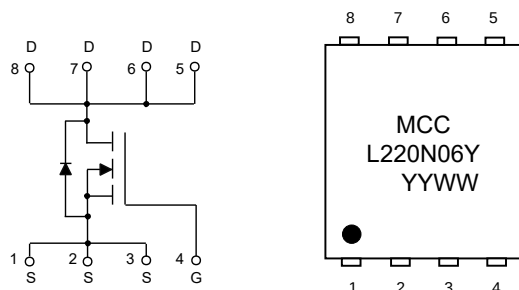
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient^(Note 2)
- Thermal Resistance: 0.85°C/W Junction to Case

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	220	A
		139	
Pulsed Drain Current ^(Note 3)	I_{DM}	800	A
Total Power Dissipation ^(Note 4)	P_D	147	W
Avalanche Energy ^(Note 5)	E_{AS}	1428	mJ

Note:

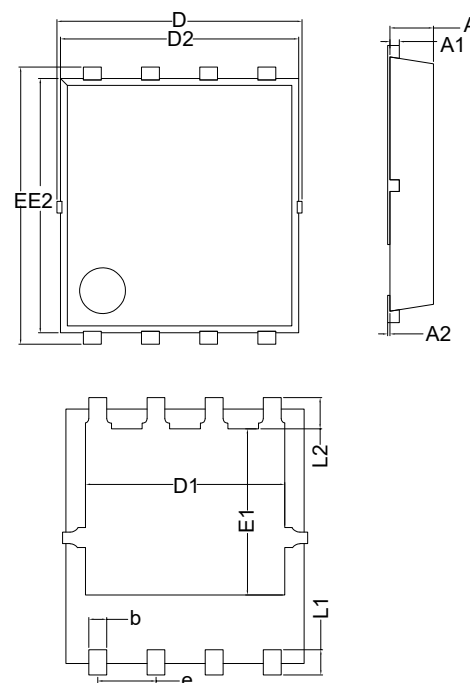
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-case thermal resistance.
5. $T_J=25^\circ\text{C}$, $V_{DD}=50\text{V}$, $V_{GS}=10\text{V}$, $L=2\text{mH}$

Internal Structure and Marking Code



N-CHANNEL MOSFET

DFN5060-C



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
D	0.203	0.218	5.15	5.55	
D2	0.201	0.209	5.10	5.30	
E	0.234	0.242	5.95	6.15	
E2	0.215	0.222	5.45	5.65	
A	0.033	0.041	0.85	1.05	
A1	0.008		0.203		BSC
A2	0.000	0.004	0.00	0.10	
D1	0.167	0.175	4.25	4.45	
E1	0.139	0.147	3.52	3.73	
L1	0.018	0.026	0.45	0.65	
L2	0.027		0.68		BSC
b	0.012	0.020	0.30	0.50	
e	0.050		1.27		BSC

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.8	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		1.3	1.6	mΩ
		V _{GS} =4.5V, I _D =20A		1.7	2.4	
Gate Resistance	R _g	F=1 MHz, Open drain		1.5		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				220	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =110A			1.2	V
Reverse Recovery Time	t _{rr}	I _S =110A,di/dt=100A/μs		90		ns
Reverse Recovery Charge	Q _{rr}			143		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =30V,V _{GS} =0V,f=1MHz		7500		pF
Output Capacitance	C _{oss}			1450		
Reverse Transfer Capacitance	C _{rss}			80		
Total Gate Charge	Q _g	V _{DS} =30V,V _{GS} =10V,I _D =110A		119		nC
Gate-Source Charge	Q _{gs}			25		
Gate-Drain Charge	Q _{gd}			20		
Turn-On Delay Time	t _{d(on)}	V _{GS} =10V,V _{DD} =30V, I _D =110A, R _{GEN} =2.2Ω		20		ns
Turn-On Rise Time	t _r			250		
Turn-Off Delay Time	t _{d(off)}			50		
Turn-Off Fall Time	t _f			15		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

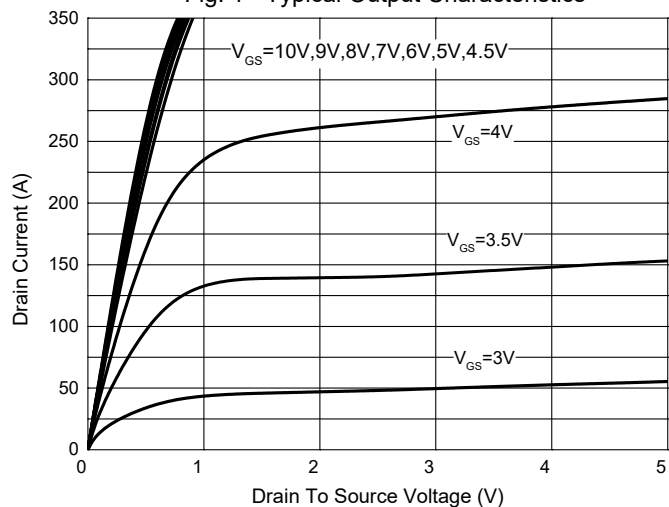


Fig. 2 - Transfer Characteristics

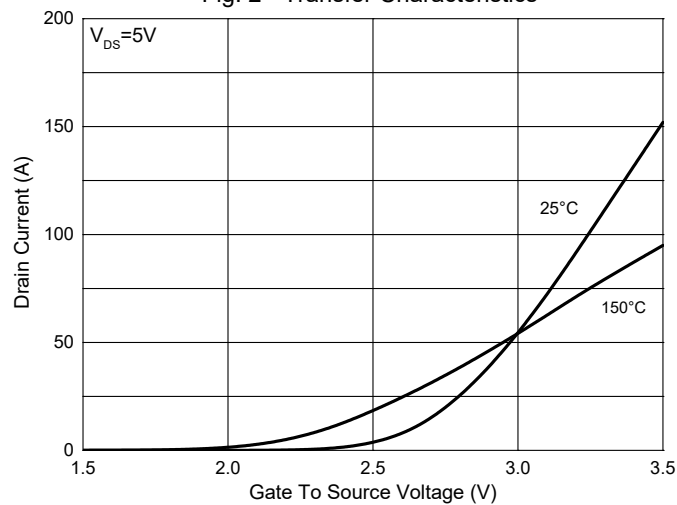


Fig. 3 - $R_{DS(ON)} - V_{GS}$

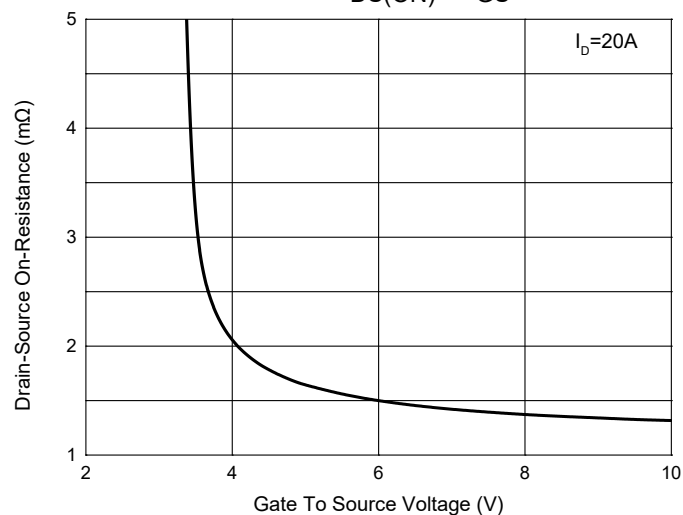


Fig. 4 - $R_{DS(ON)} - I_D$

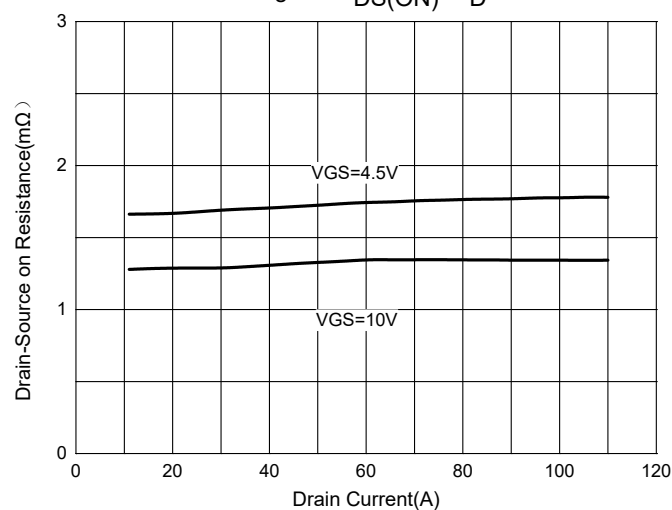


Fig. 5 - Capacitance Characteristics

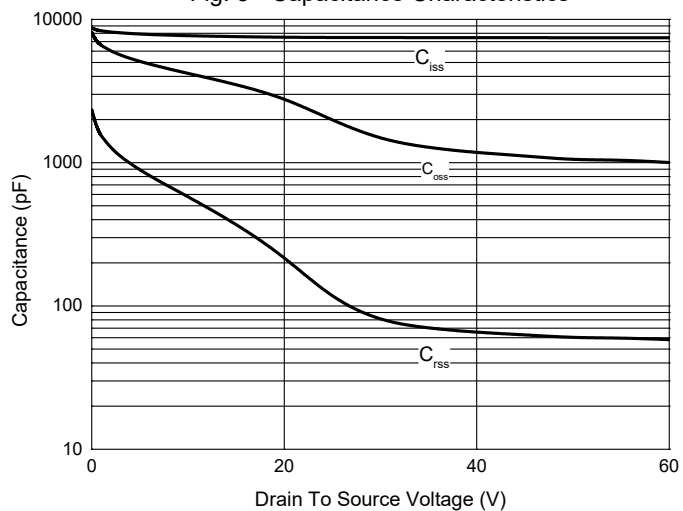
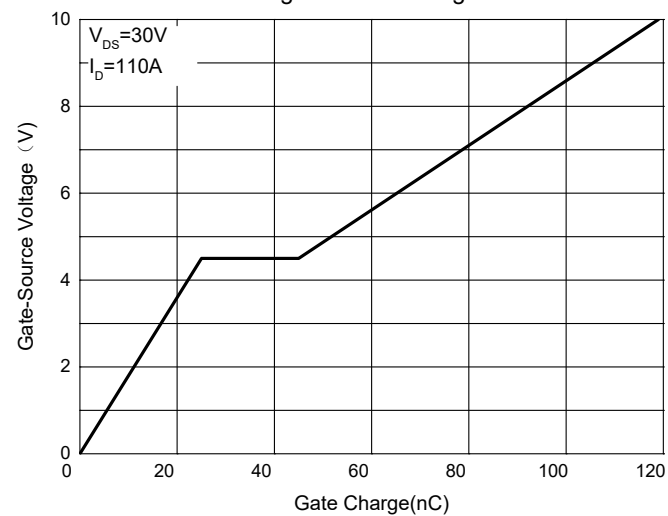


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Normalized Threshold Voltage

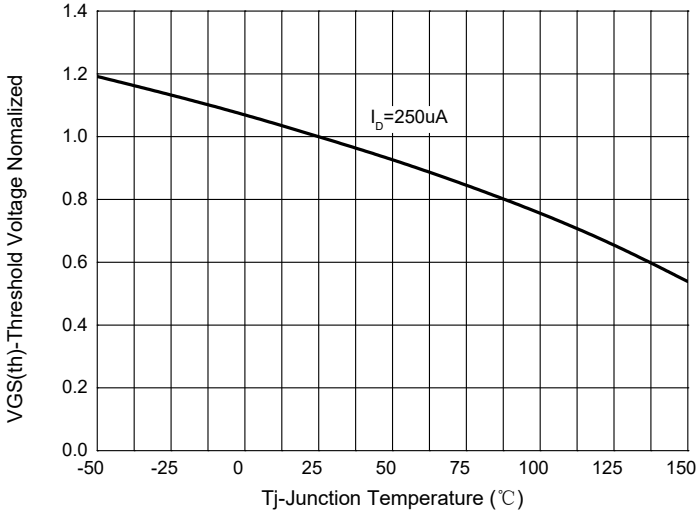


Fig.8-Normalized On Resistance Characteristics

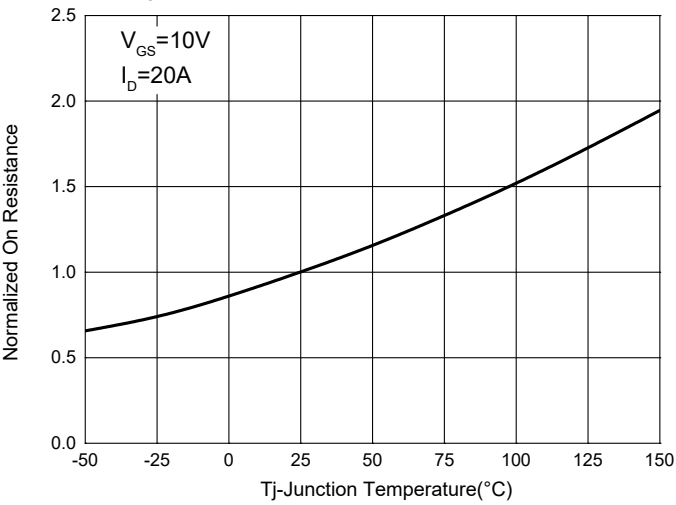


Fig.9 - $I_S - V_{SD}$

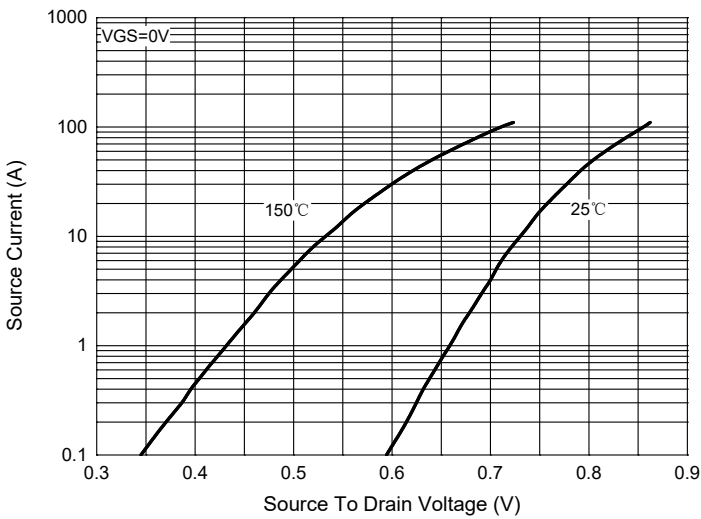


Fig. 10 - Drain Current

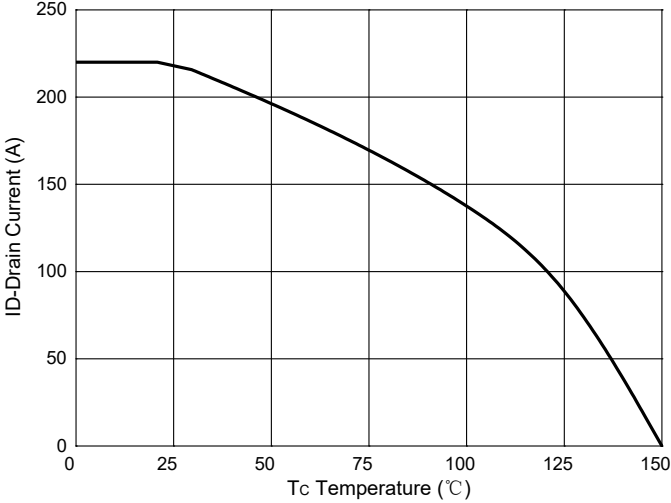
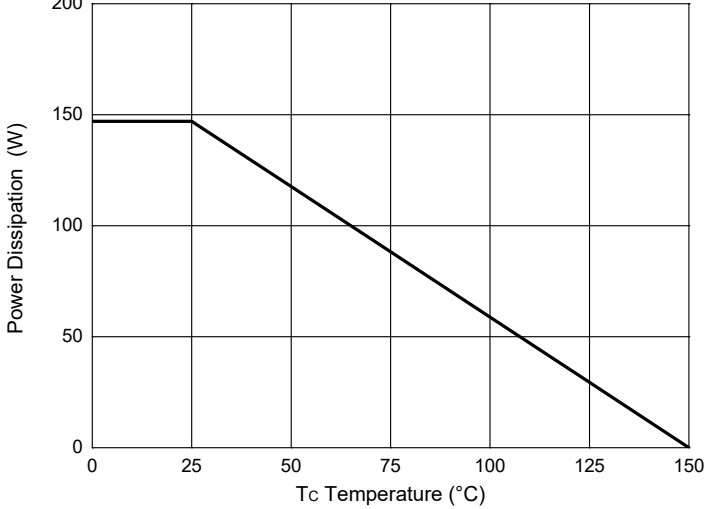


Fig.11-PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

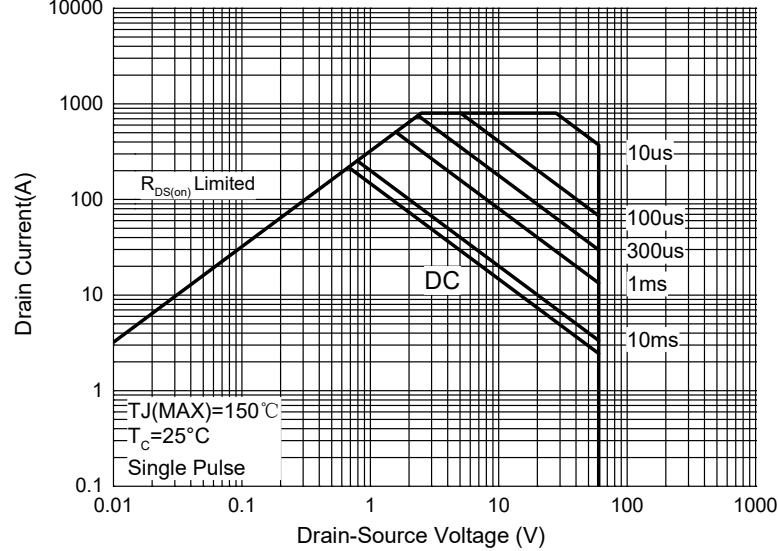
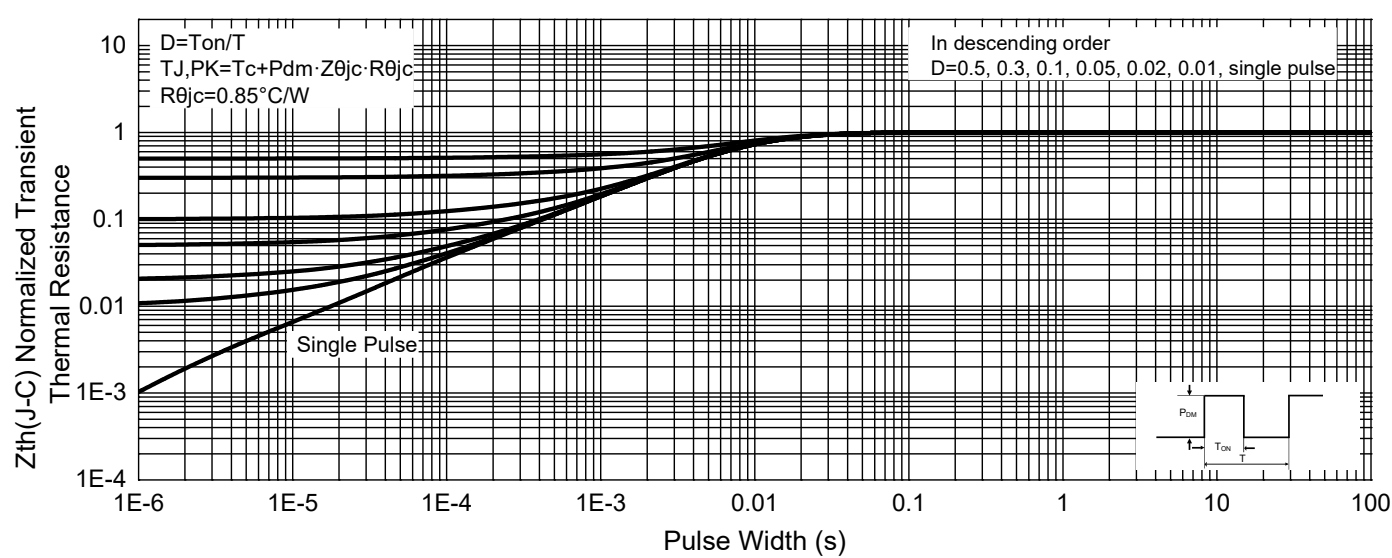


Fig. 13 -Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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