

Features

- AEC-Q101 Qualified
- Split Gate Trench MOSFET technology
- Excellent Package for Heat Dissipation
- High Density Cell Design for Low $R_{DS(on)}$
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

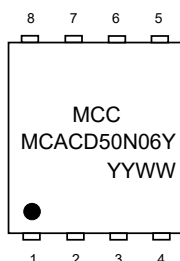
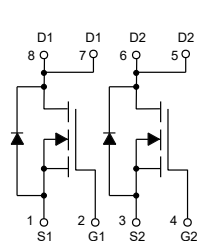
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 50°C/W Junction to Ambient (Note 2)
- Thermal Resistance: 1.8°C/W Junction to Case

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	60	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current	T _c =25°C	I _D	50	A
	T _c =100°C		31	
Pulsed Drain Current ^(Note 3)		I _{DM}	150	A
Total Power Dissipation ^(Note 4)		P _D	69	W
Single Pulsed Avalanche Energy ^(Note 5)		E _{AS}	162	mJ

Note:

1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The Power dissipation P_{DSM} is based on $R_{\theta JA} \leq 10\text{s}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
3. Repetitive rating; pulse width limited by max. junction temperature.
4. P_D is based on max. junction temperature, using junction-Case thermal resistance.
5. $T_J=25^\circ\text{C}$, $V_{DD}=40\text{V}$, $V_{GS}=10\text{V}$, $R_G=25\Omega$, $L=1\text{mH}$.

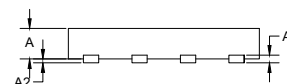
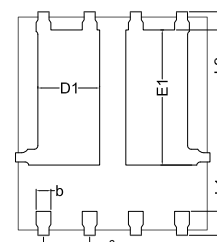
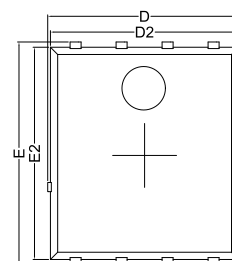
Internal Structure and Marking Code



4 codes in total
YY is the year
WW is the week

Dual N-CHANNEL MOSFET

PDFN5060-8D



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
D	0.203	0.218	5.15	5.55	
D2	0.197	0.213	5.00	5.40	
E	0.234	0.250	5.95	6.35	
E2	0.223	0.238	5.66	6.06	
A	0.039	0.047	1.00	1.20	
A1	0.010		0.254		BSC
A2	0.000	0.004	0.00	0.10	
D1	0.059	0.075	1.50	1.90	
E1	0.139	0.154	3.52	3.92	
L1	0.022	0.030	0.56	0.76	
L2	0.019		0.50		BSC
b	0.012	0.020	0.31	0.51	
e	0.050		1.27		BSC

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1	1.8	2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		6.5	9	mΩ
		V _{GS} =4.5V, I _D =10A		8.5	13	
Gate Resistance	R _g	f=1MHz		1.5		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				50	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =20A			1.2	V
Reverse Recovery Time	t _{rr}	I _S =20A, di/dt=500A/μs		18		ns
Reverse Recovery Charge	Q _{rr}			30		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} =0V, f=1MHz		2100		pF
Output Capacitance	C _{oss}			630		
Reverse Transfer Capacitance	C _{rss}			33		
Total Gate Charge	Q _g	V _{DS} =30V, V _{GS} =10V, I _D =20A		30		nC
Gate-Source Charge	Q _{gs}			6		
Gate-Drain Charge	Q _{gd}			5		
Turn-On Delay Time	t _{d(on)}	V _{DS} =30V, V _{GS} =10V, R _{GEN} =3Ω, I _D =20A		10		ns
Turn-On Rise Time	t _r			34		
Turn-Off Delay Time	t _{d(off)}			26.2		
Turn-Off Fall Time	t _f			45		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

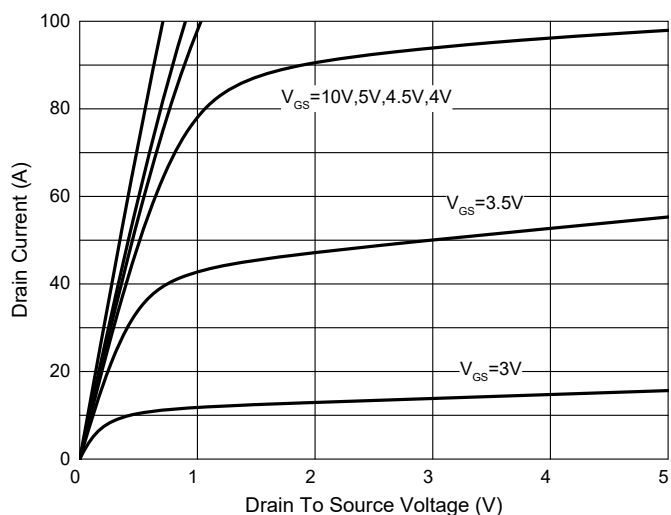


Fig. 2 - Transfer Characteristics

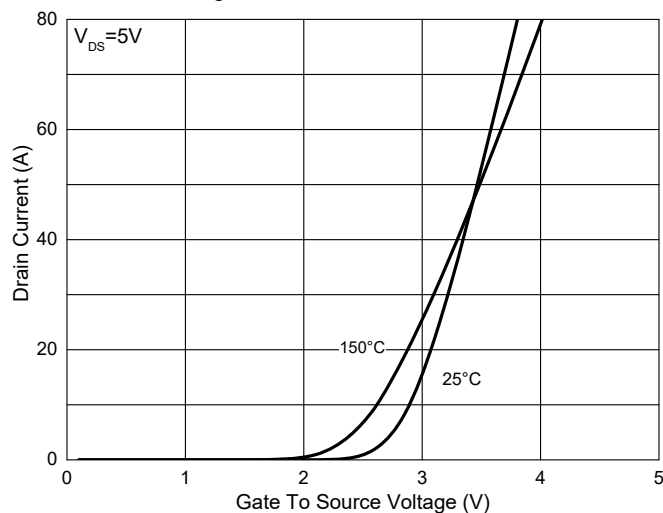


Fig. 3 - $R_{DS(ON)} - V_{GS}$

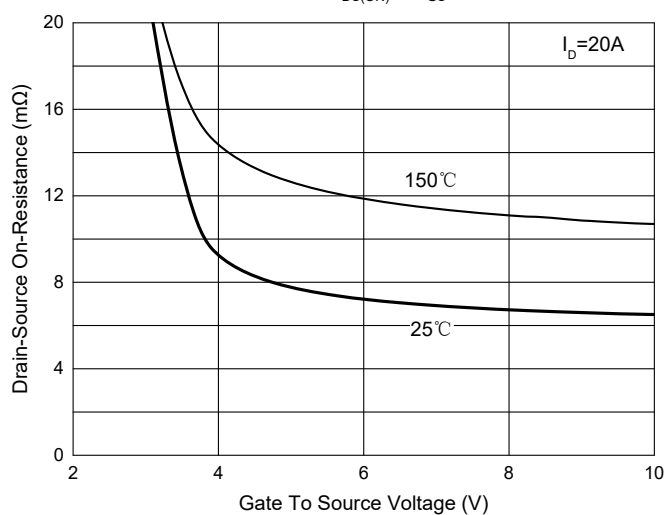


Fig. 4 - $R_{DS(ON)} - I_D$

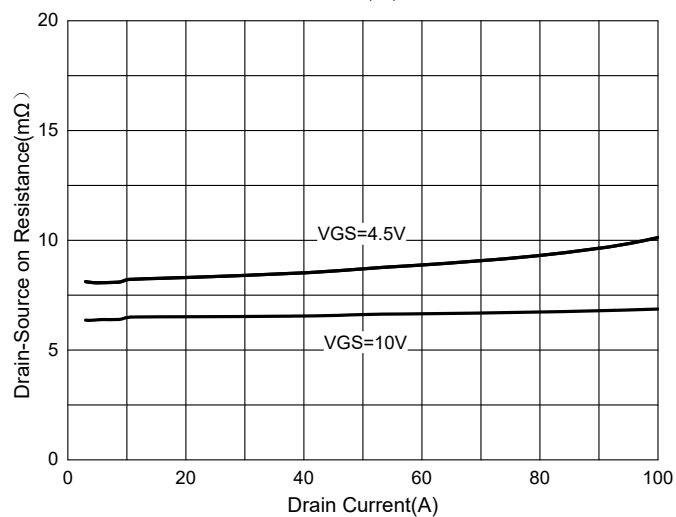


Fig. 5 - Capacitance Characteristics

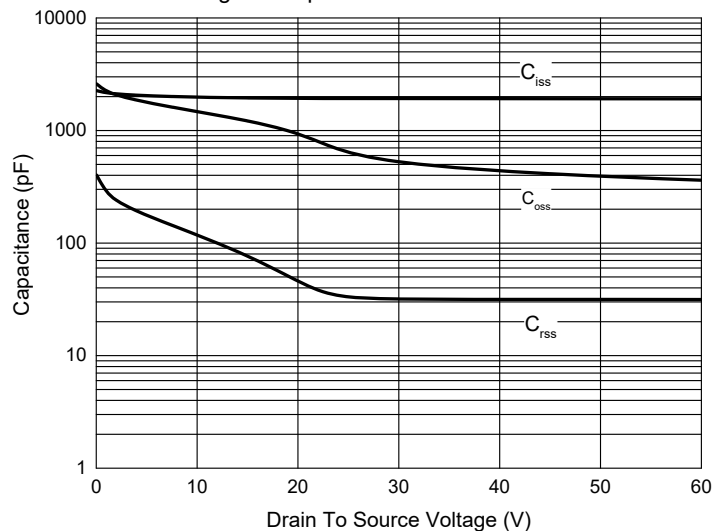
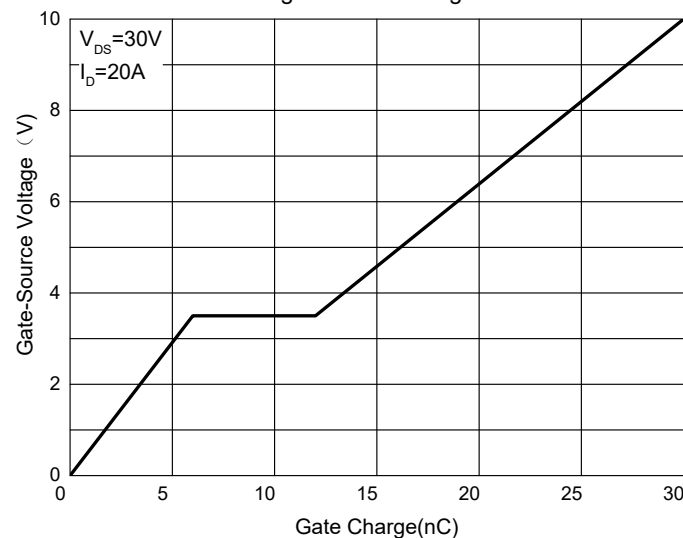


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Nomalized Threshold voltage

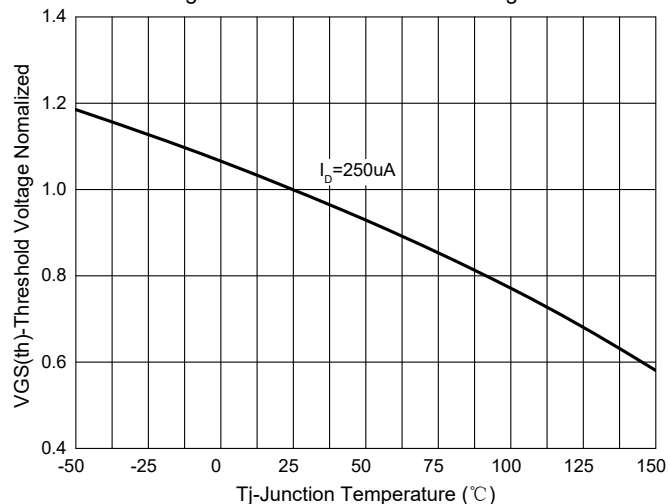


Fig.8-Normalized On Resistance Characteristics

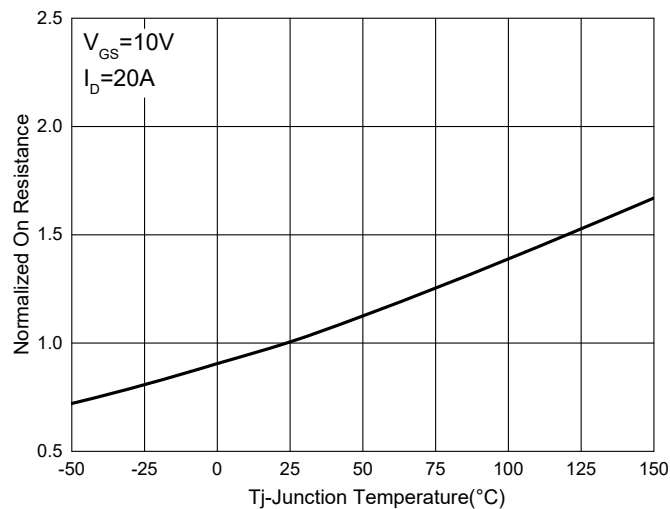


Fig.9 - $I_S - V_{SD}$

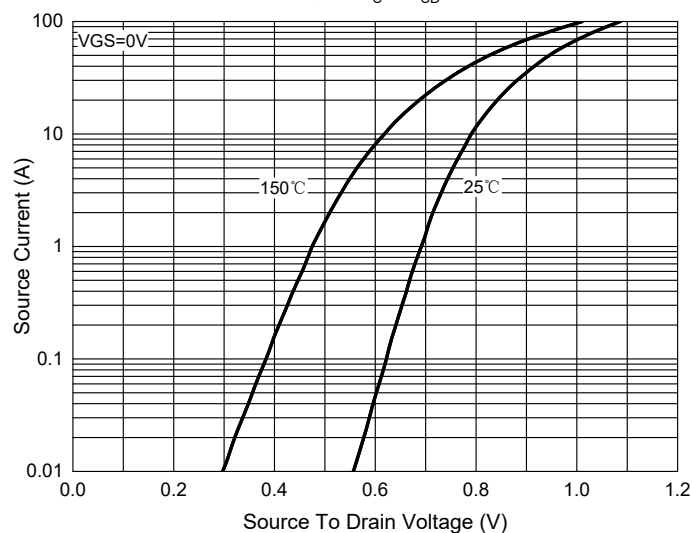


Fig. 10 - Drain Current

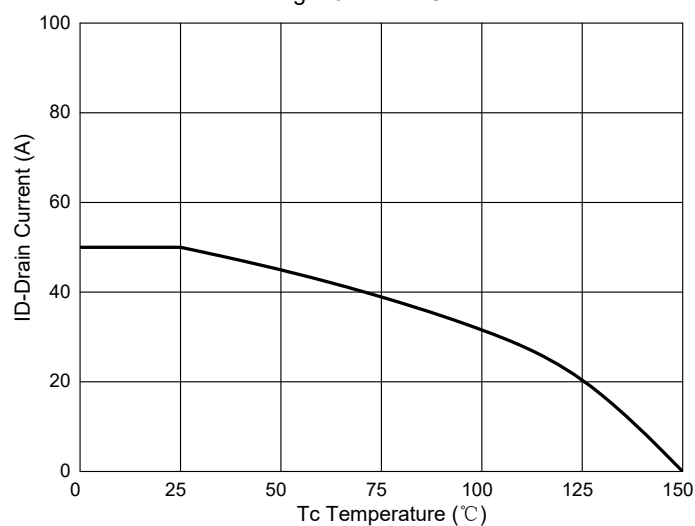
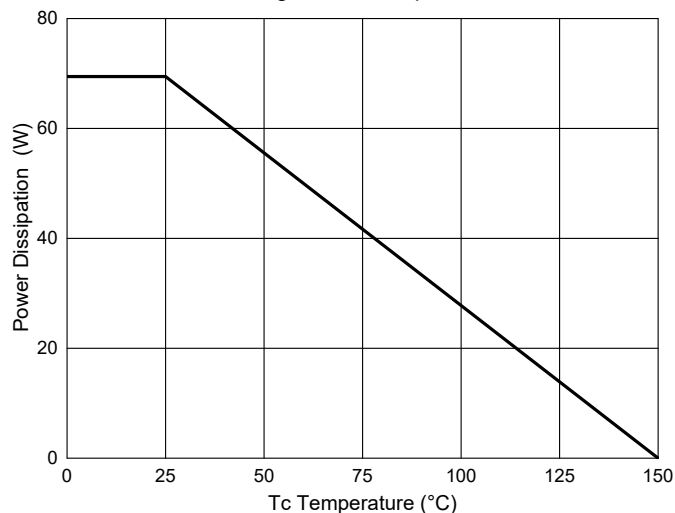


Fig.11-PD Dissipation



Curve Characteristics

Fig. 12 - Safe Operation Area

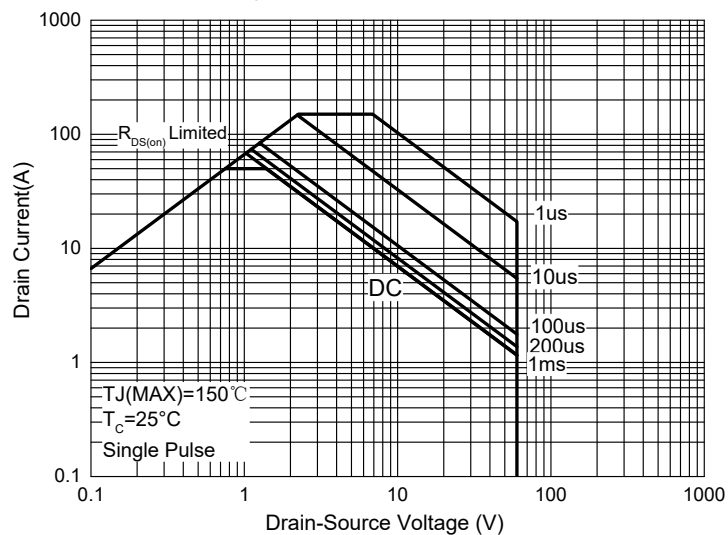
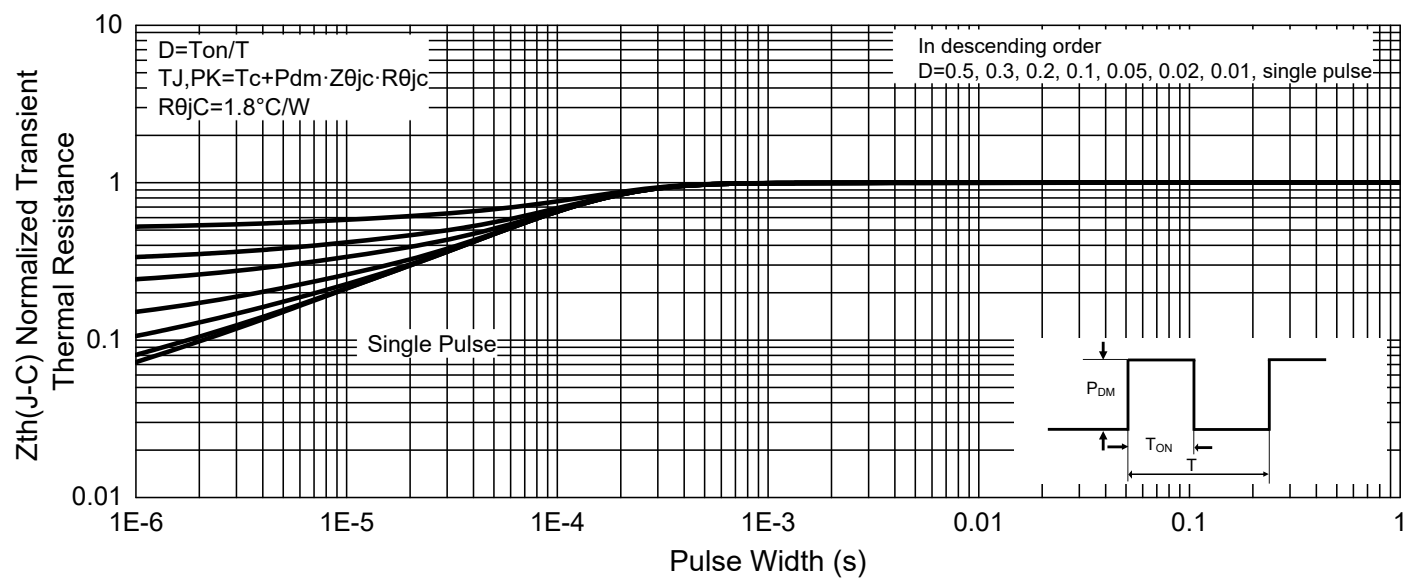


Fig. 13 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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