

Features

- AEC-Q101 Qualified
- · Trench LV MOSFET Technology
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- · Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

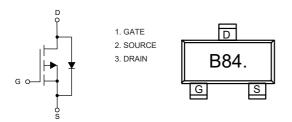
- Operating Junction Temperature Range : -55°C to +175°C
- Storage Temperature Range: -55°C to +175°C
- Thermal Resistance: 504°C/W Junction to Ambient (Note 2)

Parameter		Symbol	Rating	Unit	
Drain-Source Voltage		V _{DS}	-60	V	
Gate-Source Volltage		V _{GS}	±20	V	
Continuous Drain Current	T _A =25°C		-0.2	Α	
	T _A =100°C	- I _D	-0.14		
Pulsed Drain Current (Note 3)		I _{DM}	-0.8	Α	
Total Power Dissipation (Note 4)		P _D	0.3	W	

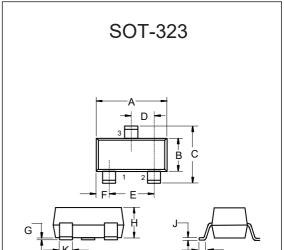
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.
- 3. Repetitive rating; pulse width limited by max. junction temperature.
- 4. P_D is based on max. junction temperature, using junction-ambient thermal resistance.

Internal Structure and Marking Code

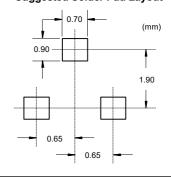


P-Channel MOSFET



DIMENSIONS						
DIM INCHES		HES	MM		NOTE	
DIIVI	MIN	MAX	MIN	MAX	NOTE	
Α	0.071	0.087	1.80	2.20		
В	0.045	0.053	1.15	1.35		
С	0.083	0.096	2.10	2.45		
D	0.026		0.65		TYP.	
E	0.047	0.055	1.20	1.40		
F	0.012	0.016	0.30	0.40		
G	0.000	0.004	0.00	0.10		
Н	0.035	0.044	0.90	1.10		
J	0.002	0.010	0.05	0.25		
K	0.006	0.016	0.15	0.40		
L	0.010	0.018	0.26	0.46		

Suggested Solder Pad Layout



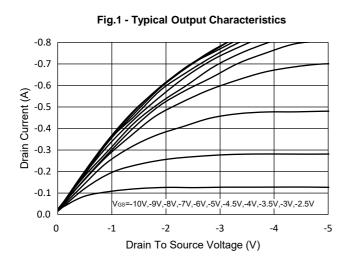


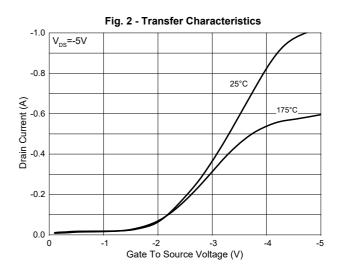
Electrical Characteristics @ 25°C (Unless Otherwise Specified)

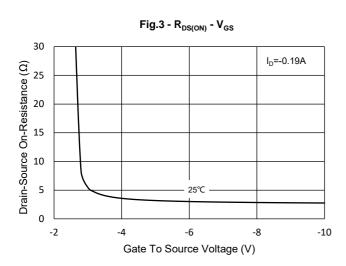
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-60			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-1	μΑ	
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-0.9	-1.4	-2.0	V	
Drain-Source On-Resistance	Б	V _{GS} =-10V, I _D =-0.15A		3.0	3.6		
	$R_{DS(on)}$			3.5	4.5	Ω	
Gate Resistance	R _g	f=1 MHz, Open drain		48		Ω	
Diode Characteristics							
Continuous Body Diode Current	Is				-0.2	Α	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-0.17A			-1.2	V	
Reverse Recovery Time	t _{rr}	I _F =-0.19A,di/dt=100A/μs		20		ns	
Reverse Recovery Charge	Q _{rr}	- 1 _F =-0.19Α,αι/αι=100Α/μS		10		nC	
Dynamic Characteristics							
Input Capacitance	C _{iss}			26			
Output Capacitance	C _{oss}	V _{DS} =-25V,V _{GS} =0V,f=1MHz		3.3		pF	
Reverse Transfer Capacitance	C _{rss}			1.7			
Total Gate Charge	Q_g			1.5			
Gate-Source Charge	Q _{gs}	V _{DS} =-30V,V _{GS} =-10V,I _D =-0.19A		0.3		nC	
Gate-Drain Charge	Q_{gd}			0.2		İ	
Turn-On Delay Time	t _{d(on)}			2.3			
Turn-On Rise Time	t _r	V _{DS} =-30V, V _{GS} =-10V,		16			
Turn-Off Delay Time	t _{d(off)}	$R_{G}=3\Omega$, $I_{D}=-0.19A$		11		ns	
Turn-Off Fall Time	t _f			28			

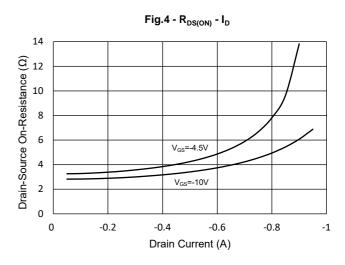


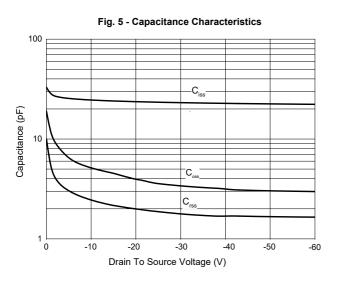
Curve Characteristics

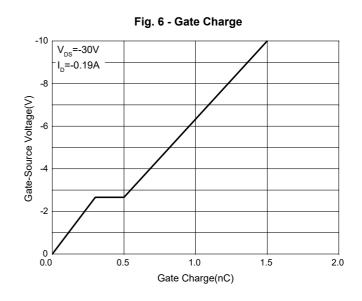






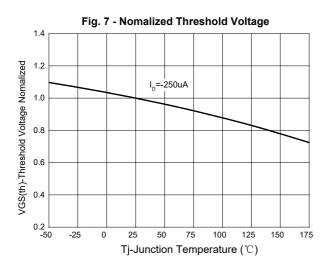


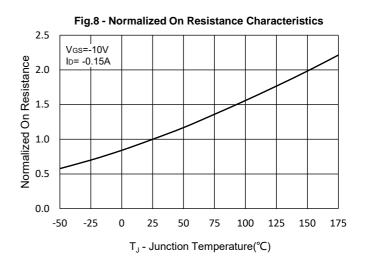


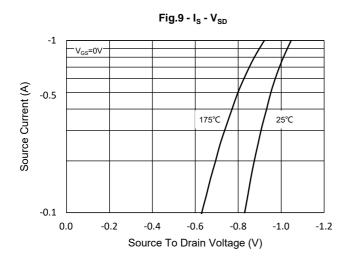


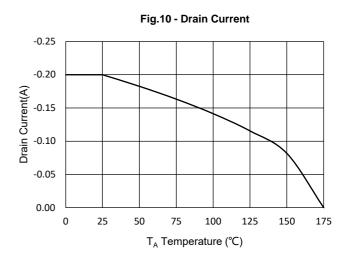


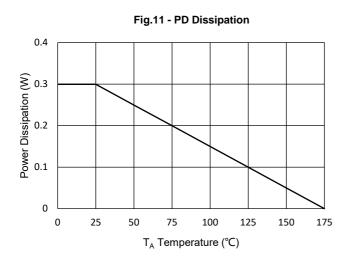
Curve Characteristics













Curve Characteristics



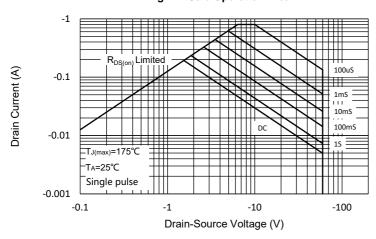
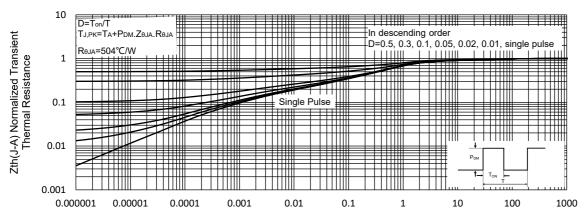


Fig.13 - Normalized Transient Thermal Impedance





Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel:3Kpcs/Reel	

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