

Features

- High Density Cell Design for Low $R_{DS(ON)}$
- Voltage Controlled Small Signal Switch
- Epoxy Meets UL 94 V-0 Flammability Rating
- Moisture Sensitivity Level 1
- Halogen Free. "Green" Device (Note 1)
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

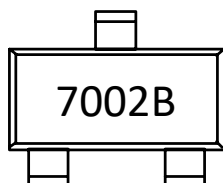
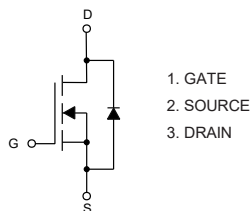
Maximum Ratings

- Operating Junction Temperature Range: -55°C to $+150^{\circ}\text{C}$
- Storage Temperature: -55°C to $+150^{\circ}\text{C}$
- Thermal Resistance: 417°C/W Junction to Ambient

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 30	V
Drain Current-Continuous	I_D	0.34	A
Pulsed Drain Current	I_{DM}	1.2	A
Power Dissipation	P_D	0.3	W

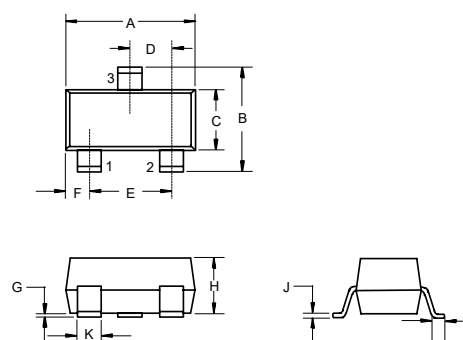
Note: 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Internal Structure and Marking Code



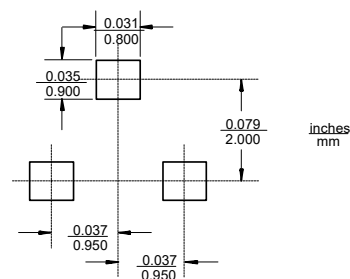
N-Channel MOSFET

SOT-23



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.110	0.120	2.80	3.04	
B	0.083	0.104	2.10	2.64	
C	0.047	0.055	1.20	1.40	
D	0.034	0.041	0.85	1.05	
E	0.067	0.083	1.70	2.10	
F	0.018	0.024	0.45	0.60	
G	0.0004	0.006	0.01	0.15	
H	0.035	0.043	0.90	1.10	
J	0.003	0.007	0.08	0.18	
K	0.012	0.020	0.30	0.51	
L	0.007	0.020	0.20	0.50	

Suggested Solder Pad Layout



ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	60			V
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	1.0	1.4	2.5	V
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V			1	μA
On-State Drain Current	I _{D(ON)}	V _{DS} =7.0V, V _{GS} =10V	500			mA
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =500mA			3	Ω
		V _{GS} =4.5V, I _D =200mA			4	
Dynami Characteristics						
Input Capacitance ^(Note2)	C _{iss}	V _{DS} =25V,V _{GS} =0V, f=1MHz			50	pF
Output Capacitance ^(Note2)	C _{oss}				25	
Reverse Transfer Capacitance ^(Note2)	C _{rss}				5	
Switching Characteristics						
Turn-On Delay Time ^(Note2)	t _{d(on)}	V _{DD} =25V,V _{GEN} =10V,R _L =50Ω, I _D =500mA,R _{GEN} =25Ω			20	ns
Turn-Off Delay Time ^(Note2)	t _{d(off)}				40	
Source-Drain Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =115mA	0.6	0.82	1	V
Source Current Continuous	I _S				340	mA

Note: 2. These parameters have no way to verify.

Curve Characteristics

Fig. 1 - Typical Output Characteristics

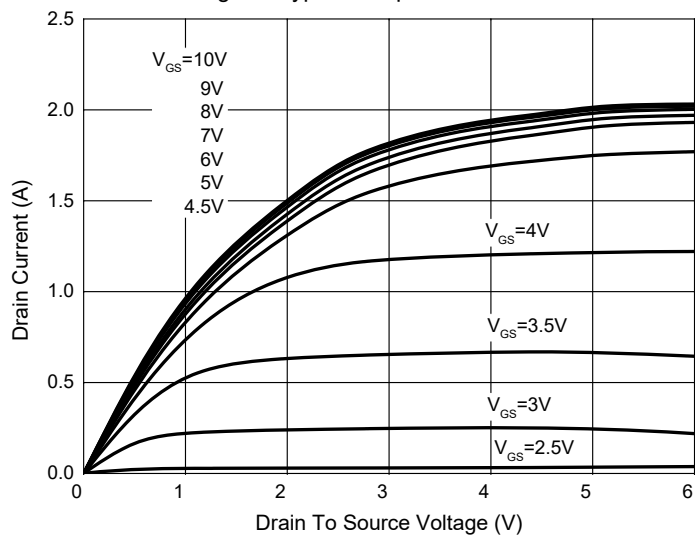


Fig. 2 - Transfer Characteristics

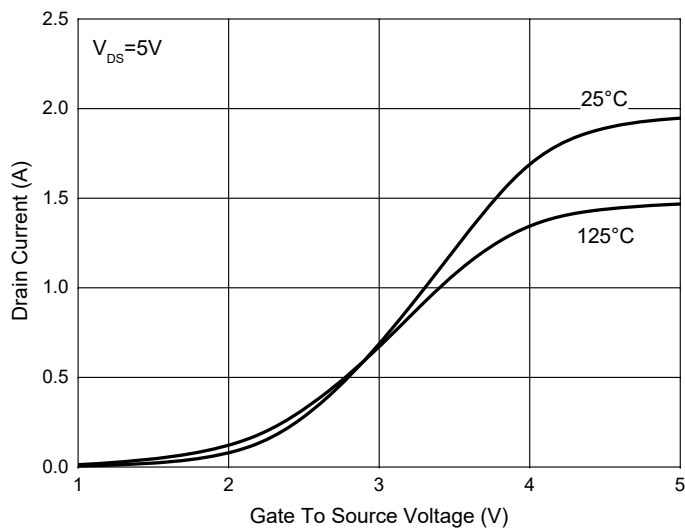


Fig. 3 - $R_{DS(ON)} - I_D$

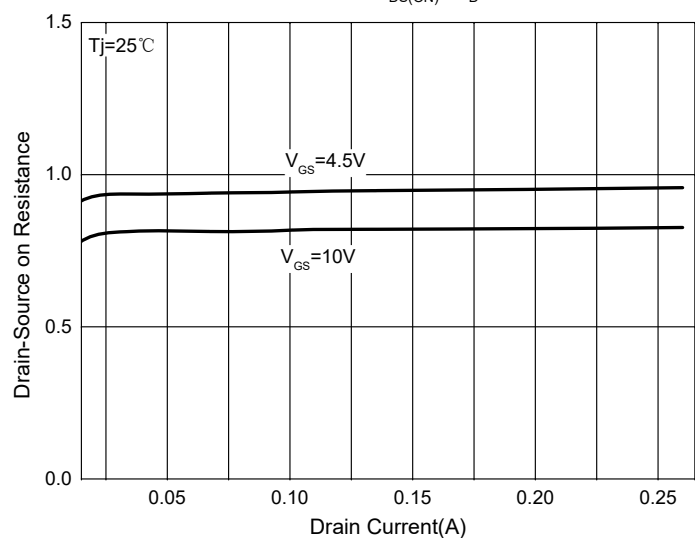


Fig. 4 - $R_{DS(ON)} - V_{GS}$

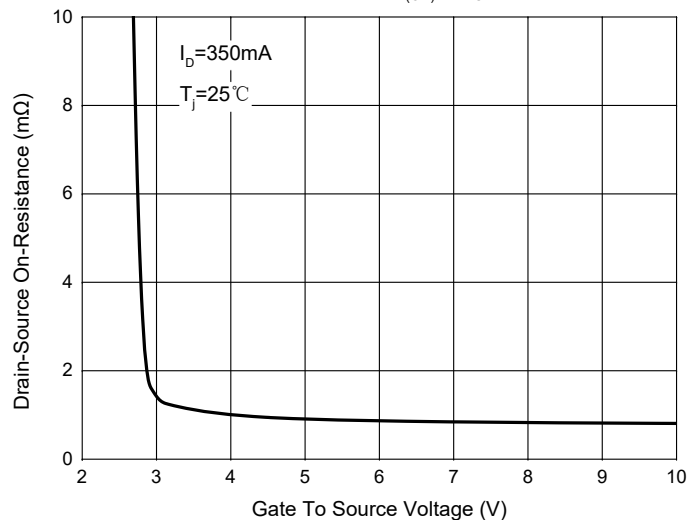


Fig. 5 - $I_S - V_{SD}$

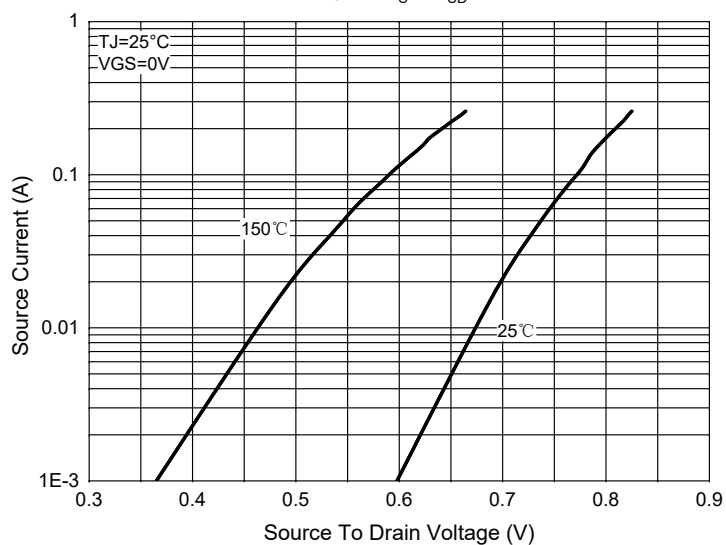
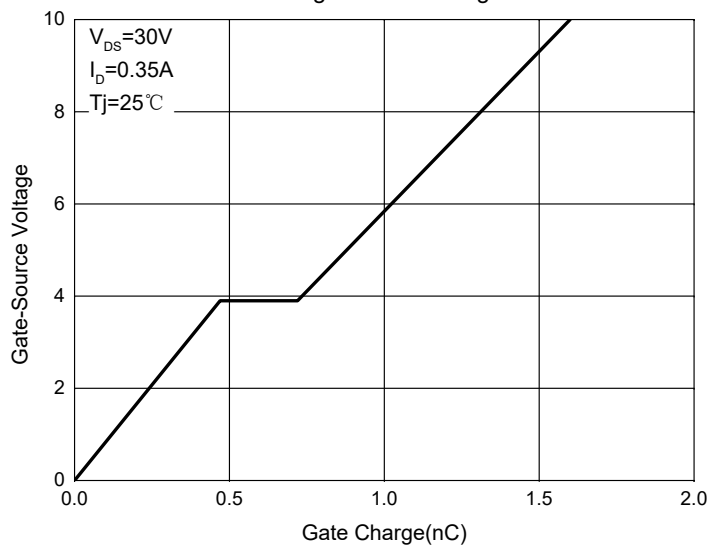


Fig. 6 - GateCharge



Curve Characteristics

Fig.7-NormalizedOnResistanceCharacteristics

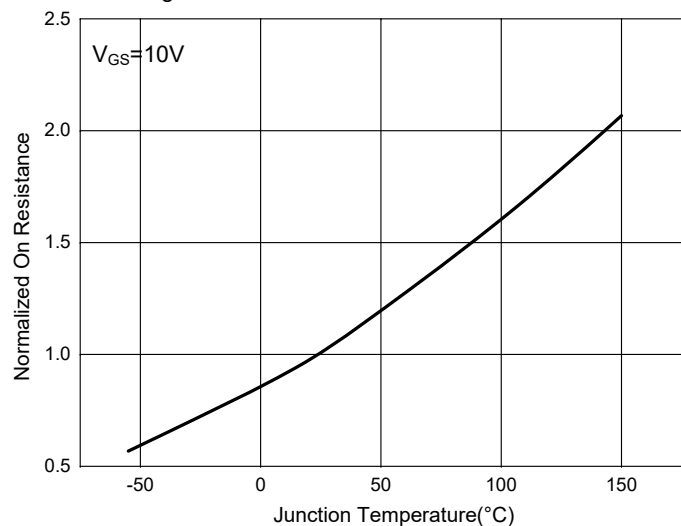


Fig. 8- Capacitance Characteristics

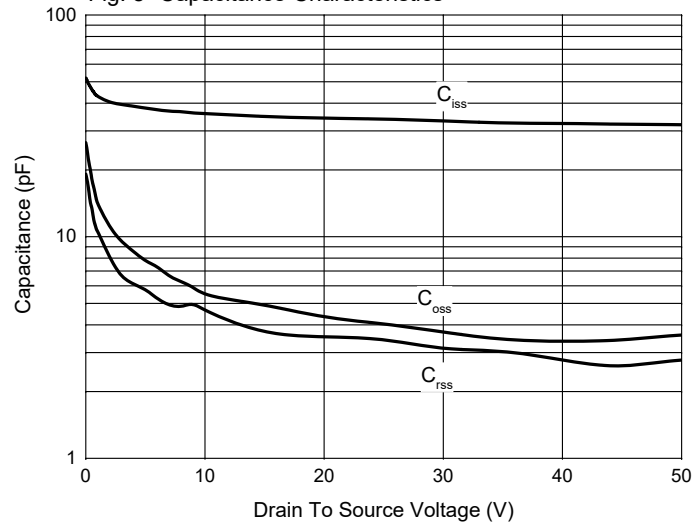


Fig. 6 - Safe Operation Area

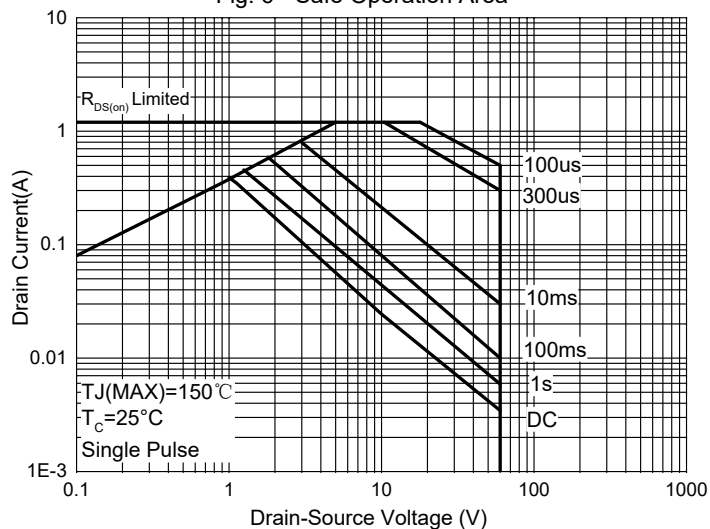
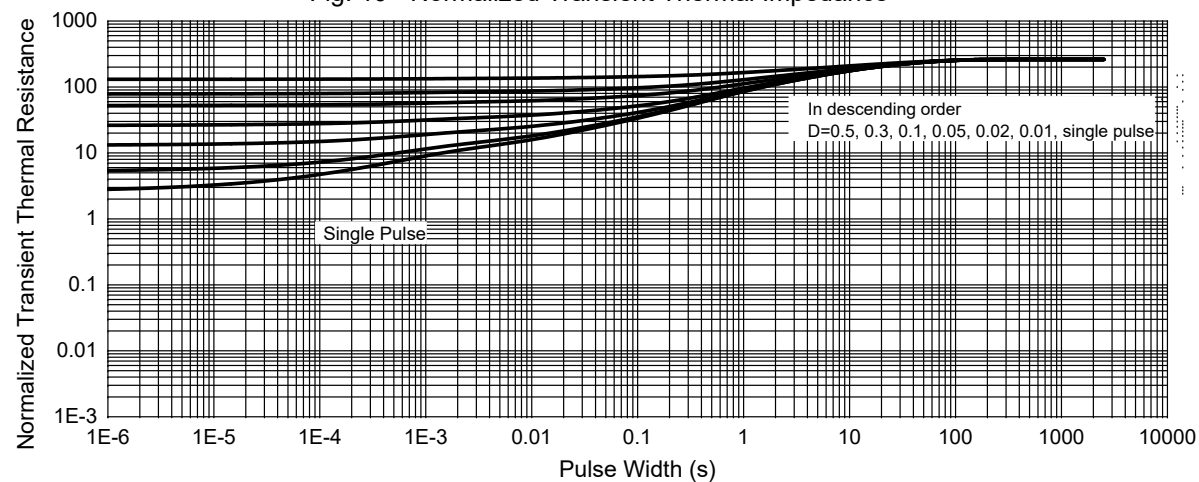


Fig. 10 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel:3Kpcs/Reel

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