General Description

The MAX6887/MAX6888 multivoltage supply supervisors provide several voltage-detector inputs, one watchdog input, and three outputs. Each voltage-detector input offers a factory-set undervoltage and overvoltage threshold. Manual reset and margin disable inputs offer additional flexibility.

The MAX6887 offers six voltage-detector inputs, while the MAX6888 offers four inputs. Output RESET asserts when any input voltage drops below its respective undervoltage threshold or manual reset MR is asserted. Output OV asserts when any input voltage exceeds its respective overvoltage threshold. Monitor standard supply voltages listed in the *Selector Guide*.

The MAX6887/MAX6888 offer a watchdog timer with an initial and normal timeout periods of 102.4s and 1.6s, respectively. Watchdog output WDO asserts when the watchdog timer expires. Connect WDO to manual reset input MR to generate resets when the watchdog timer expires. RESET, OV, and WDO are active-low, opendrain outputs.

The MAX6887/MAX6888 are available in a 5mm x 5mm x 0.8mm, 16-pin thin QFN package and operate over the extended -40°C to +85°C temperature range.

Applications

Multivoltage Systems Telecom Networking Servers/Workstations/Storage Systems

Fe	а	tı	ır	e	S

- Hex/Quad Voltage Detectors
- Undervoltage and Overvoltage Thresholds

- 1% Threshold Accuracy
- Margining Disable and Manual Reset Input
- Watchdog Timer
- ♦ Open-Drain RESET, OV, and WDO Outputs
- ♦ 180ms (min) Reset Timeout Period
- Few External Components
- Small 5mm x 5mm, 16-Pin Thin QFN Packages

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX6887_ETE+	-40°C to +85°C	16 Thin QFN-EP*
MAX6888_ETE+	-40°C to +85°C	16 Thin QFN-EP*

Note: Insert the desired letter from the Selector Guide into the blank to complete the part number.

+Denotes a lead(Pb)-free/RoHS-compliant package. *EP = Exposed pad.

Pin Configurations and Typical Operating Circuit appear at end of data sheet.

Selector Guide

DADT	NC	NOMINAL INPUT VOLTAGE (V)*							
PARI	IN1	IN2	IN3	IN4	IN5	IN6	(%)		
MAX6887AETE	5.0	3.3	2.5	1.8	Adj	Adj	5		
MAX6887BETE	5.0	3.3	2.5	Adj	Adj	Adj	5		
MAX6887CETE	5.0	3.3	1.8	Adj	Adj	Adj	5		
MAX6887DETE	3.3	2.5	1.8	1.5	Adj	Adj	5		
MAX6887EETE	3.3	2.5	1.8	Adj	Adj	Adj	5		
MAX6887FETE	3.3	2.5	1.5	Adj	Adj	Adj	5		
MAX6887GETE	3.3	2.5	Adj	Adj	Adj	Adj	5		
MAX6887HETE	3.3	1.8	Adj	Adj	Adj	Adj	5		
MAX6887QETE	Adj	Adj	Adj	Adj	Adj	Adj	5		

NOMINAL INPUT VOLTAGE (V)* TOL PART (%) IN1 IN2 IN3 IN4 IN5 IN6 MAX6887IETE 5.0 3.3 2.5 1.8 Adj 10 Adj MAX6887JETE 5.0 3.3 2.5 Adj Adj Adj 10 MAX6887KETE 5.0 3.3 1.8 Adj Adj Adj 10 MAX6887LETE 3.3 2.5 1.8 1.5 Adj 10 Adj MAX6887METE 3.3 2.5 1.8 Adi Adj Adj 10 MAX6887NETE 3.3 2.5 1.5 Adj Adj 10 Adj MAX6887OETE 3.3 2.5 Adj Adj Adj Adj 10 MAX6887PETE 3.3 1.8 Adi Adi Adj Adi 10 MAX6887RETE Adj Adj Adj Adj Adj Adj 10

*See thresholds options tables (Tables 1 and 2) for actual undervoltage and overvoltage thresholds.

Selector Guides continued at end of data sheet.

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

ABSOLUTE MAXIMUM RATINGS

Maximum Junction Temperature	+150°C
Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{IN1}-V_{IN4} \text{ or } V_{CC} = 2.7 \text{V to } 5.8 \text{V}, \text{WDI} = \text{GND}, \overline{\text{MARGIN}} = \overline{\text{MR}} = \text{BP}, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted}.$ Typical values are at $T_A = +25^{\circ}\text{C}.$) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	МАХ	UNITS
Operating Voltage Range (Note 3)		Voltage on either one of IN1–IN4 or V_{CC} to guarantee the part is fully operational	2.7		5.8	V
Supply Current	Icc	V_{IN1} = 5.8V, IN2–IN6 = GND, no load		0.9	1.2	mA
Threshold Accuracy	V_{τ}	IN1–IN6, IN_ falling, $T_A = +25^{\circ}C$ to $+85^{\circ}C$	-1		+1	9/ \/
(See the Selector Guide)	VIH	IN1–IN6, IN_ falling, $T_A = -40^{\circ}C$ to $+85^{\circ}C$	-1.5		+1.5	≫ VIH
Threshold Hysteresis	VTH-HYST			0.3		% V _{TH}
Threshold Tempco	$\Delta V_{TH}/^{\circ}C$			10		ppm/°C
IN_ Input Impedance	R _{IN}	For VIN_ < highest VIN1–IN4 and VIN_ < VCC (not ADJ), thresholds are not set as adjustable	130	200	300	kΩ
	lu i	IN5, IN6 (MAX6887 only)	150		. 150	A
IN_ Input Leakage Current	IIN	IN1–IN4 set as adjustable thresholds	-150		+150	ΠA
Power-Up Delay	tD-PO	$V_{CC} \ge 2.5V$			2.5	ms
IN_ to $\overline{\text{RESET}}$ or $\overline{\text{OV}}$ Delay	tD-R	IN_ falling/rising, 100mV overdrive		20		μs
RESET Timeout Period	t _{RP}		180	200	220	ms
OV Timeout Period	top			25		μs
$\overline{\text{RESET}}, \overline{\text{OV}}, \text{ and } \overline{\text{WDO}} \text{ Output} \\ \text{Low}$	V _{OL}	I _{SINK} = 4mA, output asserted			0.4	V
RESET, OV, and WDO Output Open-Drain Leakage Current	ILKG	Output high impedance	-1		+1	μA

ELECTRICAL CHARACTERISTICS (continued)

 $(V_{IN1}-V_{IN4} \text{ or } V_{CC} = 2.7V \text{ to } 5.8V, \text{ WDI} = \text{GND}, \overline{\text{MARGIN}} = \overline{\text{MR}} = \text{BP}, T_A = -40^{\circ}\text{C} \text{ to } +85^{\circ}\text{C}, \text{ unless otherwise noted}.$ Typical values are at $T_A = +25^{\circ}\text{C}.)$ (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	МАХ	UNITS
	VIL				0.6	V
INA, MARGIN, WDI Input Voltage	VIH		1.4			v
MR Input Pulse Width	tMR		1			μs
MR Glitch Rejection				100		ns
MR to RESET or OV Delay	tD-MR			200		ns
MR to Internal BP Pullup Current	IMR	$V_{\overline{\text{MR}}} = 1.4 V$	5	10	15	μA
MARGIN to Internal BP Pullup Current	IMARGIN	VMARGIN = 1.4V	5	10	15	μA
WDI Pulldown Current	Iwdi	$V_{WDI} = 0.6V$	5	10	15	μA
WDI Input Pulse Width			50			ns
Matchele er Time e out Davie el	twDI	Initial	92.16	102.4	112.64	
	twp	Normal	1.44	1.6	1.76	S

Note 1: 100% production tested at $T_A = +25^{\circ}C$ and $T_A = +85^{\circ}C$. Specifications at $T_A = -40^{\circ}C$ are guaranteed by design.

Note 2: Device may be supplied from any one of IN1–IN4 or V_{CC}.

Note 3: The internal supply voltage, measured at V_{CC} , equals the maximum of IN1–IN4.

Note 4: Versions Q and R require that power be applied through V_{CC}.

Typical Operating Characteristics

 $(V_{IN1}-V_{IN4} \text{ or } V_{CC} = 5V, WDI = GND, \overline{MARGIN} = \overline{MR} = BP, T_A = +25^{\circ}C, unless otherwise noted.)$





Pin Description

M /X / M

TEMPERATURE (°C)

85

85

PIN		NAME	EUNCTION			
MAX6887	MAX6888	NAME	FUNCTION			
1	1	RESET	Open-Drain, Active-Low Reset Output. RESET asserts when any input voltage falls below its undervoltage threshold or when MR is pulled low. RESET remains low for 200ms after all assertion-causing conditions are cleared. An external pullup resister is required.			
2	2	WDO	Open-Drain, Active-Low Watchdog Timer Output. Logic output for the watchdog timer function. WDO goes low when WDI is not strobed high-to-low or low-to-high within the watchdog timeout period.			
3	3	ŌV	Open-Drain Active-Low Overvoltage Output. $\overline{\text{OV}}$ asserts when any input voltage exceeds its overvoltage threshold. $\overline{\text{OV}}$ remains low for 25µs after all overvoltage conditions are cleared. An external pullup resistor is required.			
4	4	GND	Ground			

SINK CURRENT (mA)

4

IN_ THRESHOLD OVERDRIVE (mV)

Pin Description (continued)

PIN			FUNCTION				
MAX6887	MAX6888	NAME	FUNCTION				
5	5	MR	Manual Reset Input. Pull $\overline{\text{MR}}$ low to assert $\overline{\text{RESET}}$. Connect $\overline{\text{MR}}$ to $\overline{\text{WDO}}$ to generate resets when the watchdog timer expires. Leave $\overline{\text{MR}}$ unconnected or connect to DBP if unused. $\overline{\text{MR}}$ is internally pulled up to BP through a 10µA current source.				
6	6	MARGIN	Margin Input. When $\overline{\text{MARGIN}}$ is pulled low, $\overline{\text{RESET}}$ is held in its existing state independent of subsequent changes in monitored input voltages or the watchdog timer expiration. $\overline{\text{MARGIN}}$ is internally pulled up to BP through a 10µA current source. Leave $\overline{\text{MARGIN}}$ unconnected or connect to BP if unused. $\overline{\text{MARGIN}}$ overrides $\overline{\text{MR}}$ if both are asserted at the same time.				
7	7	WDI	Watchdog Timer Input. Logic input for the watchdog timer function. If WDI is not strobed with a valid low-to-high or high-to-low transition within the selected watchdog timeout period, \overline{WDO} asserts. WDI is internally pulled down to GND through a 10µA current sink.				
8	8	I.C.	Internal Connection. Leave unconnected.				
9	9	V _{CC}	Internal Power-Supply Voltage. Bypass V _{CC} to GND with a 1µF ceramic capacitor as close to the device as possible. V _{CC} supplies power to the internal circuitry. V _{CC} is internally powered from the highest of the monitored IN1–IN4 voltages. Do not use V _{CC} to supply power to external circuitry. To externally supply V _{CC} , see the <i>Powering the MAX6887/MAX6888</i> section.				
10	10	BP	Bypass Voltage. The internally generated voltage at BP supplies power to internal logic and output RESET. Connect a 1μ F capacitor from BP to GND as close to the device as possible. Do not use BP to supply power to external circuitry.				
11	_	IN6	Input Voltage Detector 6. IN6 monitors both undervoltage and overvoltage conditions. See the thresholds options (Tables 1 and 2) for available thresholds. IN6 cannot power the device. For improved noise immunity, bypass IN6 to GND with a 0.1μ F capacitor installed as close to the device as possible.				
12	_	IN5	Input Voltage Detector 5. IN5 monitors both undervoltage and overvoltage conditions. See the thresholds options (Tables 1 and 2) for available thresholds. IN5 cannot power the device. For improved noise immunity, bypass IN5 to GND with a 0.1μ F capacitor installed as close to the device as possible.				
13	13	IN4	Input Voltage Detector 4. IN4 monitors both undervoltage and overvoltage conditions. See the thresholds options (Tables 1 and 2) for available thresholds. Power the device through IN1–IN4 or V _{CC} (see the <i>Powering the MAX6887/MAX6888</i> section). For improved noise immunity, bypass IN4 to GND with a 0.1μ F capacitor installed as close to the device as possible.				
14	14	IN3	Input Voltage Detector 3. IN3 monitors both undervoltage and overvoltage conditions. See the thresholds options (Tables 1 and 2) for available thresholds. Power the device through IN1–IN4 or V _{CC} (see the <i>Powering the MAX6887/MAX6888</i> section). For improved noise immunity, bypass IN3 to GND with a 0.1μ F capacitor installed as close to the device as possible.				
15	15	IN2	Input Voltage Detector 2. IN2 monitors both undervoltage and overvoltage conditions. See the thresholds options (Tables 1 and 2) for available thresholds. Power the device through IN1–IN4 or V _{CC} (see the <i>Powering the MAX6887/MAX6888</i> section). For improved noise immunity, bypass IN2 to GND with a 0.1μ F capacitor installed as close to the device as possible.				
16	16	IN1	Input Voltage Detector 1. IN1 monitors both undervoltage and overvoltage conditions. See the thresholds options (Tables 1 and 2) for available thresholds. Power the device through IN1–IN4 or V _{CC} (see the <i>Powering the MAX6887/MAX6888</i> section). For improved noise immunity, bypass IN1 to GND with a 0.1 μ F capacitor installed as close to the device as possible.				
	11, 12	N.C.	No Connection. Not internally connected.				
_	—	EP	Exposed Paddle. Internally connected to GND. Connect EP to GND or leave unconnected.				



Functional Diagram

MAX6887/MAX6888

Hex/Quad, Power-Supply Supervisory Circuits

Detailed Description

The MAX6887/MAX6888 provide several supply-detector inputs, one watchdog input, and three outputs for powersupply monitoring applications. The MAX6887 offers six voltage-detector inputs, while the MAX6888 offers four. Each voltage-detector input offers both an undervoltage and overvoltage threshold.

The undervoltage and overvoltage thresholds are factory-set for monitoring standard supply voltages (see the *Selector Guide*). Inputs in the *Selector Guide* that contain "Adj" allow an external voltage-divider to be connected to set a user-defined threshold.

RESET goes low when any input voltage drops below its undervoltage threshold or when MR is brought low. RESET stays low for 200ms after all assertion-causing conditions have been cleared. OV goes low when an input voltage rises above its overvoltage threshold. OV typically stays low for 25µs (typ) after all inputs fall back under their overvoltage thresholds.

The MAX6887/MAX6888 offer a watchdog timer with initial and normal timeout periods of 102.4s and 1.6s, respectively. WDO goes low when the watchdog timer expires and deasserts when WDI transitions from low-to-high or high-to-low.

Powering the MAX6887/MAX6888

The MAX6887/MAX6888 derive power from the voltagedetector inputs IN1–IN4 or through an externally supplied V_{CC}. A virtual diode-ORing scheme selects the positive input that supplies power to the device (see the *Functional Diagram*). The highest input voltage on IN1–IN4 supplies power to the device. One of IN1–IN4 must be at least 2.7V to ensure proper operation.

Internal hysteresis ensures that the supply input that initially powered the device continues to power the device when multiple input voltages are within 50mV of each other.

V_{CC} powers the analog circuitry and is the bypass connection for the MAX6887/MAX6888 internal supply. Bypass V_{CC} to GND with a 1µF ceramic capacitor installed as close to the device as possible. The internal supply voltage, measured at V_{CC}, equals the maximum of IN1–IN4. If V_{CC} is externally supplied, V_{CC} must be at least 200mV higher than any voltage applied to IN1–IN4 and V_{CC} must be brought up first. V_{CC} always powers the device when all IN_ are factory set as "Adj." Do not use the internally generated V_{CC} to provide power to external circuitry.

The MAX6887/MAX6888 generate a supply voltage at BP for the internal logic circuitry. Bypass BP to GND with a 1 μ F ceramic capacitor installed as close to the device as possible. The nominal BP output voltage is +2.55V. Do not use BP to provide power to external circuitry.

Inputs

The MAX6887 offers six voltage-detector inputs, while the MAX6888 offers four voltage-detector inputs. Each voltage-detector input offers an undervoltage and overvoltage threshold set at the factory to monitor standard supply voltages (see the *Selector Guide*). The 5% and 10% tolerances are based on maximum and minimum threshold values. Actual thresholds for the MAX6887/MAX6888 are shown in Tables 1 and 2. Inputs in the *Selector Guide* listing "Adj" allow an external voltage-divider to be connected to set a userdefined threshold.

Adjustable Threshold Inputs

Inputs listed in the *Selector Guide* containing "Adj" for inputs allow external resistor voltage-dividers to be connected at the voltage-detector inputs. These inputs monitor any voltage supply higher than 0.6V (see Figure 1). Use the following equation to set a voltage-



Figure 1. Adjusting the Monitored Threshold



detector input (IN1–IN6) to monitor a user-defined supply voltage:

$$0.6V = V_{MON} \times \left(\frac{R2}{R1+R2}\right)$$

where V_{MON} is the desired voltage to be monitored. Use the following procedure to design the proper voltage-divider and calculate thresholds:

- Pick a value for R2. Use the equation above with the desired supply voltage to be monitored and solve for R1. Use high-value resistors R1 and R2 to minimize current consumption due to low leakage currents.
- 2) To find the actual undervoltage and overvoltage thresholds, use the following equations:

$$V_{ACTUALUV} = V_{MON} \times \left(\frac{V_{REFUV}}{0.6V}\right)$$
$$V_{ACTUALOV} = V_{MON} \times \left(\frac{V_{REFOV}}{0.6V}\right)$$

VREFUV and VREFOV are the undervoltage and overvoltage thresholds listed in Tables 1 and 2 that allow adjustable thresholds. Their values are based on tolerances of $\pm 7.5\%$ and $\pm 12.5\%$ from a 0.6V reference. See the *Selector Guide* to find which thresholds in Tables 1 and 2 are adjustable.

Manual Reset (MR)

Many μ P-based products require manual reset capability to allow an operator or external logic circuitry to initiate a reset. The manual reset input (MR) can be connected directly to a switch without an external pullup resistor or debouncing network. MR is internally pulled up to BP. Leave unconnected if not used. MR is internally pulled up to BP through a 10µA current source. MR is designed to reject fast, falling transients (typically 100ns pulses) and MR must be held low for a minimum of 1µs to assert RESET. Connect a 0.1µF capacitor from MR to ground to provide additional noise immunity. After MR transitions from low to high, RESET remains asserted for the duration of its time delay.

Margin Output Disable (MARGIN)

MARGIN allows system-level testing while power supplies exceed the normal operating ranges. Drive MARGIN low to hold RESET, OV, and WDO in their

DADT	UV THRESHOLDS (V)						OV THRESHOLDS (V)					
FADI	IN1	IN2	IN3	IN4	IN5	IN6	IN1	IN2	IN3	IN4	IN5	IN6
MAX6887AETE	4.620	3.060	2.310	1.670	0.557	0.557	5.360	3.540	2.680	1.930	0.643	0.643
MAX6887BETE	4.620	3.060	2.310	0.557	0.557	0.557	5.360	3.540	2.680	0.643	0.643	0.643
MAX6887CETE	4.620	3.060	1.670	0.557	0.557	0.557	5.360	3.540	1.930	0.643	0.643	0.643
MAX6887DETE	3.060	2.310	1.670	1.390	0.557	0.557	3.540	2.680	1.930	1.610	0.643	0.643
MAX6887EETE	3.060	2.310	1.670	0.557	0.557	0.557	3.540	2.680	1.930	0.643	0.643	0.643
MAX6887FETE	3.060	2.310	1.390	0.557	0.557	0.557	3.540	2.680	1.610	0.643	0.643	0.643
MAX6887GETE	3.060	2.310	0.557	0.557	0.557	0.557	3.540	2.680	0.643	0.643	0.643	0.643
MAX6887HETE	3.060	1.670	0.557	0.557	0.557	0.557	3.540	1.930	0.643	0.643	0.643	0.643
MAX6887QETE	0.557	0.557	0.557	0.557	0.557	0.557	0.643	0.643	0.643	0.643	0.643	0.643
MAX6887IETE	4.380	2.880	2.190	1.580	0.527	0.527	5.620	3.700	2.810	2.020	0.673	0.673
MAX6887JETE	4.380	2.880	2.190	0.527	0.557	0.557	5.620	3.700	2.810	0.673	0.673	0.673
MAX6887KETE	4.380	2.880	1.580	0.527	0.557	0.557	5.620	3.700	2.020	0.673	0.673	0.673
MAX6887LETE	2.880	2.190	1.580	1.310	0.557	0.557	3.700	2.810	2.020	1.680	0.673	0.673
MAX6887METE	2.880	2.190	1.580	0.527	0.557	0.557	3.700	2.810	2.020	0.673	0.673	0.673
MAX6887NETE	2.880	2.190	1.310	0.527	0.557	0.557	3.700	2.810	1.680	0.673	0.673	0.673
MAX6887OETE	2.880	2.190	0.527	0.527	0.557	0.557	3.700	2.810	0.673	0.673	0.673	0.673
MAX6887PETE	2.880	1.580	0.527	0.527	0.557	0.557	3.700	2.020	0.673	0.673	0.673	0.673
MAX6887RETE	0.527	0.527	0.527	0.527	0.527	0.527	0.673	0.673	0.673	0.673	0.673	0.673

Table 1. MAX6887 Threshold Options



DADT		UV THRES	HOLDS (V)			OV THRES	HOLDS (V)	
PARI	IN1	IN2	IN3	IN4	IN1	IN2	IN3	IN4
MAX6888AETE	4.620	3.060	2.310	1.670	5.360	3.540	2.680	1.930
MAX6888BETE	4.620	3.060	2.310	0.557	5.360	3.540	2.680	0.643
MAX6888CETE	4.620	3.060	1.670	0.557	5.360	3.540	1.930	0.643
MAX6888DETE	3.060	2.310	1.670	1.390	3.540	2.680	1.930	1.610
MAX6888EETE	3.060	2.310	1.670	0.557	3.540	2.680	1.930	0.643
MAX6888FETE	3.060	2.310	1.390	0.557	3.540	2.680	1.610	0.643
MAX6888GETE	3.060	2.310	0.557	0.557	3.540	2.680	0.643	0.643
MAX6888HETE	3.060	1.670	0.557	0.557	3.540	1.930	0.643	0.643
MAX6888QETE	0.557	0.557	0.557	0.557	0.643	0.643	0.643	0.643
MAX6888IETE	4.380	2.880	2.190	1.580	5.620	3.700	2.810	2.020
MAX6888JETE	4.380	2.880	2.190	0.527	5.620	3.700	2.810	0.673
MAX6888KETE	4.380	2.880	1.580	0.527	5.620	3.700	2.020	0.673
MAX6888LETE	2.880	2.190	1.580	1.310	3.700	2.810	2.020	1.680
MAX6888METE	2.880	2.190	1.580	0.527	3.700	2.810	2.020	0.673
MAX6888NETE	2.880	2.190	1.310	0.527	3.700	2.810	1.680	0.673
MAX6888OETE	2.880	2.190	0.527	0.527	3.700	2.810	0.673	0.673
MAX6888PETE	2.880	1.580	0.527	0.527	3.700	2.020	0.673	0.673
MAX6888RETE	0.527	0.527	0.527	0.527	0.673	0.673	0.673	0.673

Table 2. MAX6888 Threshold Options

existing state while system-level testing occurs. Leave MARGIN unconnected or connect to <u>BP</u> if unused. An internal 10 μ A current source pulls MARGIN to BP. MARGIN overrides MR if both are asserted at the same time. The state of RESET, OV, and WDO does not change while MARGIN = GND.

RESET, \overline{OV} , and \overline{WDO} Outputs

The MAX6887/MAX6888 feature three active-low opendrain outputs: RESET, OV, and WDO. After power-up or overvoltage/undervoltage conditions, RESET and OV remain in their active states until their timeout periods expire and no undervoltage/overvoltage conditions are present (see Figure 2).

OV asserts when any monitored input is above its overvoltage threshold and remains asserted until all inputs are below their thresholds and its respective 25µs timeout period expires. Connect OV to MR to bring RESET low during an overvoltage condition. OV requires a pullup resistor (unless connected to MR). RESET asserts when any monitored input is below its undervoltage threshold or MR is asserted. RESET remains asserted for 200ms after all assertion-causing conditions have been cleared. Configure RESET to assert when the watchdog timer expires by connecting WDO to MR. RESET requires a pullup resistor.

WDO asserts when the watchdog timer expires. See the *Configuring the Watchdog Timer* section for a complete description. WDO requires a pullup resistor.

Configuring the Watchdog Timer

A watchdog timer monitors microprocessor (μ P) software execution for a stalled condition and resets the μ P if it stalls. Connect the watchdog timer output WDO to the reset input or a nonmaskable interrupt of the μ P. The watchdog timer features independent initial and normal watchdog timeout periods of 102.4s and 1.6s, respectively.



Figure 2. Output Timing Diagram



Figure 3. Watchdog, Reset, and Power-Up Timing Diagram

At power-up, WDO goes high after tp-po (see Figure 3). The initial watchdog timeout period (twp) applies immediately after WDO is high. The initial watchdog timeout period allows the µP to perform its initialization process. A normal watchdog timeout period (twp) applies whenever WDI transitions from high to low after the initial watchdog timeout period occurs. WDI monitors the toggling output of the µP, indicating normal processor behavior. If WDI does not toggle during the normal watchdog timeout period (twp), indicating that the processor has stopped operating or is stuck in an infinite execution loop, WDO goes low. WDO stays low until the next transition on WDI. An initial watchdog timeout period (twpi) starts when WDO goes high.

If WDO is connected to MR, the WDO will assert for a short duration (~5µs), long enough to assert the RESET output. Asserting RESET clears the watchdog timer and WDO goes high. The reset output will remain asserted for its timeout period after a watchdog fault. The watchdog timer stays cleared as long as RESET is low.

Applications Information

Layout and Bypassing

For better noise immunity, bypass each of the voltagedetector inputs to GND with 0.1µF capacitors installed as close to the device as possible. Bypass V_{CC} and BP to GND with 1µF capacitors installed as close to the device as possible. V_{CC} (when not externally supplied) and BP are internally generated voltages and should not be used to supply power to external circuitry.

Chip Information

PROCESS: BICMOS

				-	-
PART	Ν	IOMINA VOLTA			
	IN1	IN2	IN3	IN4	(/0)
MAX6888AETE	5.0	3.3	2.5	1.8	5
MAX6888BETE	5.0	3.3	2.5	Adj	5
MAX6888CETE	5.0	3.3	1.8	Adj	5
MAX6888DETE	3.3	2.5	1.8	1.5	5
MAX6888EETE	3.3	2.5	1.8	Adj	5
MAX6888FETE	3.3	2.5	1.5	Adj	5
MAX6888GETE	3.3	2.5	Adj	Adj	5
MAX6888HETE	3.3	1.8	Adj	Adj	5
MAX6888QETE	Adj	Adj	Adj	Adj	5
MAX6888IETE	5.0	3.3	2.5	1.8	10
MAX6888JETE	5.0	3.3	2.5	Adj	10
MAX6888KETE	5.0	3.3	1.8	Adj	10
MAX6888LETE	3.3	2.5	1.8	1.5	10
MAX6888METE	3.3	2.5	1.8	Adj	10
MAX6888NETE	3.3	2.5	1.5	Adj	10
MAX6888OETE	3.3	2.5	Adj	Adj	10
MAX6888PETE	3.3	1.8	Adj	Adj	10
MAX6888RETE	Adj	Adj	Adj	Adj	10

Selector Guide (continued)

*See thresholds options tables (Tables 1 and 2) for actual undervoltage and overvoltage thresholds.

Package Information

For the latest package outline information and land patterns, go to www.maxim-ic.com/packages. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	DOCUMENT NO.
16-TQFN-EP	T1655+2	<u>21-0140</u>

MAX6887/MAX6888



Typical Operating Circuit



Pin Configurations

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	5/05	Initial release	—
1	3/07	Revised Pin Description.	1, 5, 14
2	1/10	Revised Ordering Information to add lead-free information and revised Table 2.	1, 9

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600 _

_____13

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

Maxim Integrated:
MAX6887AETE+ MAX6887AETE+T MAX6887BETE+ MAX6887BETE+T MAX6887CETE+ MAX6887CETE+T
MAX6887DETE+ MAX6887DETE+T MAX6887EETE+ MAX6887EETE+T MAX6887FETE+ MAX6887FETE+T
MAX6887GETE+ MAX6887GETE+T MAX6887HETE+ MAX6887HETE+T MAX6887IETE+ MAX6887IETE+T
MAX6887JETE+ MAX6887JETE+T MAX6887KETE+ MAX6887KETE+T MAX6887LETE+ MAX6887LETE+T
MAX6887METE+ MAX6887METE+T MAX6887NETE+ MAX6887NETE+T MAX6887OETE+ MAX6887OETE+T
MAX6887PETE+ MAX6887PETE+T MAX6887QETE+ MAX6887QETE+T MAX6887RETE+ MAX6887RETE+T
MAX6888AETE+ MAX6888AETE+T MAX6888BETE+ MAX6888BETE+T MAX6888CETE+ MAX6888CETE+T
MAX6888DETE+ MAX6888DETE+T MAX6888EETE+ MAX6888EETE+T MAX6888FETE+ MAX6888FETE+T MAX6888FETE+T
MAX6888GETE+ MAX6888GETE+T MAX6888HETE+ MAX6888HETE+T MAX6888IETE+ MAX6888IETE+T
MAX6888JETE+ MAX6888JETE+T MAX6888KETE+ MAX6888KETE+T MAX6888LETE+ MAX6888LETE+T
MAX6888METE+ MAX6888METE+T MAX6888NETE+ MAX6888NETE+T MAX6888OETE+ MAX6888OETE+T
MAX6888PETE+ MAX6888PETE+T MAX6888QETE+ MAX6888QETE+T MAX6888RETE+ MAX6888RETE+T