General Description

The MAX6800/MAX6801/MAX6802 microprocessor (μ P) supervisory circuits monitor the power supplies in 2.85V to 5.0V μ P and digital systems. They increase circuit reliability and reduce cost by eliminating external components and adjustments.

These devices perform a single function—they assert a reset signal whenever the V_{CC} supply voltage declines below a preset threshold, keeping it asserted for a preset timeout period after V_{CC} has risen above the reset threshold. The only difference among the three devices is their output. The MAX6801 (push/pull) and MAX6802 (open-drain) have an active-low RESET output, while the MAX6800 (push/pull) has an active-high RESET output. The devices are guaranteed to be in the correct state for V_{CC} down to 0.7V. The MAX6802 is guaranteed to be in the correct state for V_{CC} down to 1.0V.

The reset comparator in these ICs is designed to ignore fast transients on V_{CC}. Reset thresholds are factory-trimmable between 2.63V and 4.80V, in approximately 100mV increments. These devices are available with a 1ms (min), 20ms (min), or 100ms (min) reset pulse width. Ideal for space-critical applications, the MAX6800/MAX6801/MAX6802 come packaged in a 3-pin SOT23. For a lower threshold voltage version, see the MAX6332/MAX6334.

Applications

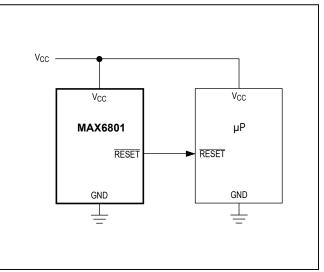
- Computers
- Controllers
- Intelligent Instruments
- Critical µP/µC Power Monitoring
- Portable/Battery-Powered Equipment

Selector Guide (Standard Versions*) appears at end of data sheet.

Benefits and Features

- Ultra-Low 0.7V Operating Supply Voltage
- Low 4.0µA Supply Current
- Precision Monitoring of 2.85V to 5.0V Power-Supply Voltages
- Reset Thresholds Available from 2.63V to 4.80V, in Approximately 100mV Increments
- Fully Specified over Temperature
- Three Power-On Reset Timeout Periods Available (1ms min, 20ms min, 100ms min)
- Low Cost
- Three Available Output Structures: Push/Pull RESET, Push/Pull RESET, Open-Drain RESET
- Guaranteed RESET/RESET Valid to V_{CC} = 0.7V (MAX6800/MAX6801)
- Power-Supply Transient Immunity
- No External Components Required
- 3-Pin SOT23 Package
- Pin-Compatible with MAX809/MAX810, MAX6326/MAX6327/MAX6328, and MAX6346/MAX6347/MAX6348

Typical Operating Circuit





3-Pin, Low-Power µP Reset Circuits

Absolute Maximum Ratings

Terminal Voltage (with respect to GND)

V _{CC}	0.3V to +6V
Push/Pull RESET, RESET	0.3V to (V _{CC} + 0.3V)
Open-Drain RESET	
Input Current (V _{CC})	20mA
Output Current (RESET, RESET)	20mA
Continuous Power Dissipation ($T_A = +70$)°C)
3-Pin SOT23 (derate 4mW/°C above	+70°C)320mW

Operating Temperature Range	40°C to +125°C
Junction Temperature	+150°C
Storage Temperature Range	65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C
Soldering Temperature (reflow)	+300°C
Lead (Pb)-free packages	+260°C
Package containing lead (Pb)	+240°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Electrical Characteristics

(V_{CC} = full range, $T_A = -40^{\circ}$ C to +125°C, unless otherwise noted. Typical values are at V_{CC} = +5.0V and $T_A = +25^{\circ}$ C, reset not asserted.) (Note 1)

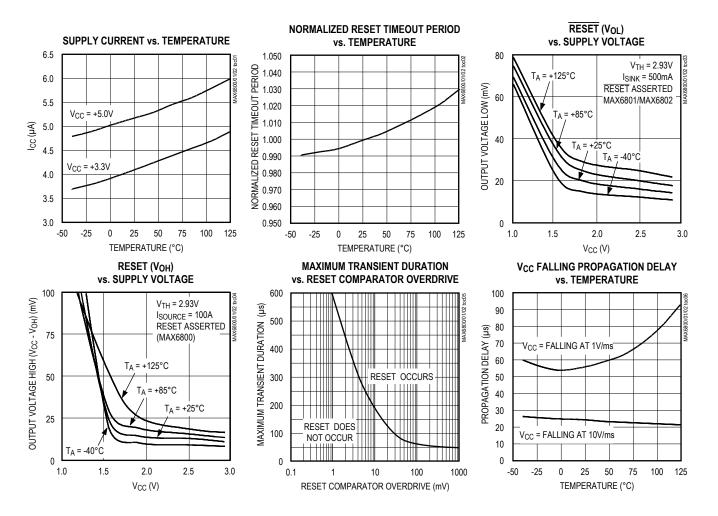
PARAMETER	SYMBOL		COND	TIONS	MIN	TYP	MAX	UNITS
	V _{CC}	$T_A = 0^{\circ}C \text{ to } +125^{\circ}C \qquad \frac{MAX6800/MAX}{MAX6802}$		MAX6800/MAX6801	0.7		5.5	
Supply Voltage Range				MAX6802	1.0		5.5	
(Note 2)		L I ∧ = -40°C to +125°C ⊢		MAX6800/MAX6801	0.78		5.5]
				MAX6802	1.2		5.5	
Supply Current	100	No load $\frac{V_{CC} = +3.0V}{V_{CC} = +5.0V}$			4	10	μA	
Supply Sullent	lcc			V _{CC} = +5.0V		5	12	μΛ
Reset Threshold		MAX680_URDT,		T _A = +25°C	V _{TH} - 1.8%	V_{TH}	V _{TH} + 1.8%	
Reset mieshold	V _{TH}	Table 1		$T_{A} = -40^{\circ}C \text{ to } +125^{\circ}C$	V _{TH} - 3%	V _{TH}	V _{TH} + 3%	
V _{CC} Falling Reset Delay		V _{CC} falling at 10V/ms			30		μs	
	MAX680_UR		RD1-T	D1-T		1.5	2	ms
Reset Active Timeout Period	t _{RP}	MAX680_URD2-T		20	30	40		
		MAX680_URD3-T		100	150	200		
	V _{OL}	Reset asserted	I _{SINK} = 50µA, V _{CC} ≥ 1.0V				0.4	
RESET Output Low-Voltage (MAX6801/MAX6802)			Reset $I_{\text{SUNK}} = 1.2 \text{mA}, V_{\text{CC}} \ge 2.5 \text{V}$				0.3	V
			I _{SINK} = 1.2mA, V _{CC} ≥ 4.25V				0.4	
RESET Output High-Voltage	V	Reset not	ISOURCE	= 500µA, V _{CC} ≥ 3.0V	0.8 x V _{CC}			V
(MAX6801)	V _{OH}	asserted	ISOURCE	= 800µA, V _{CC} ≥ 5.0V	0.8 x V _{CC}			v
			ISOURCE	= 1µA, V _{CC} ≥ 1.0V	0.8 x V _{CC}			
	V _{OH}	Reset	E ISOURCE = 2000A Vec $\geq 1.8V$		0.8 x V _{CC}			
RESET Output Voltage (MAX6800)				= 800µA, V _{CC} ≥ 4.25V	0.8 x V _{CC}			V
	V _{OL}	Reset not I _{SINK} = 1.		2mA, V _{CC} ≥ 3.0V			0.3	
				2mA, V _{CC} ≥ 5.0V			0.4	
RESET Output Leakage Current (MAX6802)		$V_{CC} > V_{TH}$,	RESET no				0.5	μA

Note 1: All parts are production tested at $T_A = +25^{\circ}$ C. Overtemperature limits are guaranteed by design and not production tested. **Note 2:** I_{SOURCE} for the MAX6800 is 100nA. I_{SINK} for the MAX6801 is 100nA. I_{SINK} for the MAX6802 is 50µA.

3-Pin, Low-Power µP Reset Circuits

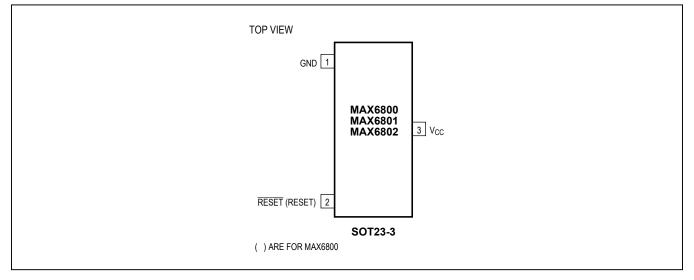
Typical Operating Characteristics

(Reset not asserted, T_A = +25°C, unless otherwise noted.)



3-Pin, Low-Power µP Reset Circuits

Pin Configuration



Pin Description

Р	IN		FUNCTION	
MAX6800	MAX6801/ MAX6802	NAME		
1	1	GND	Ground	
_	2	RESET	Active-Low Reset Output. $\overline{\text{RESET}}$ is asserted while V_{CC} is below the reset threshold and remains asserted for a reset timeout period (t_{RP}) after V_{CC} rises above the reset threshold. $\overline{\text{RESET}}$ on the MAX6801 is push/pull. $\overline{\text{RESET}}$ on the MAX6802 is open-drain.	
2	_	RESET	Active-High Reset Output. RESET is asserted while V _{CC} is below the reset threshold and remains asserted for a reset timeout period (t_{RP}) after V _{CC} rises above the reset threshold. RESET on the MAX6800 is push/pull.	
3	3	V _{CC}	Supply Voltage Input	

3-Pin, Low-Power µP Reset Circuits

Applications Information

Interfacing to μPs with Bidirectional Reset Pins

Since the $\overline{\text{RESET}}$ output on the MAX6802 is opendrain, this device interfaces easily with µPs that have bidirectional reset pins, such as the Motorola 68HC11. Connecting the µP supervisor's $\overline{\text{RESET}}$ output directly to the microcontroller's (µC's) $\overline{\text{RESET}}$ pin with a single pullup resistor allows either device to assert reset (Figure 1).

Negative-Going V_{CC} Transients

In addition to issuing a reset to the μ P during power-up, power-down, and brownout conditions, these devices are relatively immune to short-duration, negativegoing V_{CC} transients (glitches). The <u>Typical Operating</u> <u>Characteristics</u> show the Maximum Transient Duration vs. Reset Comparator Overdrive graph. The graph shows the maximum pulse width that a negative-going V_{CC} transient may typically have without issuing a reset signal. As the amplitude of the transient increases, the maximum allowable pulse width decreases.

Ensuring a Valid Reset Output Down to $V_{CC} = 0V$

When V_{CC} falls below 1V and approaches the minimum operating voltage of 0.7V, push/pull-structured reset sinking (or sourcing) capabilities decrease drastically. High-impedance CMOS-logic inputs connected to the RESET pin can drift to indeterminate voltages. This does not present a problem in most cases, since most µPs and circuitry do not operate when V_{CC} drops below 1V. For the MAX6801 application, where RESET must be valid down to 0V, adding a pulldown resistor between RESET and GND removes stray leakage currents, holding RESET low (Figure 2a). The pulldown resistor value is not critical; 100k Ω is large enough not to load RESET and small enough to pull it low. For the MAX6800 application, where RESET must be valid to V_{CC} = 0V, a 100k Ω pullup resistor between RESET and V_{CC} will hold RESET high when V_{CC} falls below 0.7V (Figure 2b).

Since the MAX6802 has an open-drain, active-low output, it typically uses a pullup resistor. With this device, RESET will most likely not maintain an active condition, but will drift to a non-active level due to the pullup resistor and the reduced sinking capability of the open-drain device. Therefore, this device is not recommended for applications where the RESET pin is required to be valid down to $V_{CC} = 0V$.

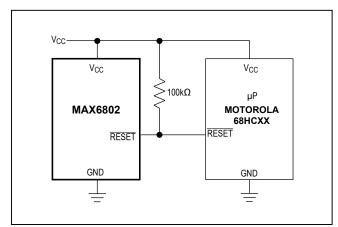


Figure 1. Interfacing to µPs with Bidirectional Reset Pins

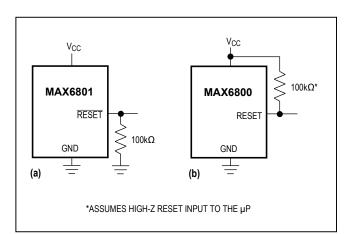


Figure 2. Ensuring Reset Valid Down to $V_{CC} = 0V$

3-Pin, Low-Power µP Reset Circuits

RESET THRESHOLD		T _A = +25°C		T _A = -40°C	to +125°C
SUFFIX	MIN	TYP (V _{TH})	MAX	MIN	MAX
48	4.714	4.80	4.886	4.656	4.944
47	4.615	4.70	4.785	4.559	4.841
46	4.547	4.63	4.713	4.491	4.769
45	4.419	4.50	4.581	4.365	4.635
44	4.301	4.38	4.459	4.249	4.511
43	4.223	4.30	4.377	4.171	4.429
42	4.124	4.20	4.276	4.074	4.326
41	4.026	4.10	4.174	3.977	4.223
40	3.928	4.00	4.072	3.880	4.120
39	3.830	3.90	3.970	3.783	4.017
38	3.732	3.80	3.868	3.686	3.914
37	3.633	3.70	3.767	3.589	3.811
36	3.535	3.60	3.665	3.492	3.708
35	3.437	3.50	3.563	3.395	3.605
34	3.339	3.40	3.461	3.298	3.502
33	3.241	3.30	3.359	3.201	3.399
32	3.142	3.20	3.258	3.104	3.296
31	3.025	3.08	3.135	2.988	3.172
30	2.946	3.00	3.054	2.910	3.090
29	2.877	2.93	2.983	2.842	3.018
28	2.750	2.80	2.850	2.716	2.884
27	2.651	2.70	2.749	2.619	2.781
26	2.583	2.63	2.677	2.551	2.709

Table 1. Factory-Trimmed Reset Thresholds

3-Pin, Low-Power µP Reset Circuits

PART	OUTPUT STAGE	NOMINAL V _{TH} (V)	MIN RESET TIMEOUT (ms)	SOT TOP MARK
MAX6800UR26D3-T	Push/Pull RESET	2.63	100	FZIE
MAX6800UR29D3-T	Push/Pull RESET	2.93	100	FZIF
MAX6800UR31D3-T	Push/Pull RESET	3.08	100	FZIG
MAX6800UR44D3-T	Push/Pull RESET	4.38	100	FZIH
MAX6800UR46D3-T	Push/Pull RESET	4.63	100	FZII
MAX6801UR26D3-T	Push/Pull RESET	2.63	100	FZIK
MAX6801UR29D3-T	Push/Pull RESET	2.93	100	FZIM
MAX6801UR31D3-T	Push/Pull RESET	3.08	100	FZIN
MAX6801UR44D3-T	Push/Pull RESET	4.38	100	FZIO
MAX6801UR46D3-T	Push/Pull RESET	4.63	100	FZIP
MAX6802UR26D3-T	Open-Drain RESET	2.63	100	FZIQ
MAX6802UR29D3-T	Open-Drain RESET	2.93	100	FZIR
MAX6802UR31D3-T	Open-Drain RESET	3.08	100	FZIS
MAX6802UR44D3-T	Open-Drain RESET	4.38	100	FZIT
MAX6802UR46D3-T	Open-Drain RESET	4.63	100	FZIU

Selector Guide (Standard Versions*)

*Sample stock is generally held on all standard versions.

Ordering Information

PART	TEMP RANGE	PIN-PACKAGE
MAX6800URDT	-40°C to +125°C	3 SOT23
MAX6801URDT	-40°C to +125°C	3 SOT23
MAX6802URDT	-40°C to +125°C	3 SOT23

*These devices are available in factory-set V_{CC} reset thresholds from 2.63V to 4.80V, in approximately 0.1V increments. Choose the desired reset threshold suffix from Table 1 and insert it in the blanks following "UR" in the part number. Factory-programmed reset timeout periods are also available. Insert the number corresponding to the desired nominal reset timeout period (1 = 1ms min, 2 = 20ms min, 3 = 100ms min) in the blank following "D" in the part number. There are 15 standard versions with a required order increment of 2500 pieces. Sample stock is generally held on the standard versions only (see Selector Guide). Contact the factory for availability of non-standard versions (required order increment is 10,000 pieces). All devices available in tape-and-reel only.

Devices are available in both leaded and lead-free packaging. Specify lead-free by replacing "-T" with "+T" when ordering.

Chip Information

PROCESS: BICMOS

Package Information

For the latest package outline information and land patterns (footprints), go to <u>www.maximintegrated.com/packages</u>. Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE	PACKAGE	OUTLINE	LAND
TYPE	CODE	NO.	PATTERN NO.
3 SOT23	U3-1	<u>21-0051</u>	<u>90-0179</u>

3-Pin, Low-Power µP Reset Circuits

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
3	10/11	Added automotive-qualified part information to Ordering Information.	1
4	3/12	Updated Factory-Trimmed Reset Thresholds table.	5
5	2/16	Updated Ordering Information table	1

For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim Integrated's website at www.maximintegrated.com.

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Maxim Integrated:

	T MAX6800UR27D2+ ⁻	T MAX6800UR27D3+	T MAX6800UR28D1+T
MAX6800UR28D3+T	MAX6800UR29D1+T	MAX6800UR29D2+T	MAX6800UR29D3+T
MAX6800UR30D2+T	MAX6800UR30D3+T	MAX6800UR31D1+T	MAX6800UR31D2+T
MAX6800UR32D1+T	MAX6800UR32D2+T	MAX6800UR32D3+T	MAX6800UR33D1+T
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