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## MAX22025–MAX22028/ MAX22025F–MAX22028F

## Compact, Isolated, Half-Duplex RS-485/RS-422 Transceivers with AutoDirection Control

### General Description

The MAX22025–MAX22028/MAX22025F–MAX22028F compact isolated RS-485/RS-422 transceivers provide 3.5kV<sub>RMS</sub> of digital galvanic isolation between the cable-side (RS-485/RS-422 driver/ receiver-side) and the UART-side of the device. Isolation improves communication by breaking ground loops and reduces noise when there are large differences in ground potential between ports. These devices allow for robust communication up to 0.5Mbps or 16Mbps.

The MAX22025–MAX22028/MAX22025F–MAX22028F feature Maxim's proprietary AutoDirection control making these devices ideal for applications such as isolated RS-485 ports, where the driver input is used in conjunction with the driver-enable signal to drive the differential bus.

The MAX22025/MAX22027/MAX22025F/MAX22027F feature reduced slew rate drivers that minimize EMI and reduce reflections caused by improper termination of cable allowing error-free transmission up to 0.5Mbps. The MAX22026/MAX22028/MAX22026F/MAX22028F driver outputs are not slew-rate limited, allowing transmit speeds up to 16Mbps.

The receiver output of the MAX22025/MAX22026/MAX22025F/MAX22026F does not follow ( $V_A - V_B$ ) when the device is in the driver-enabled state. The receiver output on the MAX22027/MAX22028/MAX22027F/MAX22028F always follows ( $V_A - V_B$ ).

The driver outputs and receiver inputs are protected from  $\pm 10\text{kV}$  electrostatic discharge (ESD) to GNDB on the cable side, as specified by the Human Body Model (HBM). The MAX22025–MAX22028/MAX22025F–MAX22028F are available in a compact 8-pin wide body SOIC package and operate over the  $-40^\circ\text{C}$  to  $+85^\circ\text{C}$  temperature range.

### Applications

- Utility Meters
- Industrial Automation Equipment
- Programmable Logic Controllers
- HVAC

**Ordering Information** appears at end of data sheet.

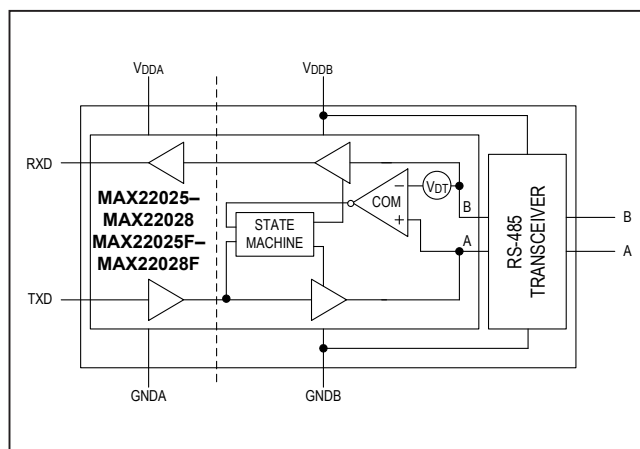
### Benefits and Features

- High-Level Integration Reduces Overall Solution Size
  - Fully Isolated Half-Duplex RS-485/RS-422 Transceivers
  - Compact 8-Pin Wide Body SOIC Package (5.5mm Creepage)
- Integrated Protection Ensures Robust Communication
  - $\pm 10\text{kV}$  ESD (HBM) on Driver Outputs/Receiver Inputs
  - Failsafe Receiver Prevents Fault Transition on Receiver Input Short or Open Events (MAX22025F–MAX22028F)
  - Withstands 3.5kV<sub>RMS</sub> Isolation Voltage for 60 Seconds ( $V_{ISO}$ )
  - Withstands 630V<sub>PEAK</sub> Maximum Repetitive Peak-Isolation Voltage ( $V_{IORM}$ )
  - Continuously Withstands 445V<sub>RMS</sub> Maximum Working-Isolation Voltage ( $V_{IOWM}$ )
- Enables Flexible System Design
  - 0.5Mbps Maximum Data Rate with Slew-Rate Limited Driver (MAX22025/MAX22027/MAX22025F/MAX22027F)
  - 16Mbps Maximum Data Rate (MAX22026/MAX22028/MAX22026F/MAX22028F)
  - AutoDirection Eliminates the Need for DE and  $\overline{\text{RE}}$  Control Signals

### Safety Regulatory Approvals

- UL According to UL1577
- cUL According to CSA Bulletin 5A

### Functional Diagram



## Absolute Maximum Ratings

$V_{DDA}$ , TXD to GNDA .....	-0.3V to +6V
$V_{DDB}$ to GNDB .....	-0.3V to +6V
RXD to GNDA .....	-0.3V to ( $V_{DDA} + 0.3V$ )
A, B to GNDB .....	-8V to +13V
Short Circuit Duration (RXD to GNDA) .....	Continuous
Short Circuit Duration (A, B to GNDB) .....	Continuous

Continuous Power Dissipation ( $T_A = +70^{\circ}\text{C}$ )	8-pin Wide SOIC (derate 23mW/ $^{\circ}\text{C}$ above $+70^{\circ}\text{C}$ ) ... 1847mW
Operating Temperature Range .....	$-40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$
Junction Temperature .....	$+150^{\circ}\text{C}$
Storage Temperature Range .....	$-65^{\circ}\text{C}$ to $+150^{\circ}\text{C}$
Lead Temperature (soldering, 10s) .....	$+300^{\circ}\text{C}$
Soldering Temperature (reflow) .....	$+260^{\circ}\text{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## Package Information

<b>PACKAGE TYPE: 8 Wide SOIC</b>	
Package Code	W8MS+1
Outline Number	<a href="#">21-0262</a>
Land Pattern Number	<a href="#">90-0258</a>
<b>THERMAL RESISTANCE, FOUR-LAYER BOARD</b>	
Junction to Ambient ( $\theta_{JA}$ )	43.3 $^{\circ}\text{C}/\text{W}$
Junction to Case ( $\theta_{JC}$ )	36.5 $^{\circ}\text{C}/\text{W}$

Package thermal resistances were obtained using the method described in JEDEC specification JESD51-7, using a four-layer board. For detailed information on package thermal considerations, refer to [www.maximintegrated.com/thermal-tutorial](http://www.maximintegrated.com/thermal-tutorial).

For the latest package outline information and land patterns (footprints), go to [www.maximintegrated.com/packages](http://www.maximintegrated.com/packages). Note that a "+", "#", or "-" in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

## DC Electrical Characteristics

( $V_{DDA} - V_{GNDA} = 1.71\text{V}$  to  $5.5\text{V}$ ,  $V_{DDB} - V_{GNDB} = 4.75\text{V}$  to  $5.25\text{V}$ ,  $T_A = T_{\text{MIN}}$  to  $T_{\text{MAX}}$ , unless otherwise noted. Typical values are at  $V_{DDA} - V_{GNDA} = 3.3\text{V}$ ,  $V_{DDB} - V_{GNDB} = 5\text{V}$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^{\circ}\text{C}$ .) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
POWER							
Supply Voltage	V <sub>DDA</sub>			1.71		5.5	V
	V <sub>ddb</sub>			4.75		5.25	
Supply Current	I <sub>DDA</sub>	V <sub>DDA</sub> = 3.3V, RXD is unconnected, TXD = low, no bus load			0.39	0.7	mA
	I <sub>ddb</sub>	V <sub>ddb</sub> = 5V, RXD is unconnected, TXD = low, no bus load			4.0	5.5	
V <sub>DDA</sub> Undervoltage Lockout Threshold	V <sub>UVLOA</sub>	V <sub>DDA</sub> rising		1.5	1.6	1.66	V
V <sub>DDA</sub> Undervoltage Lockout Threshold Hysteresis	V <sub>UVHYSTA</sub>				45		mV
LOGIC INTERFACE (TXD, RXD)							
Input High Voltage	V <sub>IH</sub>	TXD to GNDA	2.25V ≤ V <sub>DDA</sub> ≤ 5.5V	0.7 x V <sub>DDA</sub>			V
			1.71V ≤ V <sub>DDA</sub> < 2.25V	0.75 x V <sub>DDA</sub>			

## DC Electrical Characteristics (continued)

( $V_{DDA} - V_{GNDA} = 1.71V$  to  $5.5V$ ,  $V_{DDB} - V_{GNDB} = 4.75V$  to  $5.25V$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted. Typical values are at  $V_{DDA} - V_{GNDA} = 3.3V$ ,  $V_{DDB} - V_{GNDB} = 5V$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^{\circ}C$ .) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS	
Input Low Voltage	V <sub>IL</sub>	TXD to GNDA	2.25V ≤ V <sub>DDA</sub> ≤ 5.5V	0.8			V	
			1.71V ≤ V <sub>DDA</sub> < 2.25V	0.7				
Input Hysteresis	V <sub>HYS</sub>	TXD to GNDA			410		mV	
Input Capacitance	C <sub>IN</sub>	TXD, f = 1MHz			2		pF	
Input Pullup Current	I <sub>PU</sub>	TXD			-10	-5	-1.5	μA
Output Voltage High	V <sub>OH</sub>	RXD to GNDA, I <sub>OUT</sub> = -4mA			V <sub>DDA</sub> -0.4		V	
Output Voltage Low	V <sub>OL</sub>	RXD to GNDA, I <sub>OUT</sub> = 4mA			0.4		V	
DRIVER								
Differential Driver Output	V <sub>OD</sub>	R <sub>L</sub> = 100Ω, TXD = low, Figure 1a			2.0	V <sub>DDB</sub>	V	
		R <sub>L</sub> = 54Ω, TXD = low, Figure 1a			1.5	V <sub>DDB</sub>		
		-7V ≤ V <sub>CM</sub> ≤ +12V, TXD = low, Figure 1b			1.5	5		
Driver Common-Mode Output Voltage	V <sub>OC</sub>	R <sub>L</sub> = 100Ω or 54Ω, TXD = low, Figure 1a			V <sub>DDB</sub> /2		3	V
Driver Disable Threshold	V <sub>DT</sub>	TXD = low to high (Note 3)			0.6	1		V
Driver Short-Circuit Output Current	I <sub>OSD</sub>	GNDB ≤ V <sub>OUT</sub> ≤ +12V, output low			+50	+250		mA
		-7V ≤ V <sub>OUT</sub> ≤ V <sub>DDB</sub> , output high			-250	-50		
Driver Short-Circuit Foldback Output Current	I <sub>SH</sub>	(V <sub>DDB</sub> -1V) ≤ V <sub>OUT</sub> ≤ +12V, output low			+20		mA	
		-7V ≤ V <sub>OUT</sub> ≤ V <sub>DDB</sub> , output high			-20			
RECEIVER								
Input Current (A and B)	I <sub>A</sub> , I <sub>B</sub>	V <sub>DDB</sub> = GNDB or 5V, receive state	V <sub>IN</sub> = +12V	+250			μA	
			V <sub>IN</sub> = -7V	-200				
Receiver Differential Threshold Voltage	V <sub>TH</sub>	-7V ≤ V <sub>CM</sub> ≤ +12V	MAX22025– MAX22028	-200	+200		mV	
			MAX22025F– MAX22028F	-200	-50			
Receiver Input Hysteresis	ΔV <sub>TH</sub>	V <sub>CM</sub> = 0V			25		mV	
Receiver Input Resistance	R <sub>IN</sub>	-7V ≤ V <sub>CM</sub> ≤ +12V, Receive state			60		kΩ	
THERMAL SHUTDOWN								
Thermal Shutdown Threshold	T <sub>SHDN</sub>	Temperature rising			+135		°C	
Thermal Shutdown Hysteresis	T <sub>SHDN</sub> _HYS				20		°C	
PROTECTION								
ESD Protection (A and B Pins to GNDB)		Human Body Model			±10		kV	
ESD Protection (A and B Pins to GNDA) with 47pF Capacitor Connected between GNDA and GNDB		Human Body Model			±7		kV	
ESD Protection (All Other Pins)		Human Body Model			±4		kV	

### Switching Electrical Characteristics (MAX22025/MAX22027/MAX22025F/MAX22027F)

( $V_{DDA} - V_{GNDA} = 1.71\text{V}$  to  $5.5\text{V}$ ,  $V_{DDB} - V_{GNDB} = 4.75\text{V}$  to  $5.25\text{V}$ ,  $T_A = T_{\text{MIN}}$  to  $T_{\text{MAX}}$ , unless otherwise noted. Typical values are at  $V_{DDA} - V_{GNDA} = 3.3\text{V}$ ,  $V_{DDB} - V_{GNDB} = 5\text{V}$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^\circ\text{C}$ .) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Common Mode Transient Immunity	CMTI	(Note 5)		50		kV/ $\mu\text{s}$
<b>DRIVER</b>						
Driver Propagation Delay	$t_{\text{DPLH}}$ , $t_{\text{DPHL}}$	$R_L = 110\Omega$ , $C_L = 50\text{pF}$ , Figure 2 and Figure 3 (Note 4)	200		1000	ns
Driver Differential Output Rise or Fall Time	$t_{\text{LH}}$ , $t_{\text{HL}}$	$R_L = 110\Omega$ , $C_L = 50\text{pF}$ , Figure 2 and Figure 3 (Note 4)	200		900	ns
Maximum Data Rate	$\text{DR}_{\text{MAX}}$		0.5			Mbps
Driver Enable from Power Up	$t_{\text{PORD}}$			100	150	$\mu\text{s}$
<b>RECEIVER</b>						
Receiver Propagation Delay	$t_{\text{RPLH}}$ , $t_{\text{RPHL}}$	$C_L = 15\text{pF}$ , Figure 4 and Figure 5 (Note 4)			80	ns
Receiver Output Skew $ t_{\text{RPLH}} - t_{\text{RPHL}} $	$t_{\text{RSKEW}}$	$C_L = 15\text{pF}$ , Figure 5 (Note 4)			13	ns
Maximum Data Rate	$\text{DR}_{\text{MAX}}$		16			Mbps
Receiver Enable from Power Up	$t_{\text{PORR}}$			100	150	$\mu\text{s}$

### Switching Electrical Characteristics (MAX22026/MAX22028/MAX22026F/MAX22028F)

( $V_{DDA} - V_{GNDA} = 1.71\text{V}$  to  $5.5\text{V}$ ,  $V_{DDB} - V_{GNDB} = 4.75\text{V}$  to  $5.25\text{V}$ ,  $T_A = T_{\text{MIN}}$  to  $T_{\text{MAX}}$ , unless otherwise noted. Typical values are at  $V_{DDA} - V_{GNDA} = 3.3\text{V}$ ,  $V_{DDB} - V_{GNDB} = 5\text{V}$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^\circ\text{C}$ .) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Common Mode Transient Immunity	CMTI	(Note 5)		50		kV/ $\mu\text{s}$
<b>DRIVER</b>						
Driver Propagation Delay	$t_{\text{DPLH}}$ , $t_{\text{DPHL}}$	$R_L = 110\Omega$ , $C_L = 50\text{pF}$ , Figure 2 and Figure 3 (Note 4)			50	ns
Driver Differential Output Rise or Fall Time	$t_{\text{LH}}$ , $t_{\text{HL}}$	$R_L = 110\Omega$ , $C_L = 50\text{pF}$ , Figure 2 and Figure 3 (Note 4)			15	ns
Maximum Data Rate	$\text{DR}_{\text{MAX}}$		16			Mbps
Driver Enable from Power Up	$t_{\text{PORD}}$			100	150	$\mu\text{s}$
<b>RECEIVER</b>						
Receiver Propagation Delay	$t_{\text{RPLH}}$ , $t_{\text{RPHL}}$	$C_L = 15\text{pF}$ , Figure 4 and Figure 5 (Note 4)			80	ns
Receiver Output Skew $ t_{\text{RPLH}} - t_{\text{RPHL}} $	$t_{\text{RSKEW}}$	$C_L = 15\text{pF}$ , Figure 5 (Note 4)			13	ns
Maximum Data Rate	$\text{DR}_{\text{MAX}}$		16			Mbps
Receiver Enable from Power Up	$t_{\text{PORR}}$			100	150	$\mu\text{s}$

**Note 1:** All devices are 100% production tested at  $T_A = +85^\circ\text{C}$ . Specifications over temperature are guaranteed by design.

**Note 2:** All currents into the device are positive. All currents out of the device are negative. All voltages are referenced to their respective ground (GNDA or GNDB), unless otherwise noted.

**Note 3:** This is the differential voltage from A to B that the driving device must see on the bus to disable its driver.

**Note 4:** Not production tested. Guaranteed by design.

**Note 5:** CMTI is the maximum sustainable common-mode voltage slew rate while maintaining the correct output states. CMTI applies to both rising and falling common-mode voltage edges. Tested with the transient generator connected between GNDA and GNDB.  $V_{\text{CM}} = 1\text{kV}$

## Insulation Characteristics

PARAMETER	SYMBOL	CONDITIONS	VALUE	UNITS
Maximum Repetitive Peak Withstand Voltage	$V_{IORM}$	(Note 6)	630	$V_P$
Maximum Working Isolation Voltage	$V_{IOWM}$	GNDA to GNDB continuous (Note 6)	445	$V_{RMS}$
Maximum Transient Isolation Voltage	$V_{IOTM}$		5000	$V_P$
Maximum Withstand Isolation Voltage	$V_{ISO}$	GNDA to GNDB for 60s (Note 7)	3500	$V_{RMS}$
Maximum Surge Isolation Voltage	$V_{IOSM}$	Basic Insulation, 1.2/50 $\mu$ s pulse per IEC61000-4-5	10	kV
Insulation Resistance	$R_S$	$T_A = +150^{\circ}\text{C}$ , $V_{IO} = 500\text{V}$	$>10^9$	$\Omega$
Barrier Capacitance Side A to Side B	CIO	GNDA to GNDB	2	pF
Minimum Creepage Distance	CPG		5.5	mm
Minimum Clearance Distance	CLR		5.5	mm
Internal Clearance		Distance through insulation	0.015	mm
Comparative Tracking Resistance Index	CTI		$>400$	
Climatic Category			40/125/21	
Pollution Degree			2	

**Note 6:**  $V_{IORM}$ ,  $V_{IOWM}$ , and  $V_{ISO}$  are defined by the IEC 60747-5-5 standard.

**Note 7:** Product is qualified at  $V_{ISO}$  for 60 seconds. Not production tested.

## Safety Regulatory Approvals

<b>UL</b>
The MAX22025–MAX22028 and MAX22025F–MAX22028F are certified under UL1577. For more details, refer to File E351759.
Rated up to 3500 $V_{RMS}$ for single protection.
<b>cUL (Equivalent to CSA notice 5A)</b>
The MAX22025–MAX22028 and MAX22025F–MAX22028F are certified up to 3500 $V_{RMS}$ for single protection. For more details, refer to File E351759.

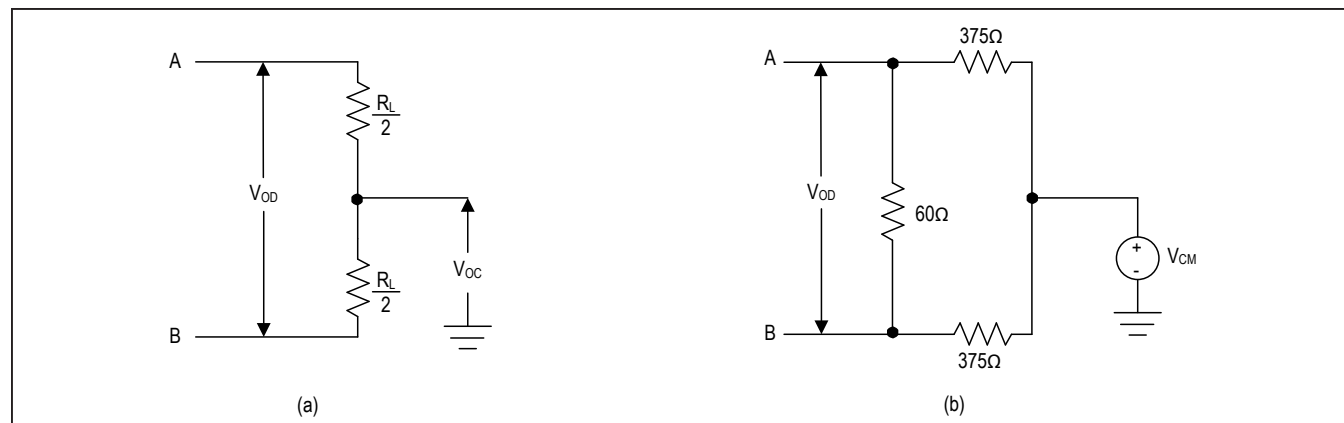


Figure 1. Driver DC Test Load

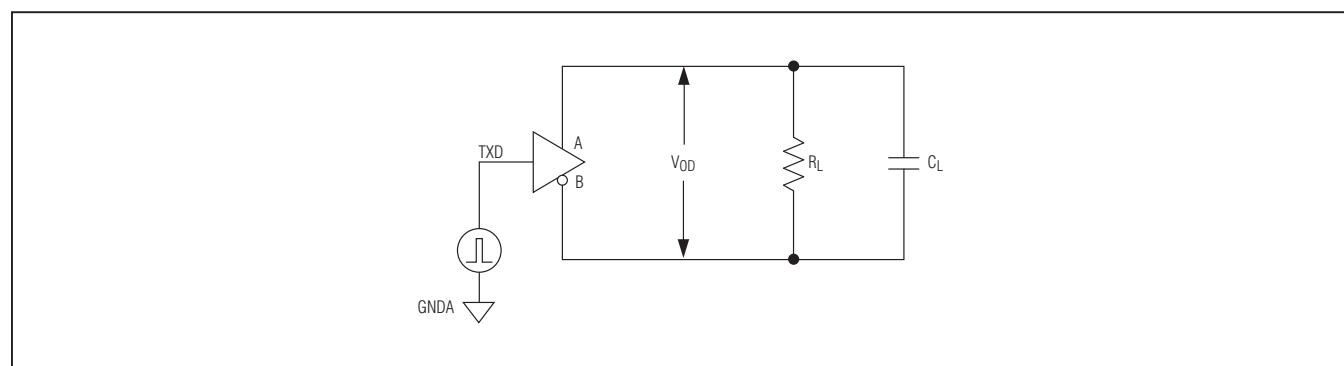


Figure 2. Driver Timing Test Circuit

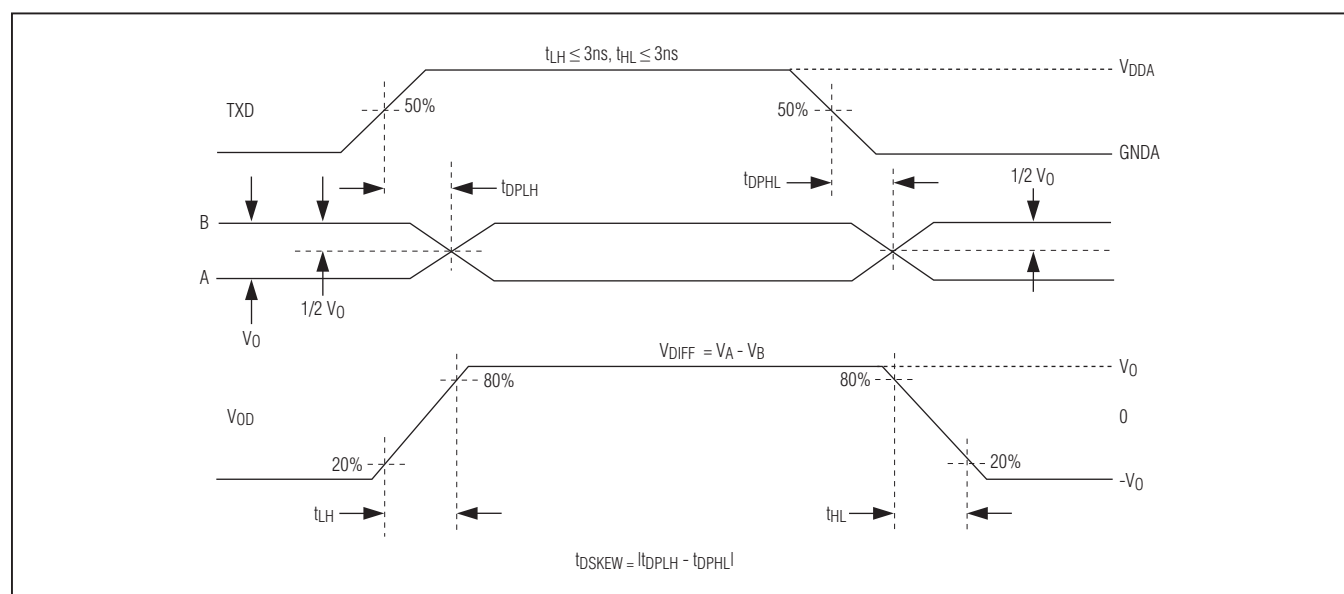


Figure 3. Driver Propagation Delays

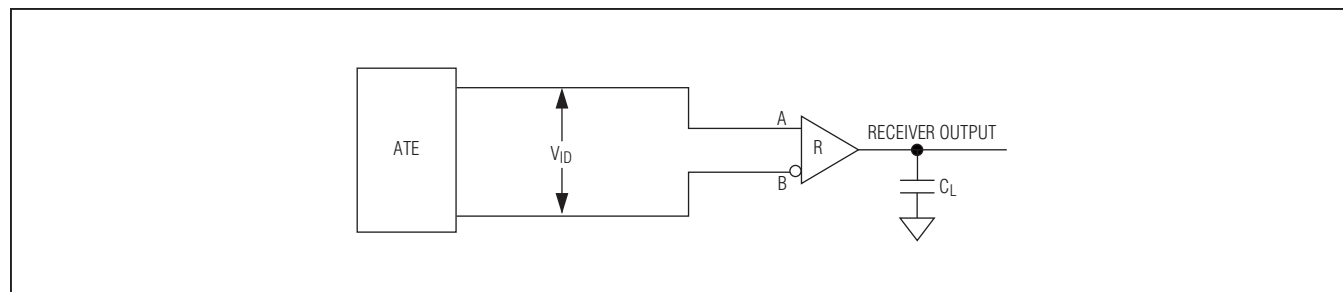


Figure 4. Receiver Propagation Delay Test Circuit

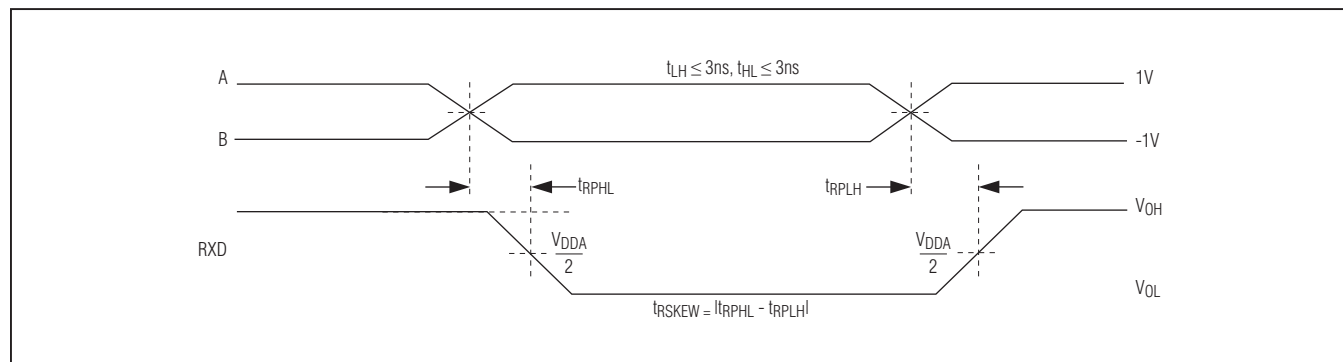
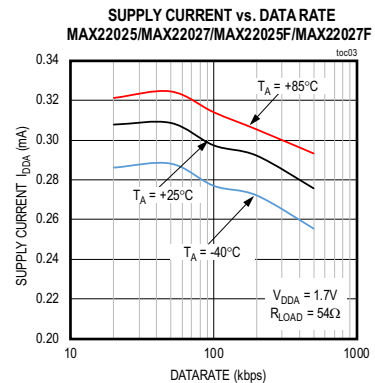
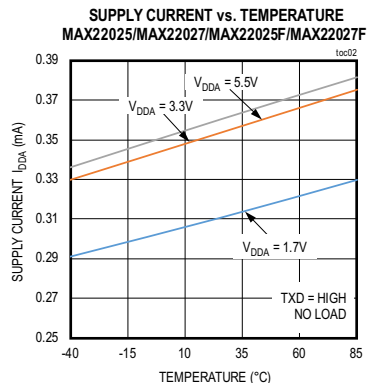
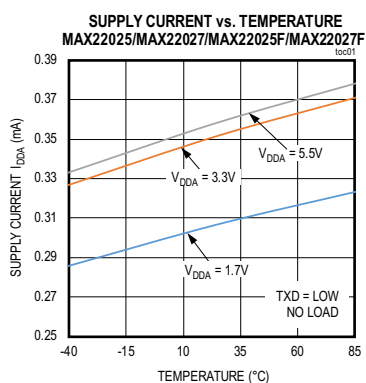


Figure 5. Receiver Propagation Delays

## Typical Operating Characteristics

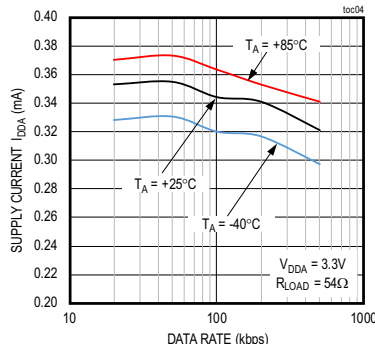
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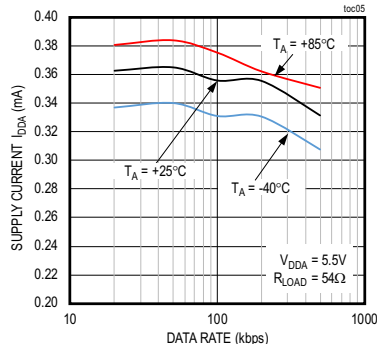
## Typical Operating Characteristics (continued)

( $V_{DDA} - V_{GNDA} = 3.3V$ ,  $V_{DDB} - V_{GNDB} = 5V$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^\circ C$ , unless otherwise noted.)

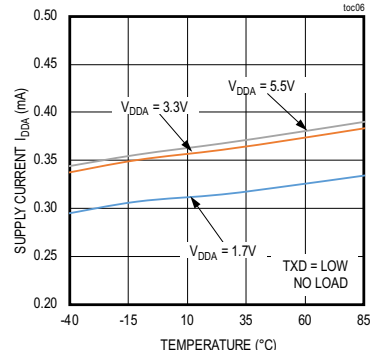
SUPPLY CURRENT vs. DATA RATE  
MAX22025/MAX22027/MAX22025F/MAX22027F



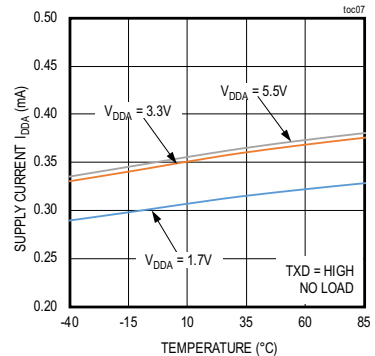
SUPPLY CURRENT vs. DATA RATE  
MAX22025/MAX22027/MAX22025F/MAX22027F



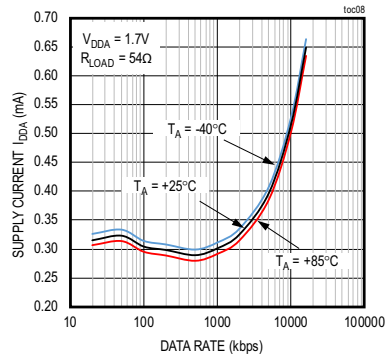
SUPPLY CURRENT vs. TEMPERATURE  
MAX22026/MAX22028/MAX22026F/MAX22028F



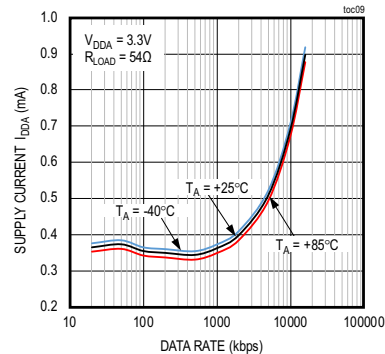
SUPPLY CURRENT vs. TEMPERATURE  
MAX22026/MAX22028/MAX22026F/MAX22028F



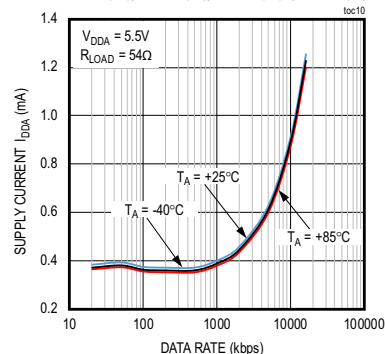
SUPPLY CURRENT vs. DATA RATE  
MAX22026/MAX22028/MAX22026F/MAX22028F



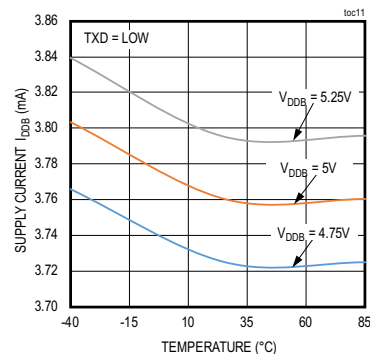
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MAX22026/MAX22028/MAX22026F/MAX22028F



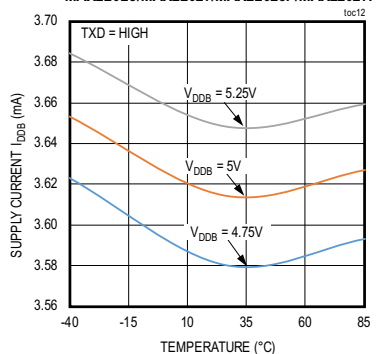
SUPPLY CURRENT vs. DATA RATE  
MAX22026/MAX22028/MAX22026F/MAX22028F



SUPPLY CURRENT vs. TEMPERATURE  
MAX22025/MAX22027/MAX22025F/MAX22027F



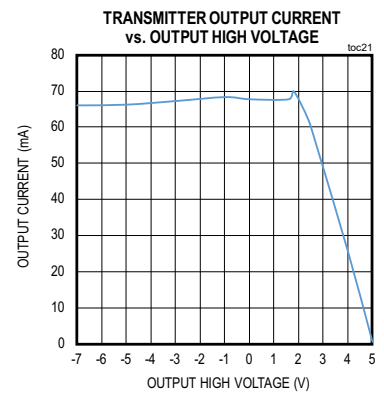
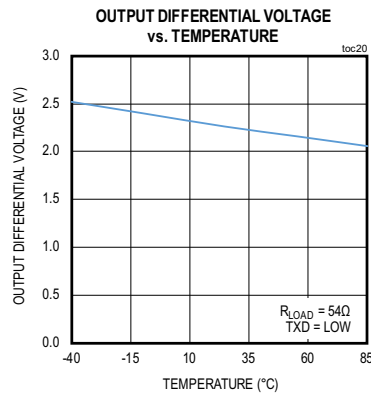
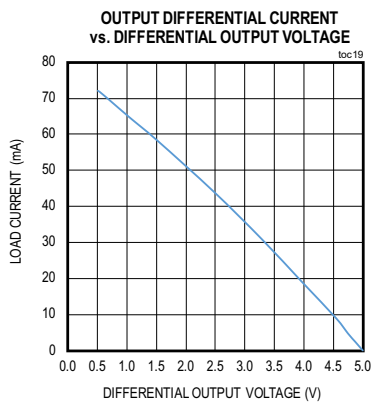
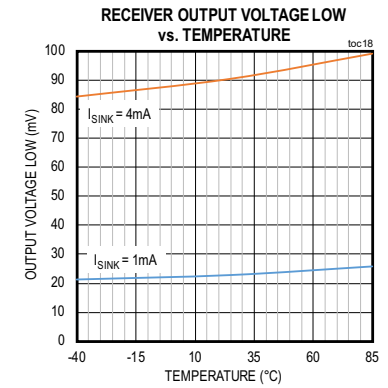
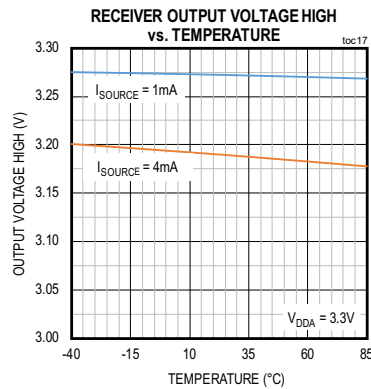
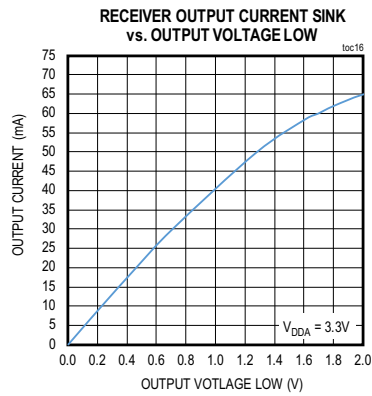
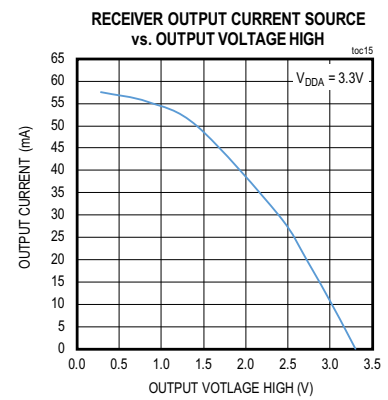
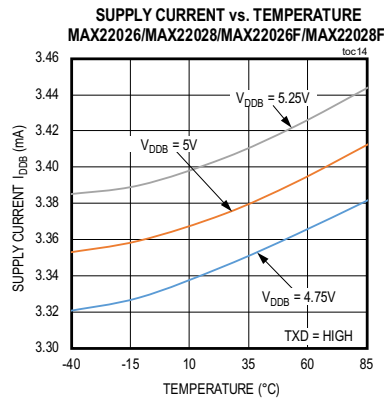
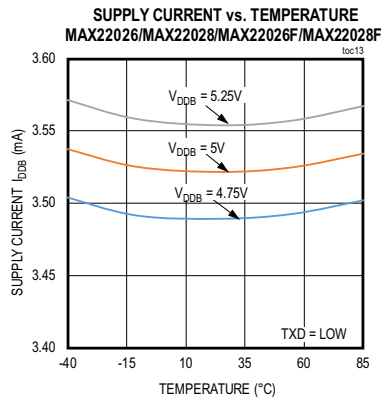
SUPPLY CURRENT vs. TEMPERATURE  
MAX22025/MAX22027/MAX22025F/MAX22027F





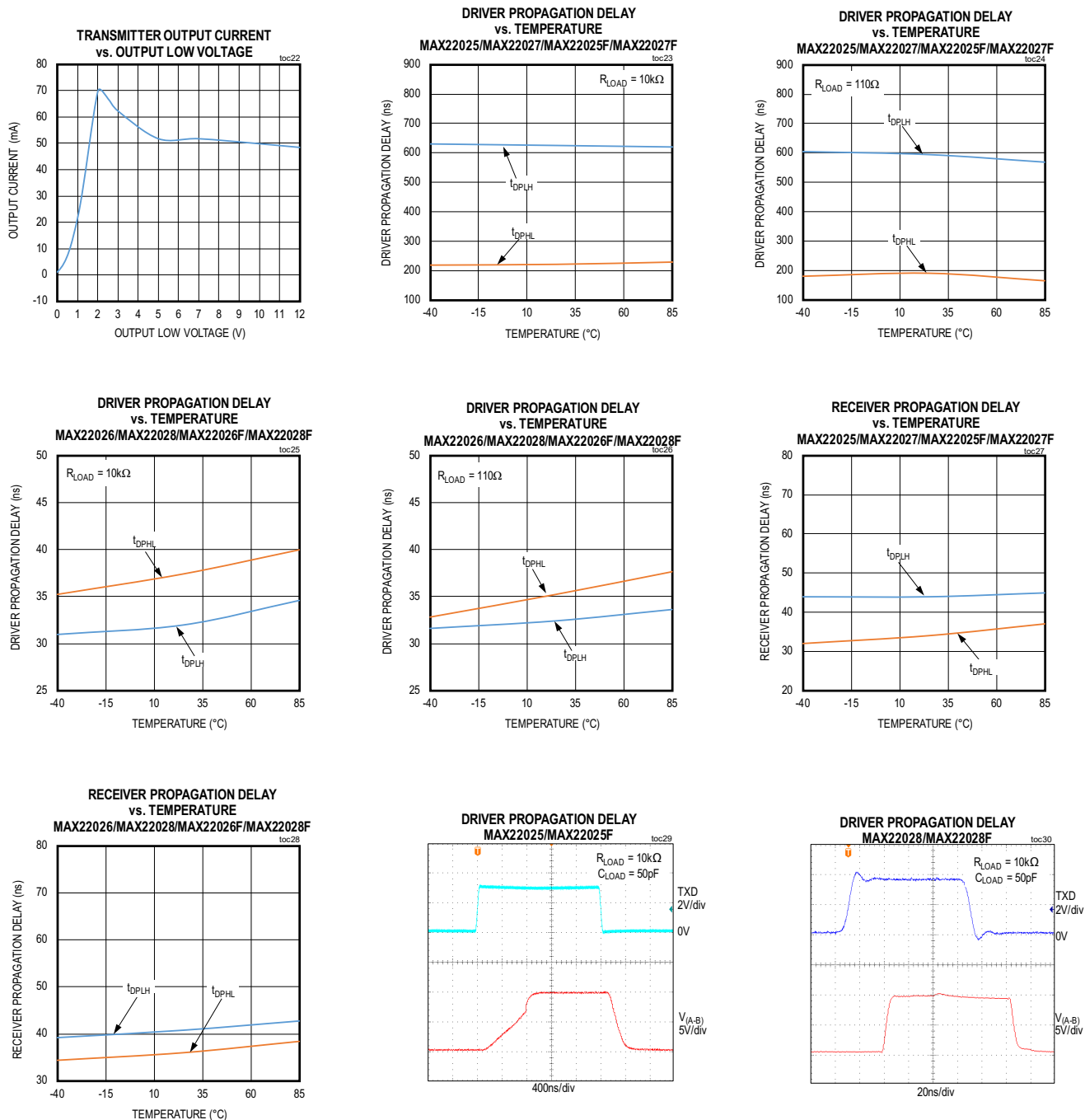
## Typical Operating Characteristics (continued)

( $V_{DDA} - V_{GNDA} = 3.3V$ ,  $V_{DDB} - V_{GNDB} = 5V$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^\circ C$ , unless otherwise noted.)



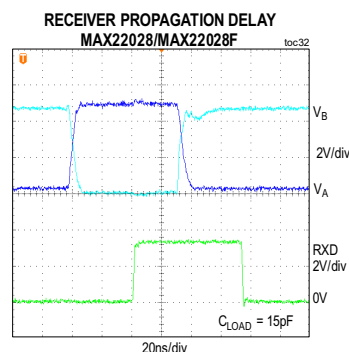
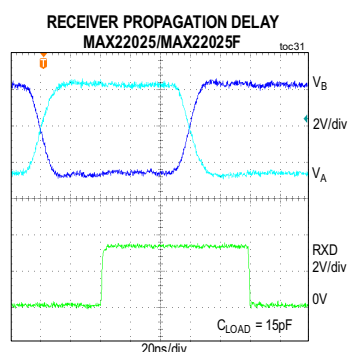
## Typical Operating Characteristics (continued)

( $V_{DDA} - V_{GNDA} = 3.3V$ ,  $V_{DDB} - V_{GNDB} = 5V$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^\circ C$ , unless otherwise noted.)

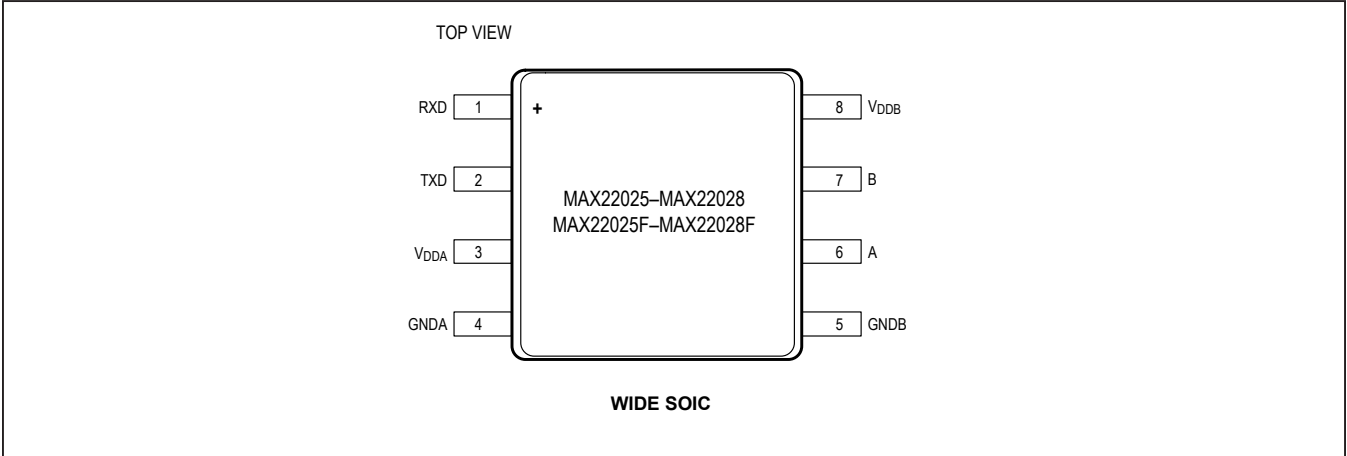


# Typical Operating Characteristics (continued)

( $V_{DDA} - V_{GNDA} = 3.3V$ ,  $V_{DDB} - V_{GNDB} = 5V$ ,  $V_{GNDA} = V_{GNDB}$ , and  $T_A = +25^{\circ}C$ , unless otherwise noted.)



# Pin Configuration



# Pin Description

PIN	NAME	REFERENCE	FUNCTION
1	RXD	GNDA	Receiver Data Output. See the <a href="#">Function Tables</a> for more information.
2	TXD	GNDA	Driver Input. TXD is the input to the internal state machine that automatically enables and disables the driver. See the <a href="#">Function Tables</a> and <a href="#">AutoDirection Circuitry</a> sections for more information. TXD has an internal 5 $\mu$ A pullup to $V_{DDA}$ .
3	$V_{DDA}$	GNDA	UART/Logic-Side Power Input. Bypass $V_{DDA}$ to GNDA with both 0.1 $\mu$ F and 1 $\mu$ F capacitors as close to the device as possible.
4	GNDA	-	UART/Logic-Side Ground. GNDA is the ground reference for digital signals.
5	GNDB	-	Cable Side Ground. GNDB is the ground reference for the RS-485/RS-422 bus signals.
6	A	GNDB	Noninverting Driver Output/Receiver Input
7	B	GNDB	Inverting Driver Output/Receiver Input
8	$V_{DDB}$	GNDB	Cable Side Power Input. Bypass $V_{DDB}$ to GNDB with both 0.1 $\mu$ F and 1 $\mu$ F capacitors as close to the device as possible.

## Function Tables

TRANSMIT FUNCTIONALITY					
TXD	$(V_A - V_B)$	PREVIOUS STATE	CURRENT STATE	A	B
0	X	X	Driver Enabled	0	1
1	$(V_A - V_B) \geq V_{DT}$	X	Receiver Enabled	High-Z	High-Z
1	$(V_A - V_B) < V_{DT}$	Driver Enabled	Driver Enabled	1	0
		Receiver Enabled	Receiver Enabled	High-Z	High-Z

X = Don't care

RXD FUNCTIONALITY (STANDARD RECEIVER)			
CURRENT STATE	$(V_A - V_B)$	RXD	
		MAX22025/MAX22026	MAX22027/MAX22028
Receiver Enabled	$(V_A - V_B) \geq +200\text{mV}$	1	1
	$(V_A - V_B) \leq -200\text{mV}$	0	0
Driver Enabled	$(V_A - V_B) \geq +200\text{mV}$	1	1
	$(V_A - V_B) \leq -200\text{mV}$	1	0

RXD FUNCTIONALITY (FAILSAFE RECEIVER)			
CURRENT STATE	$(V_A - V_B)$	RXD	
		MAX22025F/MAX22026F	MAX22027F/MAX22028F
Receiver Enabled	$(V_A - V_B) \geq -50\text{mV}$	1	1
	$(V_A - V_B) \leq -200\text{mV}$	0	0
Driver Enabled	$(V_A - V_B) \geq -50\text{mV}$	1	1
	$(V_A - V_B) \leq -200\text{mV}$	1	0

## Detailed Description

The MAX22025–MAX22028/MAX22025F–MAX22028F isolated RS-485/RS-422 transceivers provide 3500V<sub>RMS</sub> (60s) of galvanic isolation between the RS-485/RS-422 cable side of the transceiver and the UART side. These devices allow up to 0.5Mbps (MAX22025/MAX22027/MAX22025F/MAX22027F) or 16Mbps (MAX22026/MAX22028/MAX22026F/MAX22028F) communication across an isolation barrier when a large potential exists between grounds on each side of the barrier.

### Isolation

Data isolation is achieved using high-voltage capacitors that allow data transmission between the UART side and the RS-485/RS-422 cable side of the transceiver.

### AutoDirection Circuitry

Internal circuitry in the MAX22025–MAX22028/MAX22025F–MAX22028F, in conjunction with an external pullup resistor on A and pulldown resistor on B (see Typical Application Circuit), acts to automatically disable or enable the driver and the receiver to keep the bus in

the correct state. This AutoDirection circuitry consists of a state machine and an additional receive comparator that determine whether this device is trying to drive the bus or another node on the network is driving the bus.

The internal state machine has two inputs:

- TXD
- The current state of  $(V_A - V_B)$ , which is determined by a dedicated differential comparator.

The state machine also has two outputs:

- DRIVER\_ENABLE—Internal signal that enables and disables the driver
- RECEIVER\_ENABLE—Internal signal that is the inverse of the DRIVER\_ENABLE signal.

When TXD is low, the device always drives the bus low. When TXD switches high, the device drives the bus for a short time, then disables the driver and allows the external pullup/pulldown resistors to hold the bus in the high state. During each low-to-high transition of TXD, the driver stays enabled until  $(V_A - V_B) \geq V_{DT}$ . The driver is then disabled and the pullup/pulldown resistors hold the A and B lines in the correct state.

### Pullup and Pulldown Resistors

The pullup and pulldown resistors on the A and B lines are required for proper operation of the device although their exact value is not critical. They function to hold the bus in the high state following a low-to-high transition. Sizing of these resistors is determined in the same way as when using any other RS-485 driver and depends on how the line is terminated and how many nodes are on the bus. The most important factor when sizing these resistors is to guarantee that the idle voltage on the bus ( $V_A - V_B$ ) is greater than the receiver input threshold (+200mV for the MAX22025–MAX22028, -50mV for the MAX22025F–MAX22028F) in order to remain compatible with standard RS-485 receiver thresholds.

### Receive State

When not transmitting data, the MAX22025–MAX22028/MAX22025F–MAX22028F require the TXD input be high to remain in the receive state. A conventional RS-485 transceiver has DE and RE inputs that are used to enable and disable the driver and receiver. However, the MAX22025–MAX22028/MAX22025F–MAX22028F do not have a DE input, and instead use an internal state machine to enable and disable the drivers.

### Failsafe Receiver (MAX22025F–MAX22028F only)

The MAX22025F–MAX22028F guarantee a logic high on the receiver output when the receiver inputs are shorted or open, or when they are connected to a terminated transmission line with all drivers disabled. If the differential receiver input voltage ( $V_A - V_B$ ) is greater than or equal to -50mV, RXD is a logic-high.

### Receiver Output (RXD)

The receiver output (RXD) of the MAX22025–MAX22028/MAX22025F–MAX22028F does not follow TXD when the device is in the driver-enabled state. This allows for line interference detection by verifying that RXD remains high throughout data transmission.

On the MAX22027/MAX22028/MAX22027F/MAX22028F, the receiver output (RXD) always follows ( $V_A - V_B$ ).

### ESD Protection

ESD protection structures are incorporated on all pins to protect against electrostatic discharge encountered during handling and assembly. The driver outputs and receiver inputs of the devices have extra protection against static electricity to both the UART side and cable side ground references. The ESD structures withstand high-ESD events during normal operation and when powered down. After an ESD event, the devices keep working without latch-up or damage.

Bypass  $V_{DDA}$  to GND<sub>A</sub> and bypass  $V_{ddb}$  to GND<sub>B</sub> with 0.1μF and 1μF capacitors to ensure maximum ESD protection.

### ESD Test Conditions

ESD performance depends on a variety of conditions. Contact Maxim for a reliability report that documents test setup, test methodology, and test results.

### Human Body Model (HBM)

Figure 6 shows the HBM test model, while Figure 7 shows the current waveform it generates when discharged in a low-impedance state. This model consists of a 100pF capacitor charged to the ESD voltage of interest, which is then discharged into the test device through a 1.5kΩ resistor.

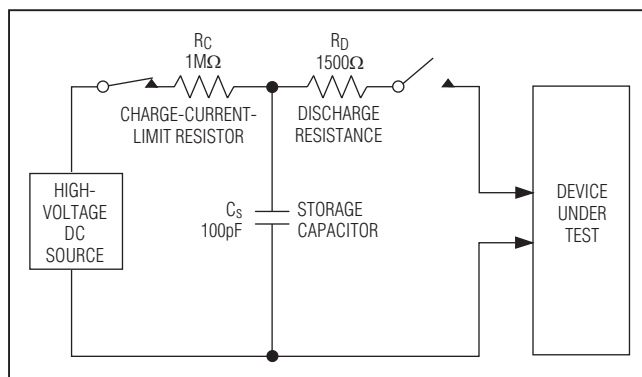


Figure 6. Human Body ESD Test Model

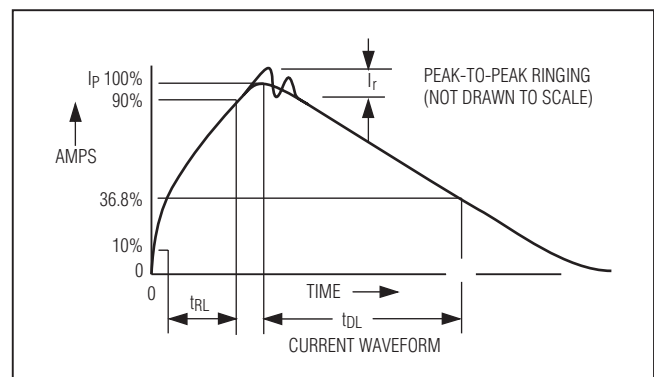
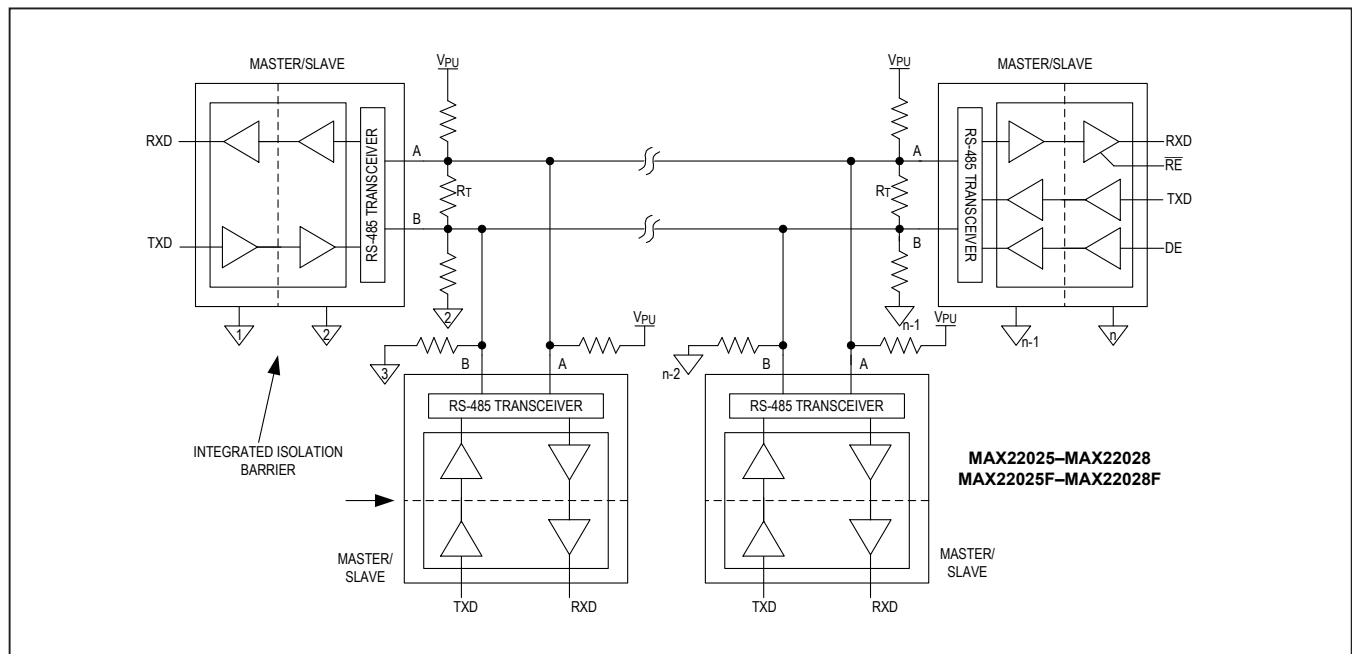
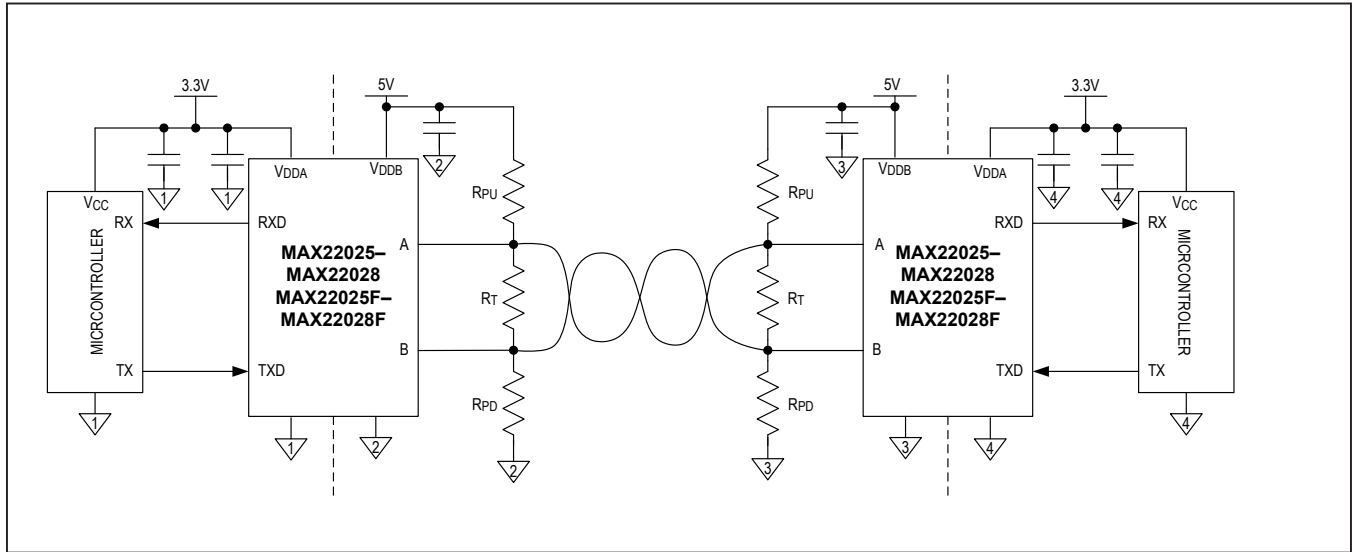


Figure 7. Human Body Current Waveform

## Typical Application Circuits



MAX22025–MAX22028/  
MAX22025F–MAX22028F

Compact, Isolated, Half-Duplex RS-485/RS-422  
Transceivers with AutoDirection Control

## Ordering Information

PART	TEMP RANGE	RXD IN DRIVER-ENABLED STATE	DRIVER SPEED (Mbps)	PIN-PACKAGE
<b>MAX22025AWA+</b>	-40°C to +85°C	HIGH	0.5	8 Wide SOIC
MAX22025AWA+T	-40°C to +85°C	HIGH	0.5	8 Wide SOIC
<b>MAX22026AWA+*</b>	-40°C to +85°C	HIGH	16	8 Wide SOIC
MAX22026AWA+T*	-40°C to +85°C	HIGH	16	8 Wide SOIC
<b>MAX22027AWA+*</b>	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	0.5	8 Wide SOIC
MAX22027AWA+T*	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	0.5	8 Wide SOIC
<b>MAX22028AWA+</b>	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	16	8 Wide SOIC
MAX22028AWA+T	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	16	8 Wide SOIC
<b>MAX22025FAWA+</b>	-40°C to +85°C	HIGH	0.5	8 Wide SOIC
MAX22025FAWA+T	-40°C to +85°C	HIGH	0.5	8 Wide SOIC
<b>MAX22026FAWA+*</b>	-40°C to +85°C	HIGH	16	8 Wide SOIC
MAX22026FAWA+T*	-40°C to +85°C	HIGH	16	8 Wide SOIC
<b>MAX22027FAWA+*</b>	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	0.5	8 Wide SOIC
MAX22027FAWA+T*	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	0.5	8 Wide SOIC
<b>MAX22028FAWA+*</b>	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	16	8 Wide SOIC
MAX22028FAWA+T*	-40°C to +85°C	FOLLOWS (V <sub>A</sub> -V <sub>B</sub> )	16	8 Wide SOIC

+Denotes a lead(Pb)-free/RoHS-compliant package.

T = Tape and reel.

\*Future product—contact factory for availability.

## Chip Information

PROCESS: BiCMOS

## Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	8/19	Initial release	—
1	9/19	Updated the <i>Electrical Characteristics</i> section and added future product designation to MAX22025AWA+ and MAX22025AWA+T in the <i>Ordering Information</i> section	3, 15
2	11/19	Removed future product designation from MAX22025AWA+ and MAX22025AWA+T in the <i>Ordering Information</i> section	15
3	2/20	Updated the title, <i>General Description, Benefits and Features, Functional Diagram, DC Electrical Characteristics, Switching Electrical Characteristics, Pin Configuration, Functional Tables, Detailed Description, AutoDirection Circuitry, Pullup and Pulldown Resistors, Receive State, Receiver Output (RXD)</i> , and <i>Typical Application Circuits</i> sections; updated TOC01–TOC14, TOC23–TOC32; added the <i>Safety Regulatory Approvals</i> table and <i>Failsafe Receiver (MAX22025F–MAX22028F)</i> section; added MAX22025FAWA+, MAX22025FAWA+T, MAX22026FAWA+, MAX22026FAWA+T, MAX22027FAWA+, MAX22027FAWA+T, MAX22028FAWA+, and MAX22028FAWA+T as future parts	1–16
3.1		Corrected typo	1
4	4/20	Removed future product designation from MAX22025FAWA+ and MAX22025FAWA+T in the <i>Ordering Information</i> table	15

For pricing, delivery, and ordering information, please visit Maxim Integrated's online storefront at <https://www.maximintegrated.com/en/storefront/storefront.html>.

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