Sampling Phase Detectors 10 MHz - 20 GHz

Features

- Surface Mount Package: 3.3 mm (L) x 2.8 mm (W) x 1.5 mm (H)
- RoHS* Compliant

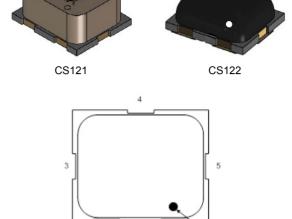
Applications

- Phase Lock Loops
- High Frequency Sampling

Description

The products of the MSPD101x-x series are fullycontained sampling phase detectors, each comprising a beam lead silicon step recovery diode, beam lead DC blocking capacitors and a beam lead series-tee pair of low-barrier silicon Schottky diodes mounted on a ceramic substrate. The semiconductors and chip capacitors are protected with an epoxy encapsulation on the top side of the ceramic substrate. These products are manufactured using a proven diode fabrication and assembly processes which optimize diode characteristics for optimal electrical performance and excellent reliability.

These low profile, compact surface mount components offer RF and microwave signal performance superior to comparable chip-and-wire discrete devices in leaded packages. These rugged devices are capable of reliable operation in all military, commercial and industrial applications.



TOP VIEW

Pin Configuration

Pin #	Description				
1	Cathode terminal of step recovery diode				
2	Anode terminal of step recovery diode				
3	Cathode connection of Schottky diode series tee				
4	Center node of Schottky diode series tee				
5	Anode connection of Schottky diode series tee				

Ordering Information

Part #	Description	Packaging		
MSPD1011-xxx ¹ -T	Low Barrier			
MSPD1012-xxx ¹ -T	Medium Barrier	Tube		
MSPD1013-xxx ¹ -T	High Barrier	_		
MSPD1011-xxx ¹ -R	Low Barrier			
MSPD1012-xxx ¹ -R	Medium Barrier	250 or 500 piece reel		
MSPD1013-xxx ¹ -R	High Barrier	_		
MSPD1011-xxx ¹ -W	Low Barrier			
MSPD1012-xxx ¹ -W	Medium Barrier	Waffle Pack		
MSPD1013-xxx ¹ -W	High Barrier			

1. Insert 121 for ceramic or 122 for epoxy.

* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

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PIN 1 INDICATOR

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Electrical Specifications: T_A = +25°C

Demonster	O an diti ana	MSPD1011		MSPD1012			MSPD1013				
Parameter	Conditions	Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Microwave Signal Level	_		-3 to 0			0 to 3			0 to 13		dBm
Schottky Diode	Schottky Diode										
Barrier Height			Low			Medium	l		High		
Forward Voltage	I _F = 1 mA	270		350	370	_	550	600	_	700	mV
Junction Capacitance	V _R = 0 V, 1 MHz			0.1	_	_	0.1	_	_	0.1	pF
Total Resistance	I _F = 5 mA	_	_	24	_	_	24		_	24	Ω
Step Recovery Diode			1						1		
Breakdown Voltage	I _R = 10 μA		20	30	_	20	30		20	30	V
Carrier Lifetime	I _F = 10 mA, I _R = 6 mA		10		_	10	_		10		ns
Transition Time	I _F = 10 mA, V _R = 10V	_	70		_	70	_		70	_	ps
Junction Capacitance	V _R = 0 V, 1 MHz	_	_	0.25	_	_	0.25		_	0.25	pF
DC Block Capacitance											
Capacitance	1 MHz		0.5	1.0	_	0.5	1.0	_	0.5	1.0	pF

Absolute Maximum Ratings @ T_A = 25°C (Unless otherwise noted)

Parameter	Conditions	Absolute Maximum		
RF Incident Power	Applied to step recovery diode (pin 1 to pin2) Applied to microwave input (pin 4)	27 dBm 20 dBm		
Total Dissipated Power	Infinite heat sink, $T_c = 25^{\circ}C$. Derate power linearty from 100 mW @ 85°C to 0 W @ 125°C	100 mW		
Junction Temperature	—	150°C		
Operating Temperature	—	-55°C to +125°C		
Storage Temperature	—	-65°C to +150°C		

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Handling Procedures

Please observe the following precautions to avoid damage:

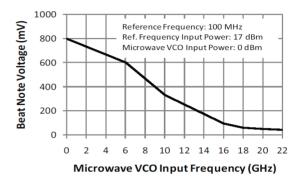
Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices. The moisture sensitivity level (MSL) rating is 1.

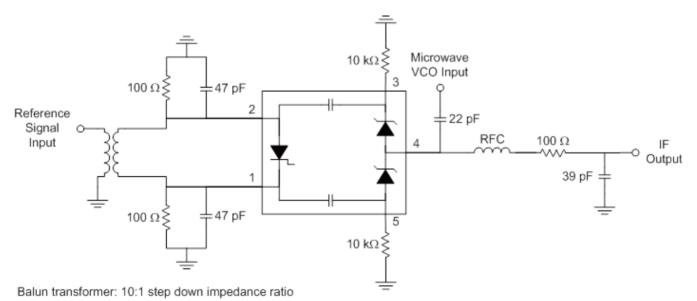
Environmental Capabilities

The MSPD101x-121 sampling phase detectors are capable of meeting the environmental requirements of MIL-STD-750 and MIL-STD-883.

Typical Performance: $T_A = +25^{\circ}C, Z_0 = 50 \Omega$



Recommended Circuit



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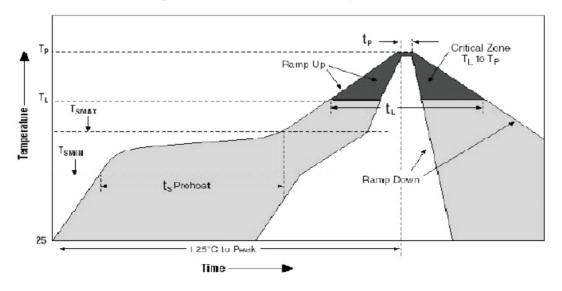
Assembly Instructions

The MSPD101x family of sampling phase detectors may be soldered to a printed circuit using conventional solder reflow or wave soldering procedures with RoHS type or Sn60/Pb40 type solders per the recommended time temperature profile described in Table I and Figure I.

Profile Feature	SnPb Solder Assembly	Pb-Free Solder Assembly		
Average Ramp-Up Rate $(T_{L} to T_{P})$	3°C /second maximum	3°C /second maximum		
Preheat:				
- Temperature Min (T _{smi})	100°C	150°C		
- Temperature Max (T _{smax})	150°C	200°C		
- Time (min to max)(t _s)	60-120 s	60-180 s		
T _{smax} to T _L - Ramp-Up Rate		3°C/s maximum		
Time Maintained Above: - Temperature (Tլ) - Time (tլ)	<mark>183</mark> °C 60-150 s	217°C 60-150 s		
Peak temperature (T _p)	<mark>225</mark> +0/-5°C	260 +0/-5°C		
Time Within 5°C of Actual Peak Temperature (t _p)	10 – 30 s	20 – 40 s		
Ramp-Down Rate	6°C /s maximum	6°C /s maximum		
Time 25°C to Peak Temperature	6 minutes maximum	8 minutes maximum		

Table 1. Time-Temperature Profile for Sn60/Pb40 or RoHS Type Solders

Figure 1. Solder Re-Flow Time-Temperature Profile



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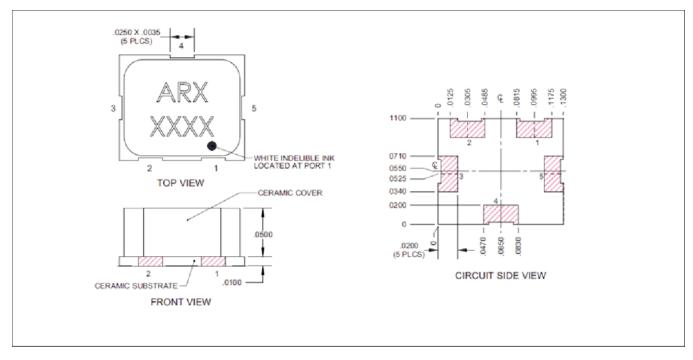
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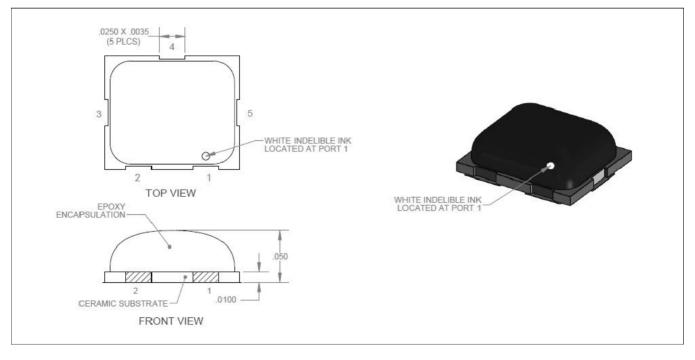
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Outline Drawing (CS121)



Outline Drawing (CS122)



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