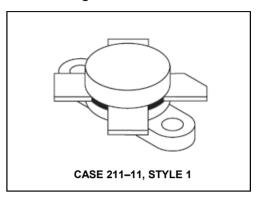


Rev. V1

Designed primarily for high–voltage applications as a high–power linear amplifier from 2.0 to 30 MHz. Ideal for marine and base station equipment.

- Specified 50 V, 30 MHz Characteristics —
 Output power = 150 W (PEP)
 Minimum gain = 13 dB
 Efficiency = 45%
- Intermodulation distortion @ 150 W (PEP) IMD = -32 dB (Max)
- · Diffused emitter resistors for superior ruggedness
- 100% tested for load mismatch at all phase angles with 30:1 VSWR @ 150 W CW

Product Image



MAXIMUM RATINGS

Rating	Symbo	ol Value	Unit
Collector-Emitter Voltage	V _{CEO}	50	Vdc
Collector-Base Voltage	V _{CBO}	100	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous	Ιc	16	Adc
Withstand Current — 10 s	_	20	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	233 1.33	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _{eJC}	0.75	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				
Collector-Emitter Breakdown Voltage (I _C = 200 mAdc, I _B = 0)	V _{(BR)CEO}	50	_	_	Vdc
Collector–Emitter Breakdown Voltage (I _C = 100 mAdc, V _{BE} = 0)	V _{(BR)CES}	100	_	_	Vdc
Collector-Base Breakdown Voltage (I _C = 100 mAdc, I _E = 0)	V _{(BR)CBO}	100	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 10 mAdc, I _C = 0)	V _{(BR)EBO}	4.0	_	_	Vdc

(continued)

1



Rev. V1

ELECTRICAL CHARACTERISTICS — continued (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
ON CHARACTERISTICS	•	•	•	•	•
DC Current Gain (I _C = 5.0 Adc, V _{CE} = 5.0 Vdc)	h _{FE}	10	30	80	_
DYNAMIC CHARACTERISTICS	•	•		•	
Output Capacitance (V _{CB} = 50 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	220	300	pF
FUNCTIONAL TESTS	•		•		•
Common–Emitter Amplifier Gain (V _{CC} = 50 Vdc, P _{out} = 150 W (PEP), I _C (max) = 3.32 Adc, f = 30; 30.001 MHz)	G _{PE}	13	15	_	dB
Output Power (V _{CE} = 50 Vdc, f = 30; 30.001 MHz)	P _{out}	150	_	_	W (PEP)
Collector Efficiency (V _{CC} = 50 Vdc, P _{out} = 150 W (PEP), I _C (max) = 3.32 Adc, f = 30, 30.001 MHz)	η	45	_	_	%
Intermodulation Distortion (1) (V _{CE} = 50 Vdc, P _{out} = 150 W (PEP), I _C = 3.32 Adc)	IMD	_	-35	-32	dB
Electrical Ruggedness (V _{CC} = 50 Vdc, P _{out} = 150 W CW, f = 30 MHz, VSWR 30:1 at all Phase Angles)	Ψ	No Degradation in Output Power			

NOTE:

^{1.} To Mil-Std-1311 Version A, Test Method 2204, Two Tone, Reference each Tone.



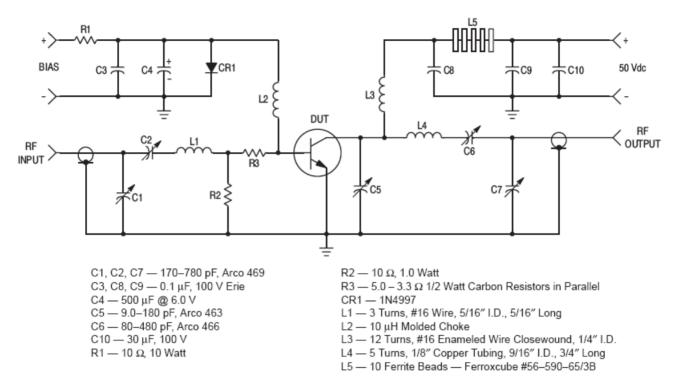
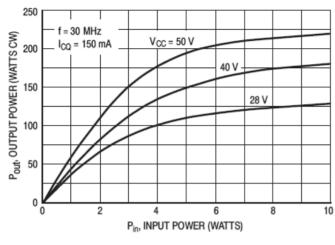


Figure 1. 30 MHz Test Circuit Schematic





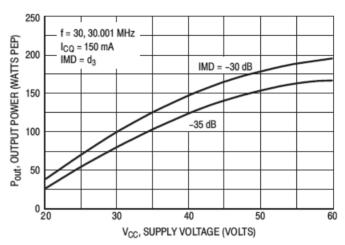
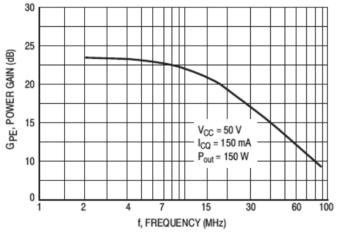


Figure 2. Output Power versus Input Power

Figure 3. Output Power versus Supply Voltage



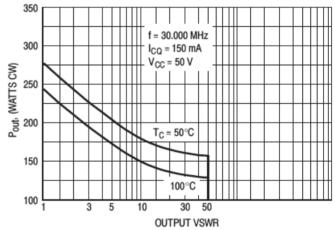


Figure 4. Power Gain versus Frequency

Figure 5. RF Safe Operating Area (SOAR)



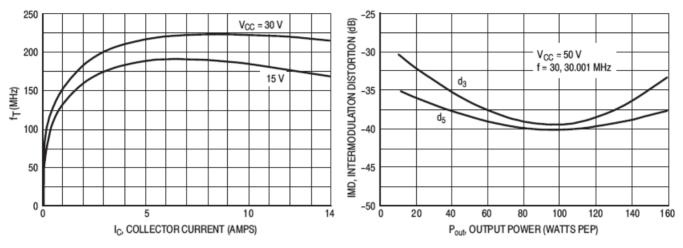


Figure 6. f_T versus Collector Current

Figure 7. IMD versus Pout

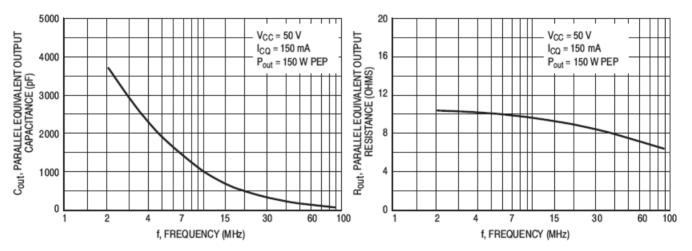


Figure 8. Output Capacitance versus Frequency

Figure 9. Output Resistance versus Frequency



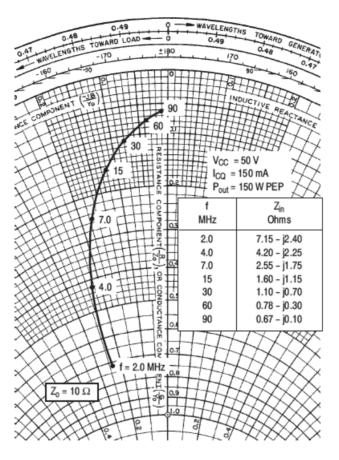
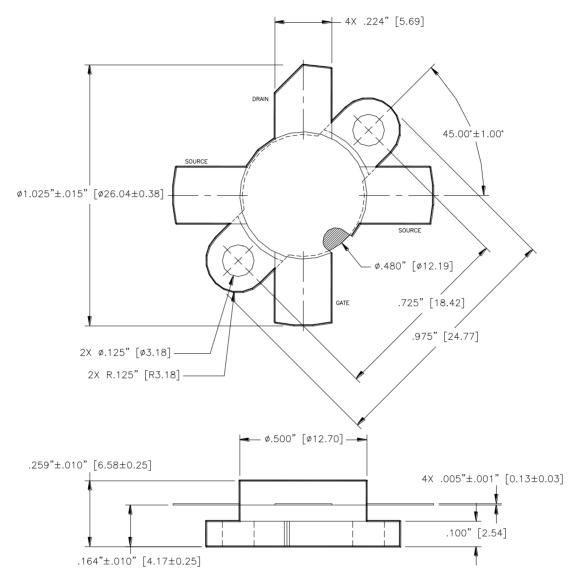


Figure 10. Series Equivalent Impedance





Unless otherwise noted, tolerances are inches $\pm .005$ " [millimeters ± 0.13 mm]

MRF429



The RF Line NPN Silicon Power Transistor 150W(PEP), 30MHz, 50V

Rev. V1

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