

PIN CONFIGURATION

PIN NO.	SYMBOL	LEVEL	FUNCTION	
1	Vss	--	POWER SUPPLY	GND (0V)
2	VDD	--		5V
3	Vo	--		FOR LCD DRIVE
4	RS	H/L	REGISTER SELECT SIGNAL H: DATA INPUT L: INSTRUCTION INPUT	
5	R/W	H/L	H: DATA READ (MODULE-->MPU) L: DATA WRITE (MODULE<--MPU)	
6	E	H,H->L	ENABLE	
7~14	DB0~DB7	H/L	DATA BUS-SOFTWARE SELECTABLE 4 OR 8 BIT MODE	
15	A	--	ANODE	LED BACKLIGHT
16	K	--	CATHODE	LED BACKLIGHT

ELECTRICAL CHARACTERISTICS TA=25°C

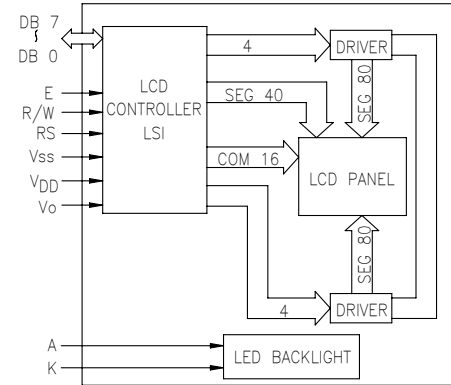
ITEM		SYMBOL	CONDITION	STANDARD			UNT
				MIN	TYP	MAX	
SUPPLY VOLTAGE FOR LOGIC		VDD-VSS	-	4.5	5.0	5.5	V
SUPPLY CURRENT FOR LOGIC		IDD	VDD=5V VDD-V0=4.5V	-	3.0	5.0	mA
INPUT VOLTAGE	HIGH	VIH	-	0.7 VDD	-	VDD	V
	LOW	VIL	-	-0.3	-	0.6	V
*LED BACKLIGHT	VOLTAGE	Vf	If=280mA	-	4.1	4.6	V
	CURRENT	If	-	-	280	-	mA
	POWER CONSUMPTION	PD	-	-	1932	-	mW
	COLOR	-	-	-	-	-	nm
	LUMINOUS	L	If=280mA	-	170	-	cd/m ²
	PEAK WAVELENGTH	λP	If=280mA	-	570	-	nm

*ONLY APPLIES TO MODULES WITH BACKLIGHT

ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	TEST CONDITION	STANDARD VALUE		UNIT
			MIN	MAX	
SUPPLY VOLTAGE FOR LOGIC	VDD-VSS	Ta=25°C	-0.3	7.0	V
SUPPLY VOLTAGE FOR LCD	VDD-V0	-	-0.3	12.0	V
INPUT VOLTAGE	Vi	Ta=25°C	-0.3	VDD	V
OPERATING TEMPERATURE	TOPR	LCM-H	-20	70	°C
STORAGE TEMPERATURE	TSTG	LCM-H	-30	80	°C

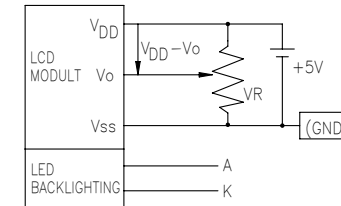
BLOCK DIAGRAM (1/16 DUTY, 1/5 BIAS)



POWER SUPPLY

VDD-V0: LCD DRIVING VOLTAGE

VR: 20KΩ



*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN= *DECIMAL PRECISION 0.00 MAX= *DECIMAL PRECISION 0.00

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