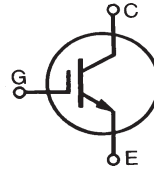


XPT™ 600V IGBT GenX3™

IXXQ30N60B3M

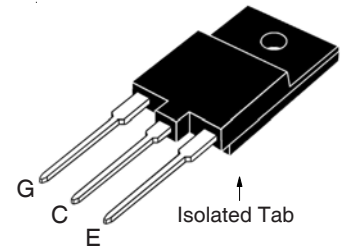
(Electrically Isolated Tab)

Extreme Light Punch Through
IGBT for 5-30 kHz Switching



$$\begin{aligned} V_{CES} &= 600V \\ I_{C110} &= 19A \\ V_{CE(sat)} &\leq 1.85V \\ t_{fi(typ)} &= 125ns \end{aligned}$$

OVERMOLDED
(IXXQ...M) OUTLINE



G = Gate C = Collector
E = Emitter

Symbol	Test Conditions	Maximum Ratings	
V_{CES}	$T_J = 25^\circ\text{C}$ to 175°C	600	V
V_{CGR}	$T_J = 25^\circ\text{C}$ to 175°C , $R_{GE} = 1M\Omega$	600	V
V_{GES}	Continuous	± 20	V
V_{GEM}	Transient	± 30	V
I_{C25}	$T_C = 25^\circ\text{C}$	33	A
I_{C110}	$T_C = 110^\circ\text{C}$	19	A
I_{CM}	$T_C = 25^\circ\text{C}$, 1ms	140	A
I_A	$T_C = 25^\circ\text{C}$	20	A
E_{AS}	$T_C = 25^\circ\text{C}$	250	mJ
SSOA (RBSOA)	$V_{GE} = 15V$, $T_{VJ} = 150^\circ\text{C}$, $R_G = 10\Omega$ Clamped Inductive Load	$I_{CM} = 48$ @ $V_{CE} \leq V_{CES}$	A
t_{sc} (SCSOA)	$V_{GE} = 15V$, $V_{CE} = 360V$, $T_J = 150^\circ\text{C}$ $R_G = 82\Omega$, Non Repetitive	10	μs
P_C	$T_C = 25^\circ\text{C}$	90	W
T_J		-55 ... +175	$^\circ\text{C}$
T_{JM}		175	$^\circ\text{C}$
T_{stg}		-55 ... +175	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering	300	$^\circ\text{C}$
T_{SOLD}	1.6 mm (0.062in.) from Case for 10s	260	$^\circ\text{C}$
V_{ISOL}	50/60 Hz, 1 Minute	2500	V~
M_d	Mounting Torque	1.13 / 10	Nm/lb.in
Weight		5	g

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{CES}	$I_C = 250\mu\text{A}$, $V_{GE} = 0V$	600		V
$V_{GE(th)}$	$I_C = 250\mu\text{A}$, $V_{CE} = V_{GE}$	3.0		5.5 V
I_{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0V$ $T_J = 150^\circ\text{C}$			25 μA 250 μA
I_{GES}	$V_{CE} = 0V$, $V_{GE} = \pm 20V$			± 100 nA
$V_{CE(sat)}$	$I_C = 24A$, $V_{GE} = 15V$, Note 1 $T_J = 150^\circ\text{C}$	1.66 1.97		V V

Features

- Plastic Overmolded Tab for Electrical Isolation
- Optimized for 5-30kHz Switching
- Square RBSOA
- Avalanche Capability
- Short Circuit Capability
- 2500V~ Electrical Isolation

Advantages

- High Power Density
- 175°C Rated
- Extremely Rugged
- Low Gate Drive Requirement

Applications

- Power Inverters
- UPS
- Motor Drives
- SMPS
- PFC Circuits
- Battery Chargers
- Welding Machines
- Lamp Ballasts

Symbol Test Conditions

($T_J = 25^\circ\text{C}$ Unless Otherwise Specified)

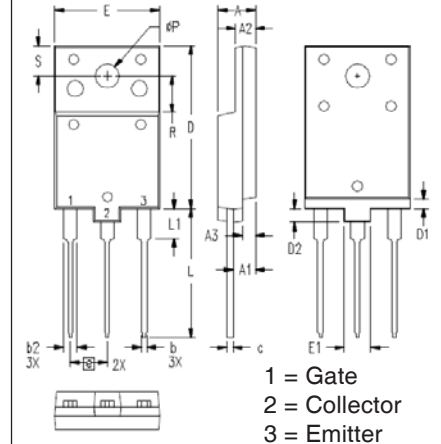
Characteristic Values

		Min.	Typ.	Max.
g_{fs}	$I_C = 24\text{A}, V_{CE} = 10\text{V}$, Note 1	8	14	S
C_{ies}	$V_{CE} = 25\text{V}, V_{GE} = 0\text{V}, f = 1\text{MHz}$		1185	pF
C_{oes}			137	pF
C_{res}			25	pF
$Q_{g(on)}$	$I_C = 24\text{A}, V_{GE} = 15\text{V}, V_{CE} = 0.5 \cdot V_{CES}$		39	nC
Q_{ge}			9	nC
Q_{gc}			17	nC
$t_{d(on)}$	Inductive load, $T_J = 25^\circ\text{C}$ $I_C = 24\text{A}, V_{GE} = 15\text{V}$ $V_{CE} = 400\text{V}, R_G = 10\Omega$ Note 2		23	ns
t_{ri}			36	ns
E_{on}			0.55	mJ
$t_{d(off)}$			97	ns
t_{fi}			125	ns
E_{off}			0.50	mJ
$t_{d(on)}$	Inductive load, $T_J = 150^\circ\text{C}$ $I_C = 24\text{A}, V_{GE} = 15\text{V}$ $V_{CE} = 400\text{V}, R_G = 10\Omega$ Note 2		23	ns
t_{ri}			34	ns
E_{on}			1.10	mJ
$t_{d(off)}$			112	ns
t_{fi}			180	ns
E_{off}			0.70	mJ
R_{thJC}				1.66 $^\circ\text{C/W}$
R_{thCS}			0.25	$^\circ\text{C/W}$

Notes:

1. Pulse test, $t \leq 300\mu\text{s}$, duty cycle, $d \leq 2\%$.
2. Switching times & energy losses may increase for higher $V_{CE}(\text{clamp})$, T_J or R_G .

OVERMOLDED OUTLINE



SYM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	.213	.228	5.40	5.80
A1	.122	.138	3.10	3.50
A2	.114	.130	2.90	3.30
A3	.075	.091	1.90	2.30
b	.026	.037	0.65	0.95
b2	.075	.091	1.90	2.30
c	.031	.043	0.80	1.10
D	.957	.972	24.30	24.70
D1	.051	.067	1.30	1.70
D2	.071	.087	1.80	2.20
E	.606	.622	15.40	15.80
E1	.154	.169	3.90	4.30
e	.215 BSC		5.45 BSC	
L	.748	.768	19.00	19.50
L1	.169	.185	4.30	4.70
øP	.134	.150	3.40	3.80
R	.209	.224	5.30	5.70
S	.169	.185	4.30	4.70

PRELIMINARY TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2	7,157,338B2
	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2	7,071,537	

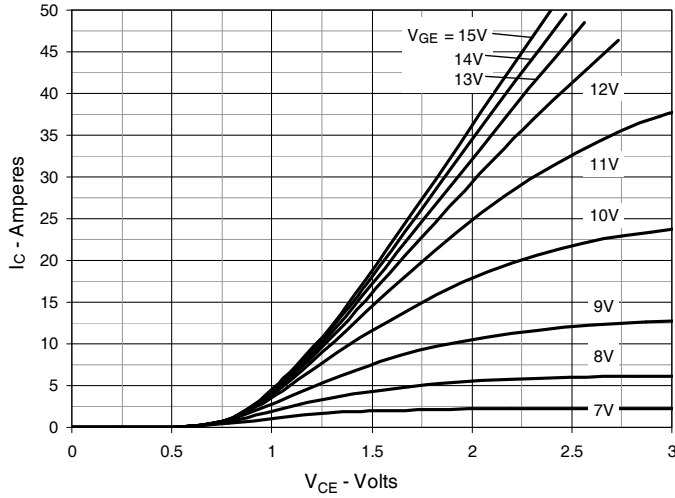
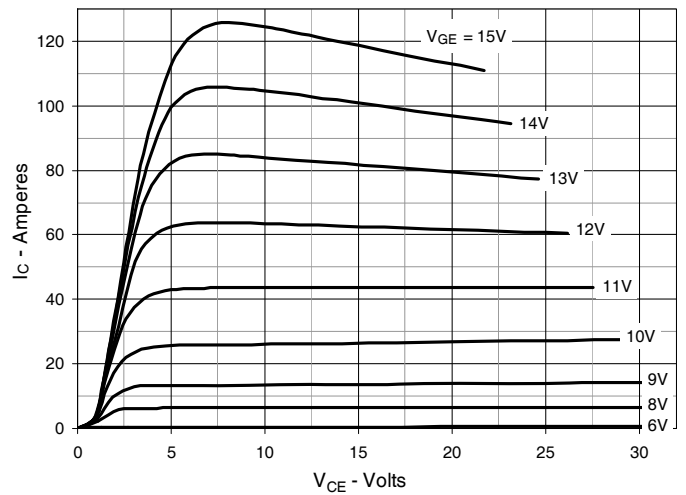
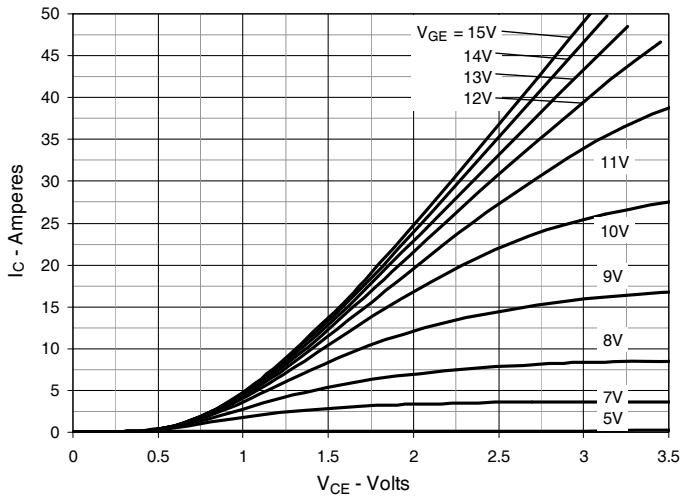
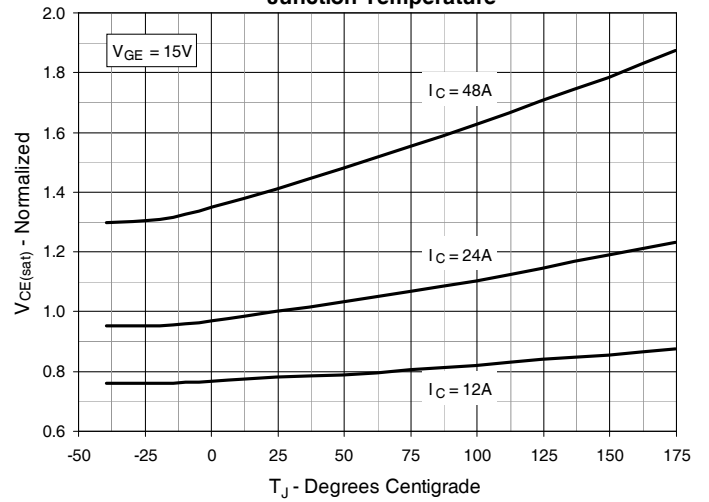
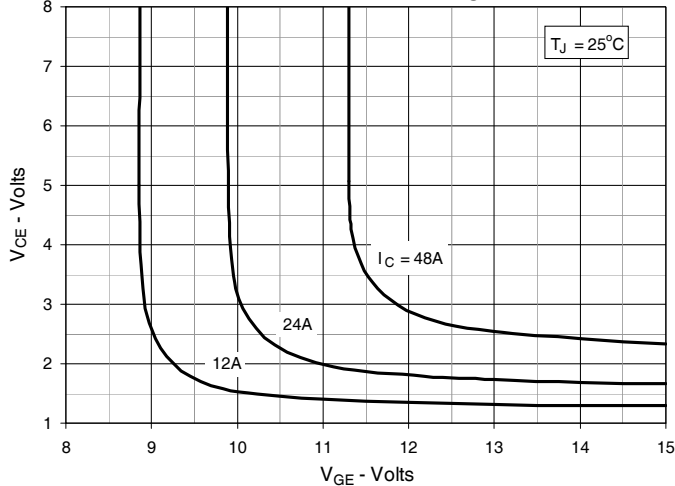
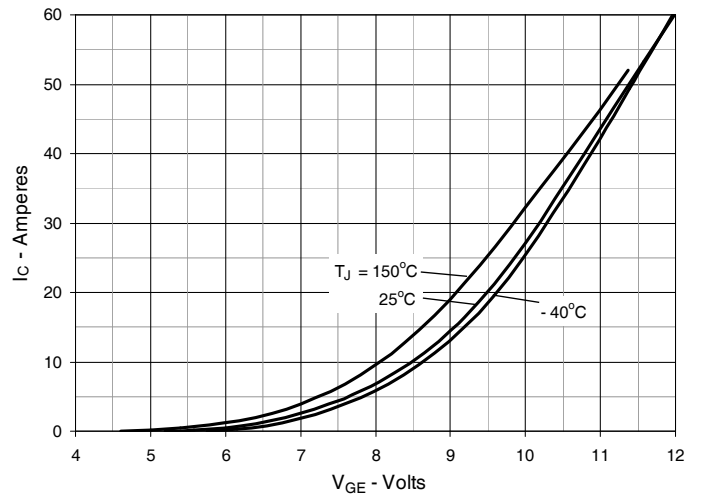
Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

Fig. 3. Output Characteristics @ $T_J = 150^\circ\text{C}$

Fig. 4. Dependence of $V_{CE(sat)}$ on Junction Temperature

Fig. 5. Collector-to-Emitter Voltage vs. Gate-to-Emitter Voltage

Fig. 6. Input Admittance


Fig. 7. Transconductance

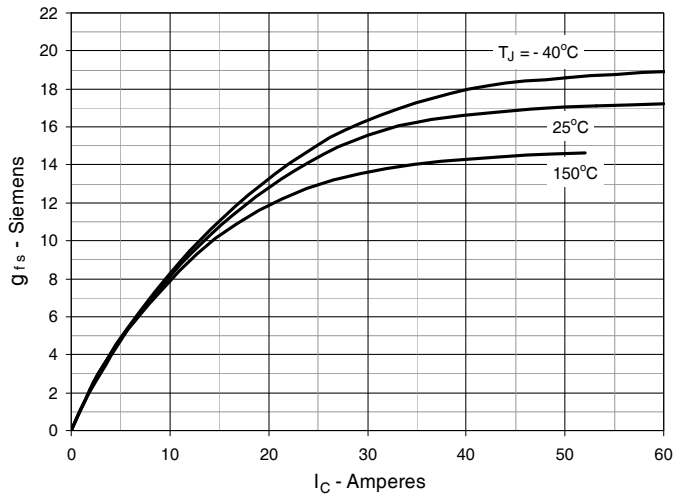


Fig. 8. Gate Charge

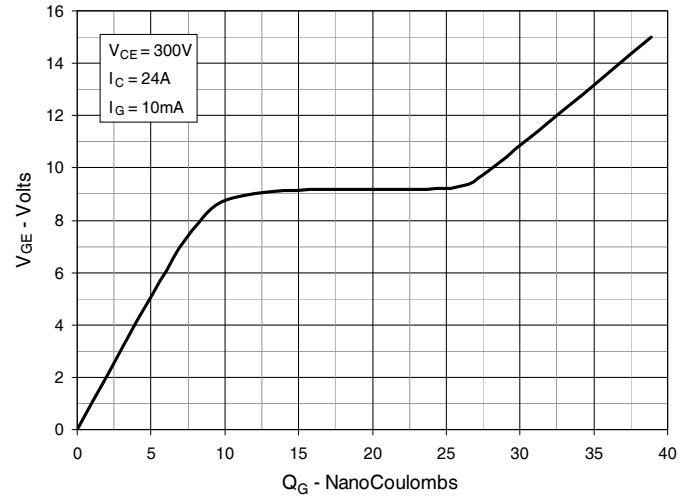


Fig. 9. Capacitance

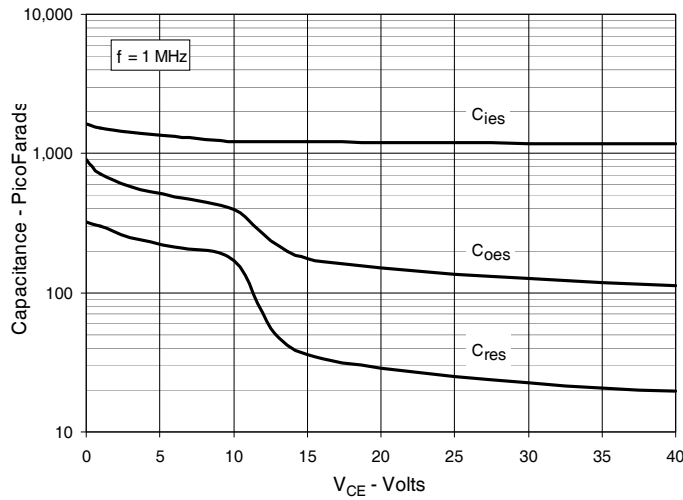


Fig. 10. Reverse-Bias Safe Operating Area

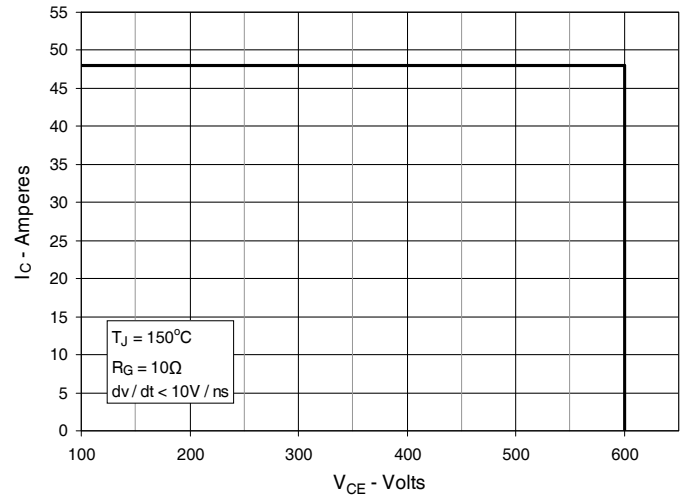


Fig. 11. Forward-Bias Safe Operating Area

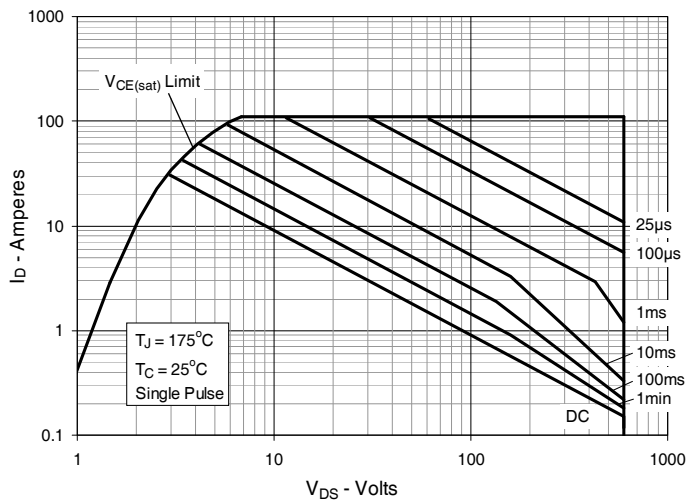


Fig. 12. Maximum Transient Thermal Impedance

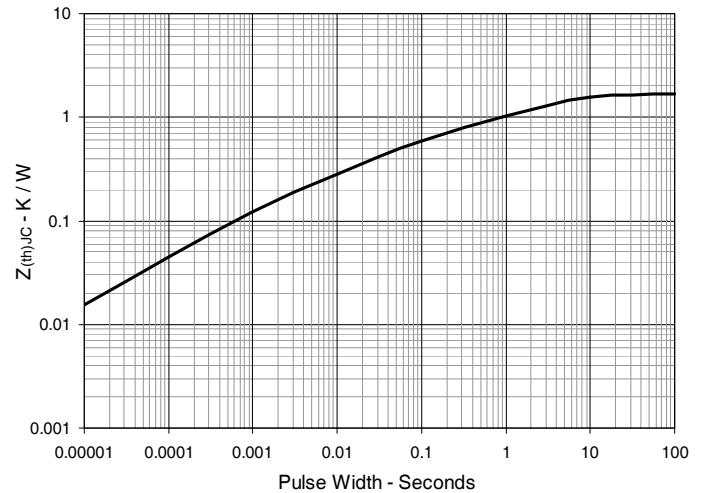


Fig. 13. Inductive Switching Energy Loss vs. Gate Resistance

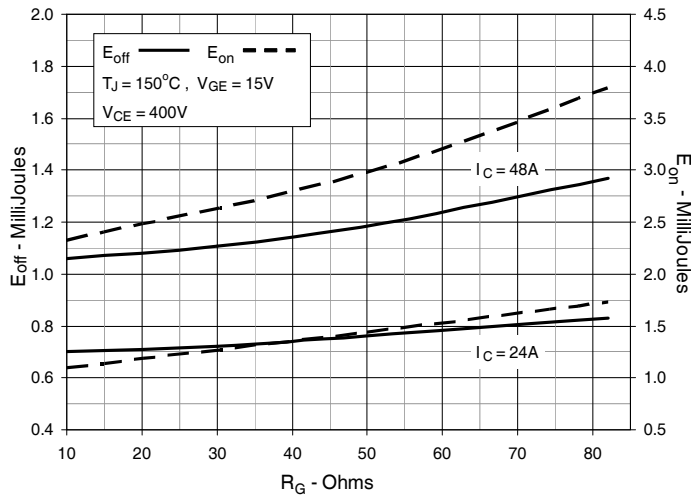


Fig. 14. Inductive Switching Energy Loss vs. Collector Current

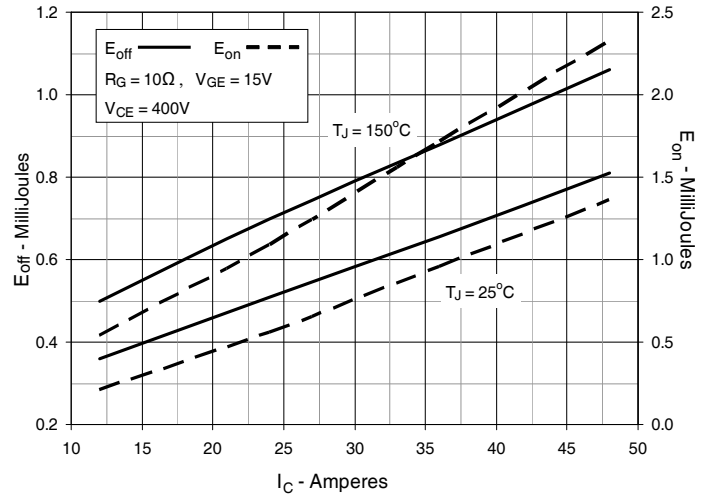


Fig. 15. Inductive Switching Energy Loss vs. Junction Temperature

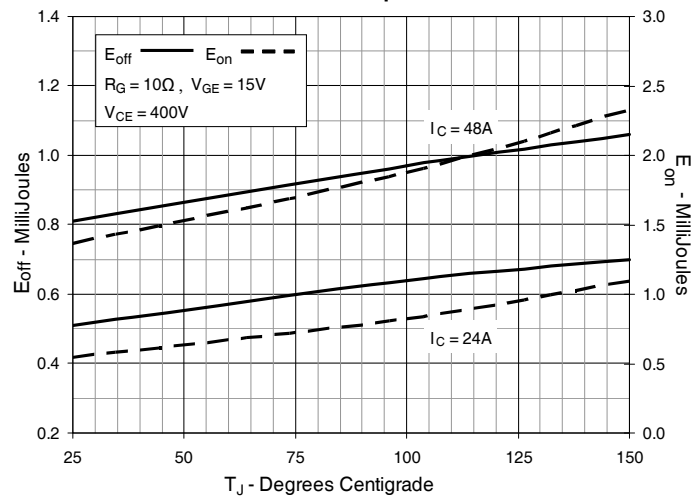


Fig. 16. Inductive Turn-off Switching Times vs. Gate Resistance

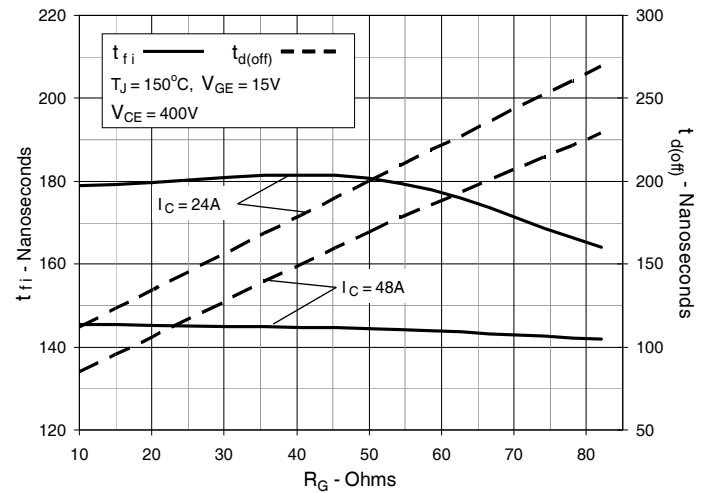


Fig. 17. Inductive Turn-off Switching Times vs. Collector Current

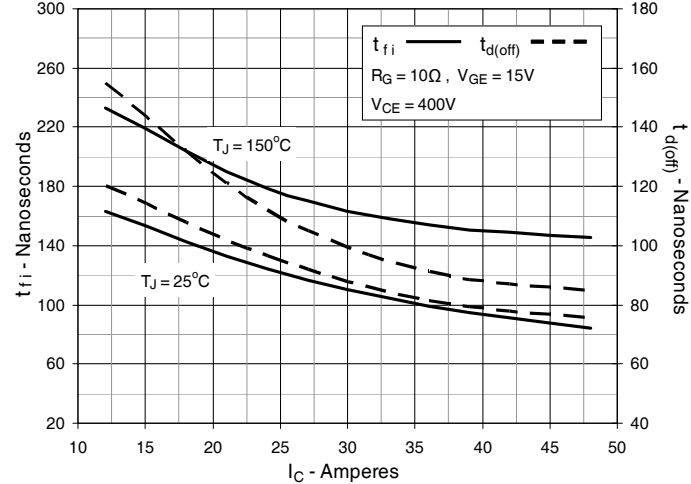


Fig. 18. Inductive Turn-off Switching Times vs. Junction Temperature

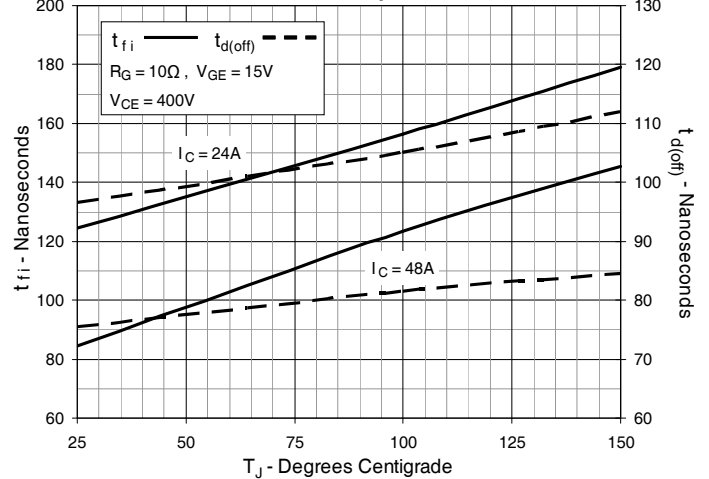


Fig. 19. Inductive Turn-on Switching Times vs. Gate Resistance

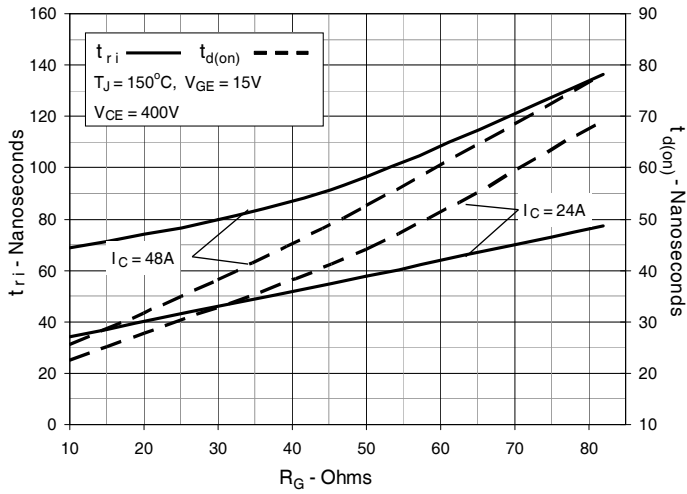


Fig. 20. Inductive Turn-on Switching Times vs. Collector Current

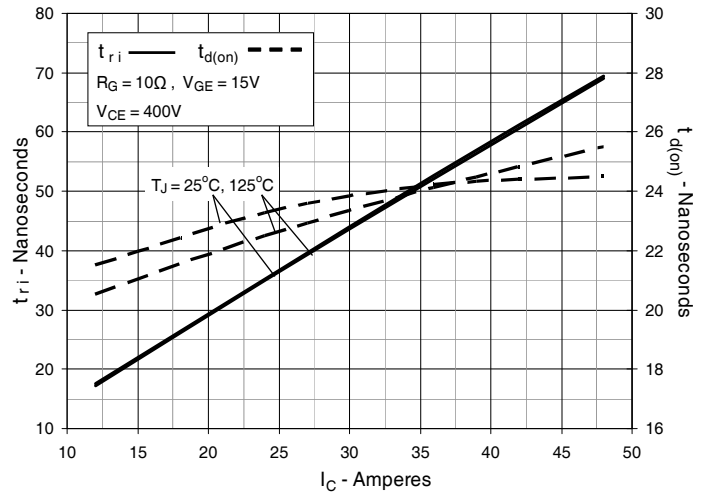
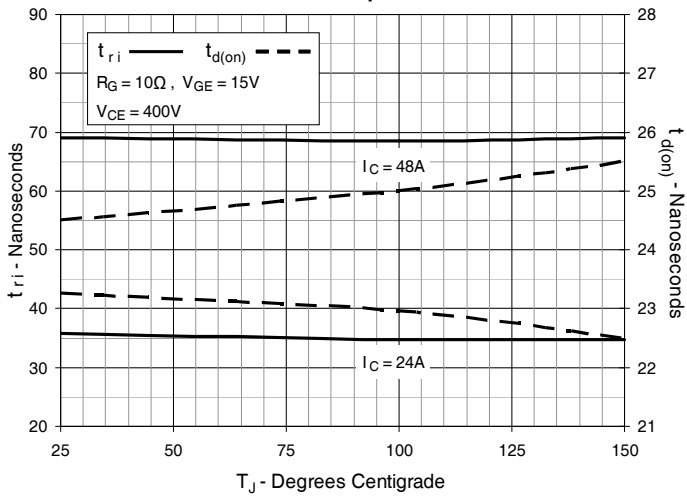


Fig. 21. Inductive Turn-on Switching Times vs. Junction Temperature





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