

PB023903-0312

The block diagram illustrates the internal architecture of the Z8051 microcontroller. At the center is the **Z8051 Core**, which is connected to various peripheral blocks. The **On-Chip Debug** block is connected to the core and external signals **DSDA** and **DSDL**. The **Low Voltage Indicator** and **Buzzer** are also connected to the core. The **SPI2** block is connected to the core and external signals **MOSI2/P14**, **MISO2/P15**, **SCK2/P16**, and **SS2/P17**. The **32KB Flash** and **XRAM (768 Bytes)** are connected to the core. The **IRAM (256 Bytes)** is also connected to the core. The **Basic Interval Timer** and **Power On Reset** are connected to the core. The **Low Voltage Reset** is connected to the core. The **Watchdog Timer 5kHz INTRC OSC** is connected to the core. The **Int. RC Osc. 16MHz** is connected to the core. The **Watch Timer** is connected to the core. The **Voltage Down Converter** is connected to the core and external signals **VDD** and **VSS**. The **Clock/System Control** block is connected to the core and external signals **RESETB/P55**, **XSOUT/P54/EINT10**, **SXIN/P53/T0/PWM00**, **XIN/P51**, and **XOUT/P50**. The **USI0** block is connected to the core and external signals **TXD0/P41**, **RXD0/P40**, **MOSI0/P41**, **MISO0/P40**, **SCK0/P42**, **SS0/P43**, **SDA0/P41**, and **SCL0/P40**. The **USI1** block is connected to the core and external signals **TXD1/P20**, **RXD1/P10**, **MOSI1/P20**, **MISO1/P10**, **SCK1/P21**, **SS1/P22**, **SDA1/P20**, and **SCL1/P10**. The **LCD Driver/Controller** is connected to the core and external signals **COM0-COM1/P37-P36**, **COM2-COM7/SEG0-SEG5/P35-P30**, **SEG6-SEG29/P27-P03**, and **VLC0-VLC3/P43-P40**. The **P5 Port** is connected to the core and external signals **P50/XOUT**, **P51/XIN**, **P52/EINT8/EC0/BLNK**, **SXIN/P53/T0/PWM00**, **SXOUT/P54/EINT10**, and **P55/RESETB**. The **P4 Port** is connected to the core and external signals **P40/VLC3/RXD0/SCL0/MISO0**, **P41/VLC2/TXD0/SDA0/MOSI0**, **P42/VLC1/SCK0**, and **P43/VLC0/SS0**. The **P3 Port** is connected to the core and external signals **P30-P33/COM7-COM4/SEG5-SEG2**, **P34-P35/COM3-COM2/SEG1-COM0**, and **P36-P37/COM1-COM0**. The **P2 Port** is connected to the core and external signals **P20/SEG13/AN14/TXD1/SDA1/MOSI1**, **P21/SEG12/AN15/SCK1**, **P22/SEG11/SS1**, and **P23-P27/SEG10-SEG6**. The **P1 Port** is connected to the core and external signals **P10/SEG14/AN13/RXD1/SCL1/MISO1**, **P11/SEG15/AN12/EINT12/T20/PWM20**, **P12/SEG16/AN11/EINT11/T10/PWM10**, **P13/SEG17/AN10/EC1/BUZO**, **P14/SEG18/AN9/MOSI2**, **P15/SEG19/AN8/MISO2**, **P16/SEG20/AN7/EINT7/SCK2**, and **P17/SEG21/AN6/EINT6/SS2**. The **P0 Port** is connected to the core and external signals **P00/EC3/DSDA**, **P01/T30/DSDL**, **P02/AN0/AVREF/EINT0/T40/PWM44A**, **P03/SEG26/AN1/EINT1/PWM44B**, **P04/SEG25/AN2/EINT2/PWM44A**, **P05/SEG24/AN3/EINT3/PWM44B**, **P06/SEG23/AN4/EINT4/PWM44A**, **P07/SEG22/AN5/EINT5/PWM44B**, **P08/SEG21/AN6/EINT6/SS2**, **P09/SEG20/AN7/EINT7/SCK2**, **P10/SEG19/AN8/MISO2**, **P11/SEG18/AN9/MOSI2**, **P12/SEG17/AN10/EC1/BUZO**, **P13/SEG16/AN11/EINT11/T10/PWM10**, **P14/SEG15/AN12/EINT12/T20/PWM20**, **P15/SEG14/AN13/RXD1/SCL1/MISO1**, **P16/SEG13/AN14/TXD1/SDA1/MOSI1**, **P17/SEG12/AN15/SCK1**, **P18/SEG11/SS1**, and **P19/SEG10/SEG6**. The **6-ch PWM** block is connected to the core and external signals **PWM44A/P02**, **PWM44B/P03**, **PWM44A/P04**, **PWM44B/P05**, **PWM44C/P06**, **PWM44C/P07**, and **EINT8/BLNK/P52**. The **8-Bit Timer 3** and **8-Bit Timer 4** are connected to the core and external signals **T30/P01**, **EC3/P00**, **EINT0/P02**, **T40/P02**, and **EINT1/P03**. The **16-Bit Timer 1** is connected to the core and external signals **T10/PWM10/P12**, **EINT11/P12**, and **EC1/P13**. The **16-Bit Timer 2** is connected to the core and external signals **T20/PWM20/P11** and **EINT12/P11**. The **8-Bit Timer 0** is connected to the core and external signals **T00/PWM00/P53**, **EINT10/P54**, and **EC0/P52**. The **12-Bit A/D Converter** is connected to the core and external signals **AN0-AN5/P02-P07**, **AN6-AN13/P17-P10**, **AN14-AN15/P20-P21**, and **AVREF/P02**. The **On-Chip Debug** block is connected to the core and external signals **DSDA** and **DSDL**. The **Low Voltage Indicator** and **Buzzer** are connected to the core. The **SPI2** block is connected to the core and external signals **MOSI2/P14**, **MISO2/P15**, **SCK2/P16**, and **SS2/P17**. The **32KB Flash** and **XRAM (768 Bytes)** are connected to the core. The **IRAM (256 Bytes)** is connected to the core. The **Basic Interval Timer** and **Power On Reset** are connected to the core. The **Low Voltage Reset** is connected to the core. The **Watchdog Timer 5kHz INTRC OSC** is connected to the core. The **Int. RC Osc. 16MHz** is connected to the core. The **Watch Timer** is connected to the core. The **Voltage Down Converter** is connected to the core and external signals **VDD** and **VSS**. The **Clock/System Control** block is connected to the core and external signals **RESETB/P55**, **XSOUT/P54/EINT10**, **SXIN/P53/T0/PWM00**, **XIN/P51**, and **XOUT/P50**. The **USI0** block is connected to the core and external signals **TXD0/P41**, **RXD0/P40**, **MOSI0/P41**, **MISO0/P40**, **SCK0/P42**, **SS0/P43**, **SDA0/P41**, and **SCL0/P40**. The **USI1** block is connected to the core and external signals **TXD1/P20**, **RXD1/P10**, **MOSI1/P20**, **MISO1/P10**, **SCK1/P21**, **SS1/P22**, **SDA1/P20**, and **SCL1/P10**. The **LCD Driver/Controller** is connected to the core and external signals **COM0-COM1/P37-P36**, **COM2-COM7/SEG0-SEG5/P35-P30**, **SEG6-SEG29/P27-P03**, and **VLC0-VLC3/P43-P40**. The **P5 Port** is connected to the core and external signals **P50/XOUT**, **P51/XIN**, **P52/EINT8/EC0/BLNK**, **SXIN/P53/T0/PWM00**, **SXOUT/P54/EINT10**, and **P55/RESETB**. The **P4 Port** is connected to the core and external signals **P40/VLC3/RXD0/SCL0/MISO0**, **P41/VLC2/TXD0/SDA0/MOSI0**, **P42/VLC1/SCK0**, and **P43/VLC0/SS0**. The **P3 Port** is connected to the core and external signals **P30-P33/COM7-COM4/SEG5-SEG2**, **P34-P35/COM3-COM2/SEG1-COM0**, and **P36-P37/COM1-COM0**. The **P2 Port** is connected to the core and external signals **P20/SEG13/AN14/TXD1/SDA1/MOSI1**, **P21/SEG12/AN15/SCK1**, **P22/SEG11/SS1**, and **P23-P27/SEG10-SEG6**. The **P1 Port** is connected to the core and external signals **P10/SEG14/AN13/RXD1/SCL1/MISO1**, **P11/SEG15/AN12/EINT12/T20/PWM20**, **P12/SEG16/AN11/EINT11/T10/PWM10**, **P13/SEG17/AN10/EC1/BUZO**, **P14/SEG18/AN9/MOSI2**, **P15/SEG19/AN8/MISO2**, **P16/SEG20/AN7/EINT7/SCK2**, and **P17/SEG21/AN6/EINT6/SS2**. The **P0 Port** is connected to the core and external signals **P00/EC3/DSDA**, **P01/T30/DSDL**, **P02/AN0/AVREF/EINT0/T40/PWM44A**, **P03/SEG26/AN1/EINT1/PWM44B**, **P04/SEG25/AN2/EINT2/PWM44A**, **P05/SEG24/AN3/EINT3/PWM44B**, **P06/SEG23/AN4/EINT4/PWM44A**, **P07/SEG22/AN5/EINT5/PWM44B**, **P08/SEG21/AN6/EINT6/SS2**, **P09/SEG20/AN7/EINT7/SCK2**, **P10/SEG19/AN8/MISO2**, **P11/SEG18/AN9/MOSI2**, **P12/SEG17/AN10/EC1/BUZO**, **P13/SEG16/AN11/EINT11/T10/PWM10**, **P14/SEG15/AN12/EINT12/T20/PWM20**, **P15/SEG14/AN13/RXD1/SCL1/MISO1**, **P16/SEG13/AN14/TXD1/SDA1/MOSI1**, **P17/SEG12/AN15/SCK1**, **P18/SEG11/SS1**, and **P19/SEG10/SEG6**. The **6-ch PWM** block is connected to the core and external signals **PWM44A/P02**, **PWM44B/P03**, **PWM44A/P04**, **PWM44B/P05**, **PWM44C/P06**, **PWM44C/P07**, and **EINT8/BLNK/P52**. The **8-Bit Timer 3** and **8-Bit Timer 4** are connected to the

KEY FEATURES

- High-Performance 8-bit CISC Core
- 12 to 16 12-Bit ADC Channels
- LCD Driver (21 segments/8 common)
- Internal RC Oscillator for Lower Component Count
- Timers with Multiple 8- and 16-bit Capture, Counter, Compare and PWM Modes

Z51F3220 MCU Feature Set

- High-Performance 8-Bit CISC Core (2 clocks per machine cycle)
- 32KB On-Chip Flash Memory
- 256 Bytes IRAM
- 768 Bytes XRAM
- Operating Frequency: 0–16 MHz
- Operating Voltage: 1.8V–5.5V
- Internal 16 MHz RC Oscillator with Programmable Clock Divider
- Power-Saving Modes (Idle, Stop)
- Configurable Timers
 - Timer/Counter (8 bits x 1 channel, 16 bits by 2 channels, 8 bits x 2 channels or 16 bits by 1 channel)
 - 8-bit Capture and Compare PWM Timer
 - Two 16-Bit Capture and Compare PWM Timers
 - Two 8-Bit or one 16-bit Counter/Capture Timers or 10-Bit PWM in 3 Complementary Pairs with Programmable Delay
 - Programmable Pulse Generator
 - Basic Interval Timer
- Watchdog Timer
- Watch Timer
- Dedicated SPI Port
- 2 Universal Asynchronous Receivers/Transmitters (UART/SPI/I²C)
- LCD Driver (21 segments/8 common)
- Buzzer Driver Port
- 42 GPIO pins, configurable as push-pull, pull-up or open-drain
 - 9 general-purpose pins
 - 33 shared LCD pins
- 33 GPIO LCD pins
- 12-Bit ADC with 12 to 16 Input Channels
- Multiple Interrupts from Multiple Sources via Priority Setting
- Programmable Brown-Out Detector
- Operating Temperature: –40°C to 85°C
- Packages: 44-pin MQFP, 32-Pin SOP
- Lead-Free Manufacture

Zilog's Z8051 Family of MCUs: flexible, industry-standard MCU solutions backed by Zilog's long-term commitment to supporting our customers.

APPLICATIONS

- Battery Management
- Motor Control
- Thermostats, Appliance Control Panels, Digital Clocks
- Medical Devices
- Embedded Controls Monitoring
- LED Lighting Control

Ordering Information

The Z51F3220 MCU is offered in the following packages. Construct your part number based on the specific package you wish to order.

Z51F3220 MCU Part Number	ROM	IRAM	XRAM	Package
Z51F3220FNX	32 KB	256b	768b	44-pin MQFP
Z51F3220SKX	32 KB	256b	768b	32-pin SOP

Order the Z51F3220 MCU separately using part numbers from the above table. For complete ordering information, please refer to the Z51F3220 MCU Product Specification (PS0299).

For more information about Zilog's Z8051 family of products, ordering or product collateral, please consult your local Zilog distributor or representative. You can find sales office locations and the most current product information on our website; please visit us at www.zilog.com.

Documentation

For a complete listing of all available application notes, data sheets, user manuals, and sample libraries, please visit us at www.zilog.com.

Document Number

Description

PS0299

Z51F3220 Product Specification

Related Products

Zilog carries a number of products based on the Z8051 Core to suit your application requirements. For more information about the following products, please visit us at www.zilog.com.

Product Name

Description

Z51F0410 MCU

Z8051 core with 4KB Flash, 256b RAM and 256b EEPROM in a 10-pin SSOP package

Z51F0811 MCU

Z8051 core with 8KB Flash, 256b RAM and 512b EEPROM in 16-, 20- & 28-pin TSSOP and 32-pin QFN packages

Z51F3221 MCU

Z8051 core with 32KB Flash, 1.25KB RAM in 64- & 80-pin LQFP packages

Z51F6412 MCU

Z8051 core with 64KB Flash, 3.25KB RAM in 64- & 80-pin LQFP packages



Warning: DO NOT USE THIS PRODUCT IN LIFE SUPPORT SYSTEMS.

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As used herein

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