

DATASHEET

# M20048-1

M20048-1 • RADIONOVA<sup>®</sup>



## Features

- GPS receiver with a built-in antenna
- MediaTek MT3337-E ROM chip
- Ultra-small SMD package; 13.8 x 9.5 x 1.8mm
- Low current consumption
- AIC- Active Interference Cancellation
- EASY - Internally generated orbit prediction for fast fix times

# Contents

|            |  |           |            |  |           |
|------------|--|-----------|------------|--|-----------|
| <b>1.</b>  | <b>Description</b>                           | <b>2</b>  | <b>12.</b> | <b>Reflow soldering</b>                          | <b>16</b> |
| <b>2.</b>  | <b>Applications</b>                          | <b>2</b>  | 12.1.      | Placement  | 16        |
| <b>3.</b>  | <b>Part number</b>                           | <b>2</b>  | 12.2.      | Soldering paste                                  | 16        |
| <b>4.</b>  | <b>Functional block diagram</b>              | <b>3</b>  | 12.3.      | Soldering  | 16        |
| <b>5.</b>  | <b>Module specifications</b>                 | <b>3</b>  | 12.4.      | Multiple soldering                               | 17        |
| 5.1.       | Absolute maximum ratings                     | 3         | 12.5.      | Hand soldering                                   | 17        |
| 5.2.       | Recommended operating conditions             | 3         | <b>13.</b> | <b>Quality and environmental specifications</b>  | <b>17</b> |
| 5.3.       | DC electrical characteristics                | 4         | <b>14.</b> | <b>Hazardous material regulation conformance</b> | <b>18</b> |
| 5.4.       | RF specifications                            | 4         | <b>15.</b> | <b>Packaging</b>                                 | <b>18</b> |
| 5.5.       | Band rejection                               | 4         | 15.1.      | Optimal storage conditions                       | 18        |
| 5.6.       | Mechanical specifications                    | 5         | 15.2.      | Tape characteristics                             | 19        |
| 5.7.       | System specifications                        | 5         | 15.3.      | Reel dimensions                                  | 20        |
| <b>6.</b>  | <b>Module dimension</b>                      | <b>6</b>  | 15.4.      | Box dimensions                                   | 20        |
| <b>7.</b>  | <b>Schematic symbol and pin definition</b>   | <b>6</b>  | 15.5.      | Bag properties                                   | 21        |
| <b>8.</b>  | <b>Host PCB footprint</b>                    | <b>7</b>  | 15.6.      | Reel label information                           | 21        |
| <b>9.</b>  | <b>Module subsystem feature</b>              | <b>7</b>  |            |  |           |
| 9.1.       | Host baud rate/protocol selection            | 7         |            |  |           |
| 9.2.       | Host interface                               | 8         |            |  |           |
| 9.3.       | Power supply                                 | 8         |            |  |           |
| 9.4.       | TM (1PPS)                                    | 8         |            |  |           |
| 9.5.       | HRST (Hardware reset pin)                    | 8         |            |  |           |
| 9.6.       | Power management                             | 8         |            |  |           |
| 9.7.       | EPO (Extended Prediction Orbit) data service | 10        |            |  |           |
| 9.8.       | EASY (Self-Generated Orbit Prediction)       | 10        |            |  |           |
| 9.9.       | AIC (Active Interference Cancellation)       | 11        |            |  |           |
| <b>10.</b> | <b>Module application</b>                    | <b>11</b> |            |  |           |
| 10.1.      | Reference schematic                          | 11        |            |  |           |
| 10.2.      | External matching                            | 12        |            |  |           |
| 10.3.      | Internal antenna clearance                   | 13        |            |  |           |
| 10.4.      | Typical antenna matching results             | 13        |            |  |           |
| 10.5.      | Module placement guideline                   | 14        |            |  |           |
| <b>11.</b> | <b>Evaluation kit</b>                        | <b>15</b> |            |  |           |

## 1. Description

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A GPS receiver module with integrated antenna incorporating the MediaTek MT3337-E ROM chip. The receiver has configurable low power modes and operates from a 2.8V-4.2V supply. A 0.5ppm TXCO helps to speed up TTFF (Time to first fix). Multi-path algorithms improve position accuracy in inner-city environments.

## 2. Applications

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- Wearable devices
- Personal safety
- Bike trackers
- Asset tracking
- Sports equipment
- Cameras
- Smart watches
- Navigation devices

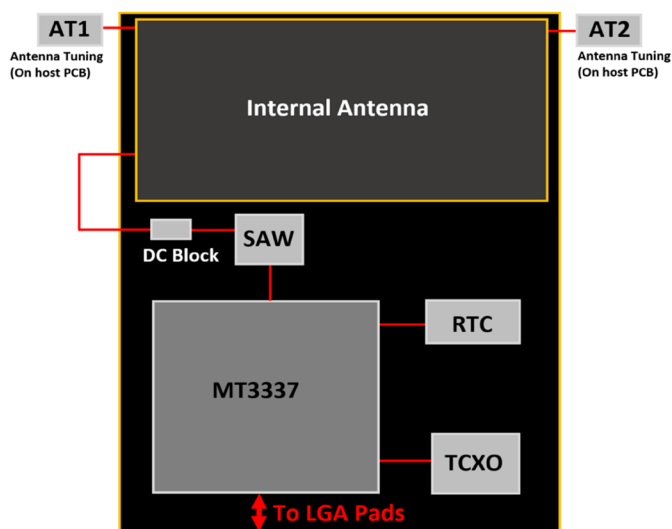
## 3. Part number

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M20048-1



## 4. Functional block diagram



## 5. Module specifications

### 5.1. Absolute maximum ratings

| SYMBOL      | PARAMETER               | MIN  | MAX | UNIT |
|-------------|-------------------------|------|-----|------|
| $V_{CC}$    | Main supply voltage     | -0.3 | 4.3 | V    |
| $V_{IO}$    | Supply voltage I/O ring | -0.3 | 3.6 | V    |
| $V_{BCKUP}$ | Backup supply voltage   | -0.3 | 4.3 | V    |
| $RF_{IN}$   | Maximum RF input power  | N/A  | +10 | dBm  |
| $T_{STG}$   | Storage temperature     | -40  | +85 | °C   |
| $T_{OP}$    | Operating temperature   | -40  | +85 | °C   |

Exposure to absolute ratings may adversely affect reliability and may cause permanent damage.

### 5.2. Recommended operating conditions

| SYMBOL      | PARAMETER             | MIN | TYP | MAX | UNIT |
|-------------|-----------------------|-----|-----|-----|------|
| $V_{CC}$    | Main supply voltage   | 2.8 | 3.3 | 4.3 | V    |
| $V_{BCKUP}$ | Backup supply voltage | 2.8 | 3.3 | 4.3 | V    |
| $T_{OP}$    | Operating temperature | -40 | -   | +85 | °C   |

### 5.3. DC electrical characteristics

Conditions:  $V_{CC} = 3.3V$ ,  $T_{OP} = 25\text{ }^{\circ}C$

| SYMBOL          | PARAMETER                    | TYP  | UNIT    |
|-----------------|------------------------------|------|---------|
| $I_{CC(ACQ)}$   | Acquisition mode current     | 24.5 | mA      |
| $I_{CC(TRK)}$   | Tracking mode current        | 21   | mA      |
| $I_{CC(STBY)}$  | Standby (sleep) mode current | <500 | $\mu A$ |
| $I_{CC(BCKUP)}$ | Backup mode current          | <30  | $\mu A$ |

### 5.4. RF specifications

Conditions:  $V_{CC} = 3.3V$ ,  $T_{OP} = 25\text{ }^{\circ}C$ , Freq = 1575.420MHz

| SYMBOL            | PARAMETER                              | TYP   | UNIT |
|-------------------|--|-------|------|
| $NF_{LNA}$        | LNA Noise Figure (MT3337 Integral LNA) | 2.2   | dB   |
| $ANT_{RL}$        | Antenna return loss                    | <-7.0 | dB   |
| $ANT_{EFF}$       | Antenna total efficiency               | >57%  | %    |
| $ANT_{EFF\ RHCP}$ | Antenna RHCP efficiency                | >28%  | %    |

### 5.5. Band rejection

| FREQUENCY | STANDARD           | TYP* | UNIT |
|-----------|--------------------|------|------|
| 698-798   | LTE700             | 43   | dB   |
| 824-849   | Cellular CDMA      | 43   | dB   |
| 869-894   | GSM850             | 43   | dB   |
| 880-915   | GSM900             | 43   | dB   |
| 1710-1785 | GSM1800/DCS        | 44   | dB   |
| 1850-1910 | GSM1900/PCS        | 46   | dB   |
| 1920-1980 | WCDMA              | 46   | dB   |
| 2400-2492 | WLAN, BT and WiMAX | 50   | dB   |
| 2500-2690 | LTE2600            | 52   | dB   |

Note: Does not include antenna rejection.

### 5.6. Mechanical specifications

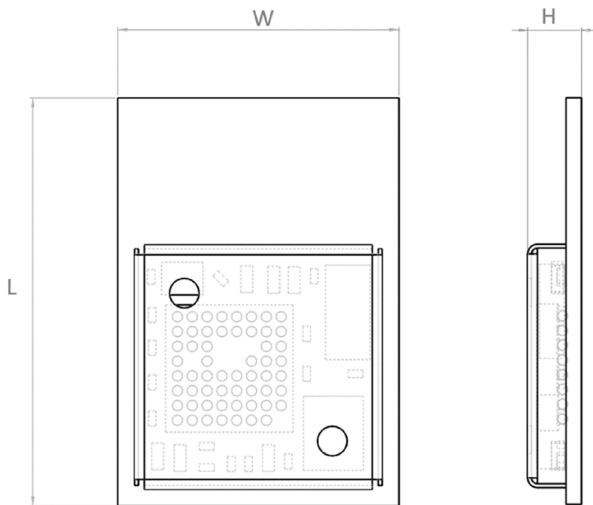
| PARAMETER                              | TYP   | UNIT |
|--|---|------|
| Module exterior dimensions (L x W x H) | 13.8 (±0.1) x 9.5 (±0.1) x 1.8 (+0.2 / - 0.0) | mm   |
| Module support and connection          | Surface mounted (SMD)                         | -    |
| Module mass                            | ≤0.4  | g    |

### 5.7. System specifications

| COMMUNICATION                  | SPECIFICATION                                    |
|--------------------------------|--|
| Data Output Protocol           | NMEA 0183  |
| Host Interfaces                | UART   |
| Default data rate on UART      | 9600 default rate, modify by input command       |
| GPS ENGINE                     |  |
| Chip                           | MTK MT3337-E ROM Chip                            |
| Channels                       | 210 PRN / 66 Acquisition / 22 Tracking           |
| TCXO                           | 0.5ppm   |
| ACCURACY                       |  |
| Horizontal position accuracy   | <2.5m CEP  |
| Maximum position update rate   | 5 Hz (Default 1Hz)                               |
| SENSITIVITY                    |  |
| Acquisition (Cold)             | -148dBm  |
| Acquisition (Hot)              | -163dBm  |
| Tracking                       | -165dBm  |
| TTFF                           |  |
| Hot start                      | <1s  |
| Warm start                     | <25s (typical)                                   |
| Cold start                     | <35s (typical)                                   |
| GENERAL                        |  |
| Maximum altitude               | <18 km   |
| Maximum speed                  | <514 m/s   |
| Active interference cancellers | 12 multi tone active cancellers; ISSCC2011 award |
| Additional features            | 1PPS Sync  |
| EPO / EASY                     | Orbit prediction                                 |

50% CEP, Open-Sky, 24hr Static, -130dBm, good view of the sky

## 6. Module dimension



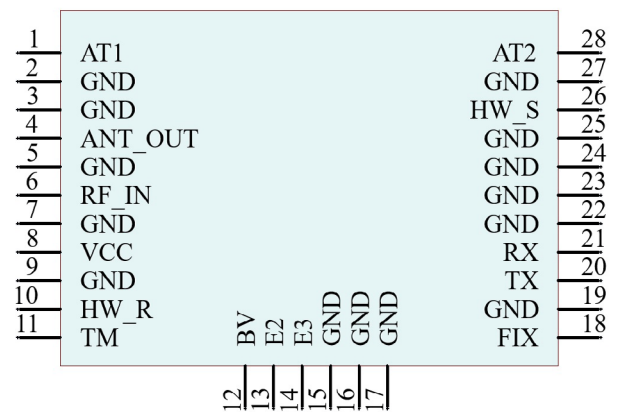
| L         | W        | H               |
|-----------|----------|-----------------|
| Length    | Width    | Height          |
| 13.8 ±0.1 | 9.5 ±0.1 | 1.8 ±0.2 / -0.0 |

*All dimensions in (mm)*

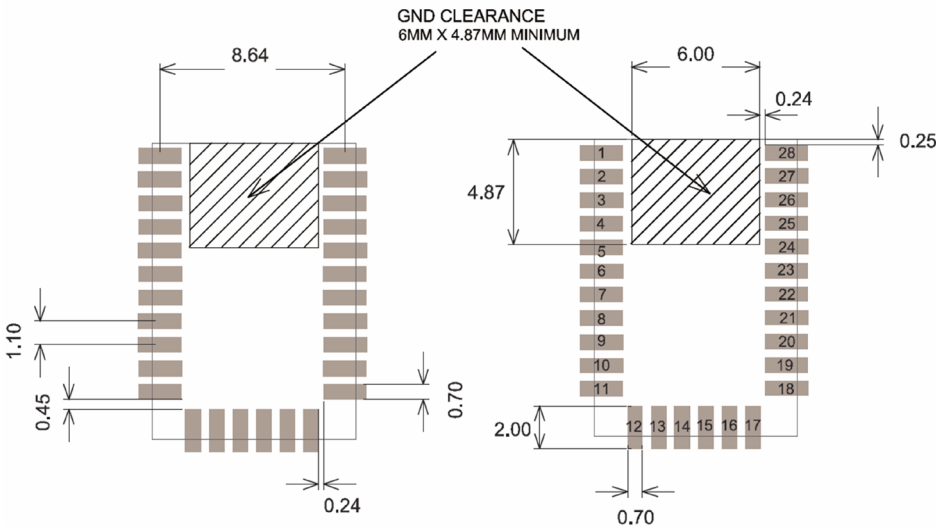
## 7. Schematic symbol and pin definition

| PIN | DESIGNATOR | DESCRIPTION  |
|-----|------------|--|
| 1   | AT1        | Antenna tuing left side  |
| 2   | GND        | Ground connection  |
| 3   | GND        | Ground connection  |
| 4   | ANT_OUT    | RF from internal antenna to external pin<br>(connect to pin 6) |
| 5   | GND        | Ground connection  |
| 6   | RF_IN      | RF input from antenna  |
| 7   | GND        | Ground connection  |
| 8   | VCC        | Main supply voltage, +3.3V in TYP                              |
| 9   | GND        | Ground connection  |
| 10  | HW_R       | System reset, active low                                       |
| 11  | TM         | 1PPS (pulse per second output)                                 |
| 12  | BV         | Backup supply voltage, +3.3V in TYP                            |
| 13  | E2         | Hardware baud select   |
| 14  | E3         | Hardware baud select   |
| 15  | GND        | Ground connection  |
| 16  | GND        | Ground connection  |
| 17  | GND        | Ground connection  |
| 18  | FIX        | Indicates once a GPS fix has been obtained                     |
| 19  | GND        | Ground connection  |
| 20  | TX         | UART Transmit data line  |

| PIN | DESIGNATOR | DESCRIPTION  |
|-----|------------|--|
| 21  | RX         | UART Receive data line                                   |
| 22  | GND        | Ground connection  |
| 23  | GND        | Ground connection  |
| 24  | GND        | Ground connection  |
| 25  | GND        | Ground connection  |
| 26  | HW_S       | Used to enable standby mode. If not used leave floating. |
| 27  | GND        | Ground connection  |
| 28  | AT2        | Antenna tuing right side                                 |



## 8. Host PCB footprint



All dimensions in (mm)

## 9. Module subsystem feature

### 9.1. Host baud rate/protocol selection

The module's default baud rate is user configurable at start-up with a hardware configuration. This is limited to the values in the following table.

The baud rate and output protocol can be changed dynamically after start-up using the relevant commands. Please contact Antenova for more information about protocol messages.

Hardware Baud Rate Selection Table

| BAUD RATE | PIN13 (E2) | PIN14 (E3) |
|-----------|------------|------------|
| 9600      | NC         | NC         |
| 115200    | NC         | PD         |
| 4800      | PD         | NC         |
| 38400     | PD         | PD         |

NC = Not connected. Leave floating  
PD = Pull down resistor to GND (10K  $\Omega$ )



## 9.2. Host interface

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The UART converts bytes of data to and from asynchronous start-stop bit streams as binary electrical impulses. The port contains a 16-byte FIFO, and 256 bytes of URAM. The baud rates are selectable from 4800, 9600, 38400 and 115200 bps.

The IO level from the UART port are CMOS compatible, however for RS232 compatibility the use of external level shifters will be required. The hardware configuration of the port baud rate can be changed dynamically by the use of commands. These will be active and saved as long as the BV supply is applied.

## 9.3. Power supply

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The M20048-1 uses two DC supply inputs. BV to power the RAM and RTC sections of the receiver, and VCC to power the digital and processing sections. BV is to be applied all the time to keep these sections on. VCC can be removed to initiate a backup power save mode (See page 9). BV can be supplied with a battery. The supply is internally regulated for 2.8V meaning the external supply is versatile for a range of voltage levels.

## 9.4. TM (1PPS)

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TM is a 1PPS (pulse per second) output from the receiver providing an accurate synchronized output. The pulse width is 100ms. Enable this function using PMTK255 (Default off)

## 9.5. HRST (Hardware reset pin)

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The External reset pin is default high by an internal 75Kohm and should be left floating if not used. To initiate a reset the pin needs to be pulled low. The module also initiates a reset if the VCC drops below the minimum 2.8V supply.

## 9.6. Power management

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The M20048-1 has two power saving modes.

- Standby mode
- Back up mode

### 9.6.1. Standby mode

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Standby mode is a power saving mode that shuts down the RF section of the module and puts the processor into a standby mode. The RTC is kept alive, and the RAM power is maintained to keep the module configuration.

The standby state can be initiated either with a hardware signal to Pin26 or by using a command.

**Hardware controlled Standby:** Enable standby mode by a low state to Pin26 (HW\_S). To wake the module back to full power a high state needs to be applied to Pin26. If Pin26 is not to be used, then it must be kept floating (not connected).

**Standby mode command:** Software on the host needs to send the "PMTK161" command through the UART interface.

| COMMAND        | M20048-1 STANDBY THEN WAKEUP        | CURRENT CONSUMPTION (TYP) |
|----------------|-------------------------------------|---------------------------|
| \$PMTK161,0*28 | M20048-1 enters standby mode        | <500uA                    |
| Any byte       | M20048-1 wakes up from standby mode |                           |

### 9.6.2. Backup mode

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To enter backup mode the VCC simply needs to be removed. Once initiated the RTC and all configuration is saved along with any ephemeris data to allow quick TTFF once the VCC is re-applied. BV needs to be applied at all times for backup mode to run correctly.

### 9.7. EPO (Extended Prediction Orbit) data service

The EPO allows the use of up to 30-day orbit predictions that can be used to aid the module for an instant fix solution.

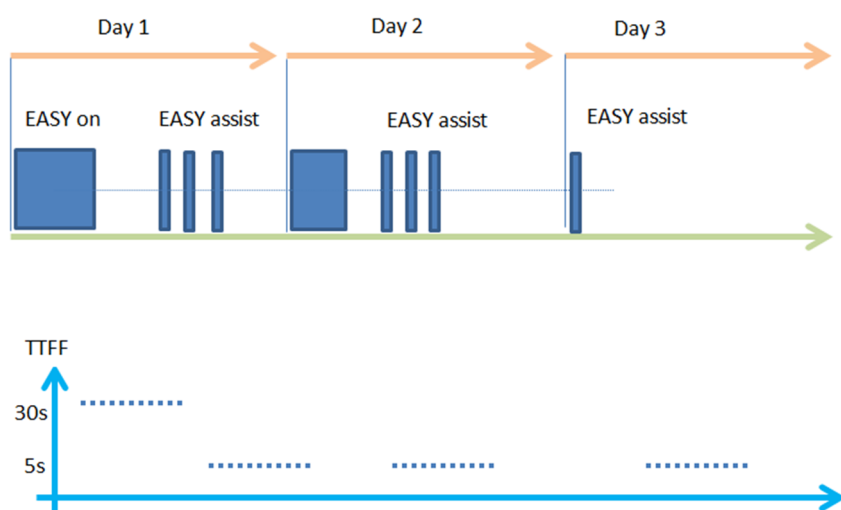
- A proxy server on the customer's side to update EPO files from the MTK server daily.
- Application software to access the proxy server through the internet (optional if host device can access internet).
- Software on host device to send EPO data to M20048-1 module to allow instant fix by using EE data. Please contact Antenova for more information. Requires permission from MTK to use service.

### 9.8. EASY (Self-Generated Orbit Prediction)

The module supports EASY™ (Embedded Assisted System) is a Self-Generated Orbit Prediction feature. It provides up to 3 days GPS orbit prediction ability without the need for any host CPU porting or internet connection requirements.

EASY works as embedded software which accelerates TTFF by predicting satellite navigation messages from received ephemeris. EASY is a fully automated receiver task that is efficiently scheduled and computed in free time of every second after a GNSS navigation solution.

EASY is default off and can be enabled by a PMTK command.



### 9.9. AIC (Active Interference Cancellation)

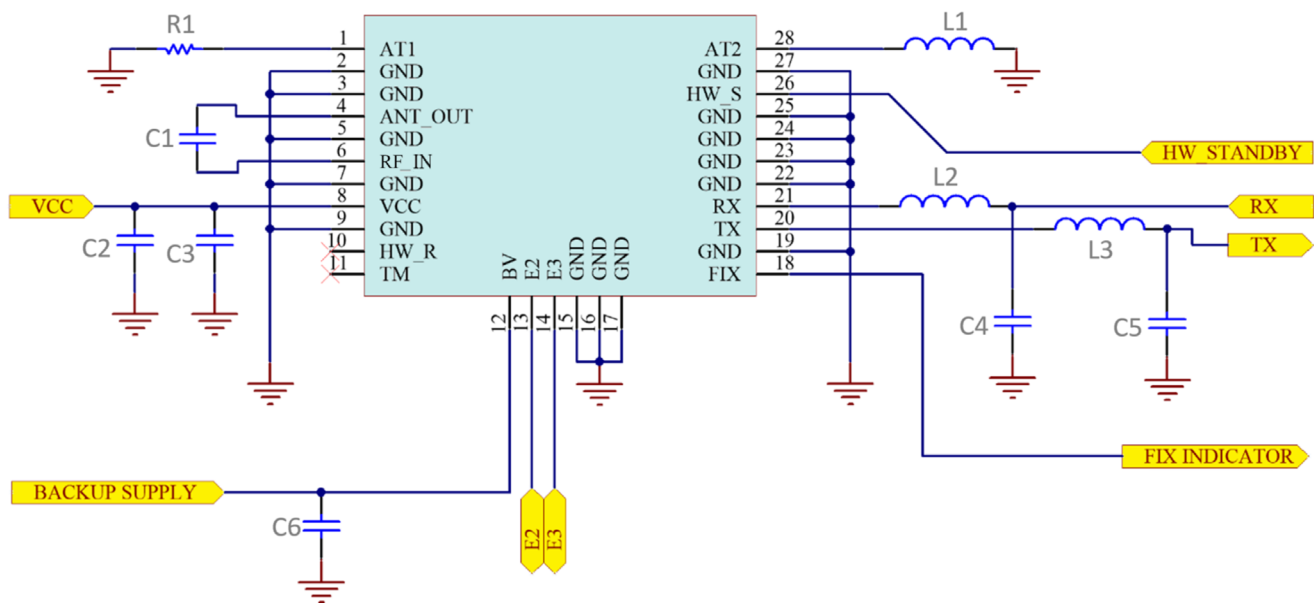
The AIC feature provides effective narrow-band interference cancellation. The GPS signal can be recovered from signals that contain interference signals. High speed processors generate clock harmonics that cause interference to be present within the GPS band.

## 10. Module application

### 10.1. Reference schematic

The circuit below shows a basic design for use with the UART interface and configuring the default baud rate to 9600. (NC on E2 and E3)

*Baud Rate = 9600 (Default)*



#### Bill of Material

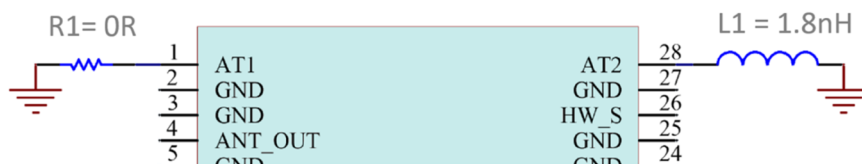
| DESIGNATOR         | VALUE           | DESCRIPTION/COMMENTS                             | QUANTITY |
|--------------------|-----------------|--|----------|
| C1, C2, C4, C5, C6 | 22pF capacitor  | Decoupling cap. Place close to corresponding pin | 5        |
| C3                 | 2.2uF capacitor | Decoupling cap. Place close to corresponding pin | 1        |
| L1                 | 1.8nH           | Tuning inductor for antenna                      | 1        |
| L2, L3             | 47nH inductor   | Filter component                                 | 2        |
| R1                 | 0 ohm           | Tuning resistor for antenna                      | 1        |

## 10.2. External matching

The M20048-1 module uses external matching circuit components on the host PCB to fine-tune the onboard antenna to each specific application. This “external matching” allows compensating for the detuning of the antenna caused by various components that can be close to the M20048-1 module in the actual application (plastic case, battery, speakers, etc).

The external matching must be placed on the host PCB as close as possible to the module pads AT1(Pad1) and AT2 (Pad28), respectively. A single matching component for each pin is required. A perfect GND plane connecting for each matching component is needed. For multi-layer PCB, an individual GND via should be placed close to the matching component as possible.

**Schematic:** In the example above AT1 uses a 0ohm resistor and AT2 uses a 1.8nH inductor to tune the antenna. Both components should be designed in as they may be required for the intended device.



### Type of Matching Components:

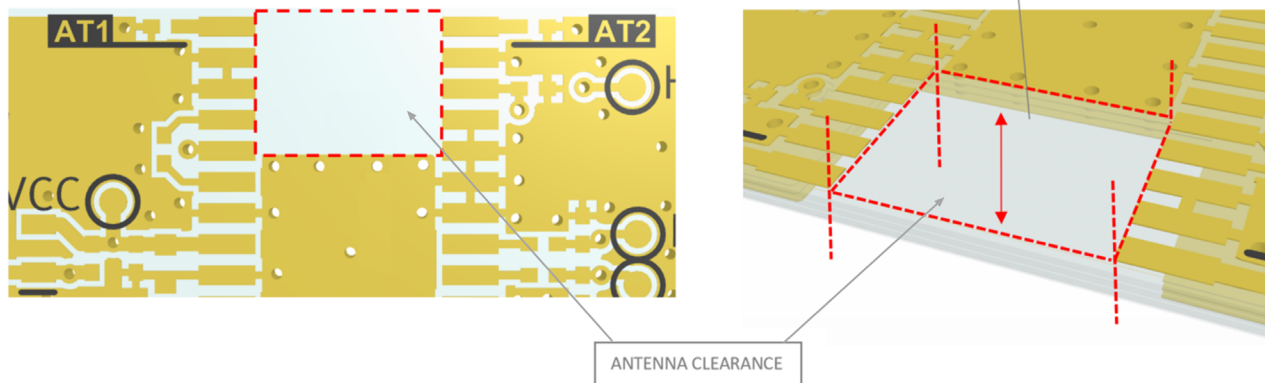
- Capacitors: Use 0402, COG components
- Inductors:
  - High-Q, wire wound inductors in 0402 sizes are recommended for maximum performance, e.g. Murata LQW15 series.
  - Good quality multi-layer type inductors (e.g. Murata LQG15 series) can also be used as a lower cost alternative.

**External Antenna Support:** A low-cost external circuit can be used to provide external antenna support. Please contact Antenova for more information, and example circuit.

### 10.3. Internal antenna clearance

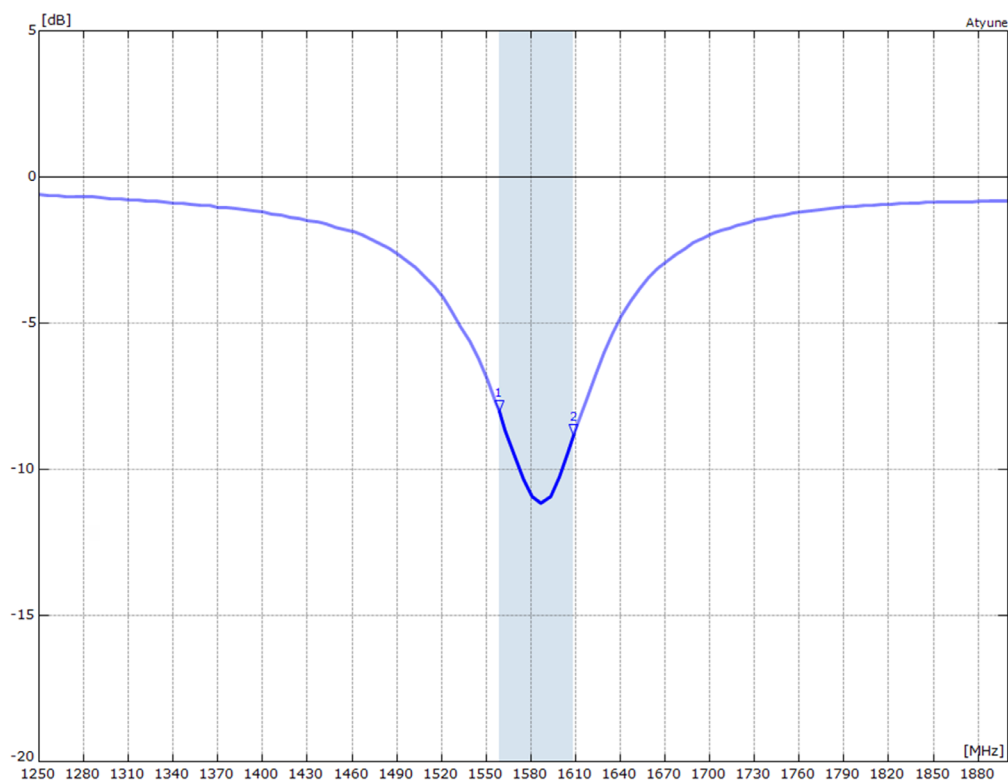
The M20048-1 module internal antenna requires a clearance on the host PCB to operate. The clearance means that no ground or tracks of any kind can be placed within this area. This clearance area also needs to be applied through the entire PCB stack up. The minimum area needed for clearance is 6mm x 4.87mm.

Top view of M20048-1 host PCB footprint



### 10.4. Typical antenna matching results

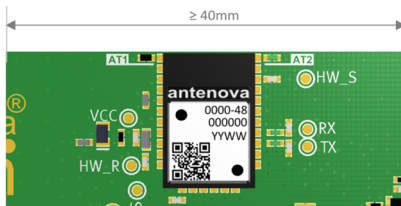
Typical antenna matching as seen by ANT\_IN (Pin6) is shown in the following plot. The matching bandwidth at -7dB is typically 65MHz. Measured on M20048-EVB-1 test board.



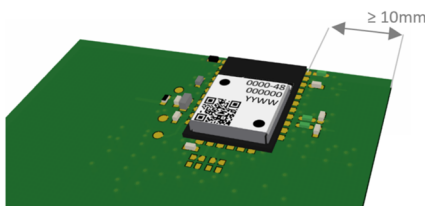
### 10.5. Module placement guideline

For the internal antenna, care must be taken when defining the placement of the module on the host PCB. Here are some guidelines that should be used when deciding the position of the module.

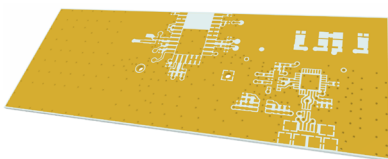
- The module top edge must be placed almost level with the edge of the host PCB.
- The edge of the host PCB that the module is to be placed at must be a minimum of 40mm in length.



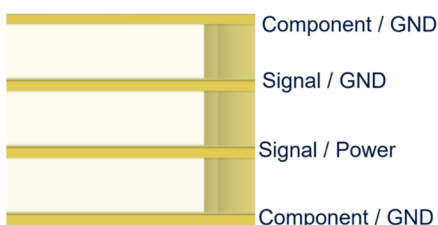
- The central placement of the module is advised. However, an offset placement is also possible.
- For an offset closer to the PCB edge to the right side of the module, a minimum of 10mm distance is required to the edge of the host PCB.



- The antenna uses the host PCB ground to effectively radiate. As such, a GND plane must be placed on the host PCB on at least one layer.
- In the example below, the only area void of GND is the antenna keep-out area. The solder mask is removed to make the copper visible.



- An ideal stack-up for a host PCB would be to use the top and bottom layers as GND planes, while using the internal layers for any signal and power planes. This not only helps the GPS antenna to perform effectively, but also helps to reduce any potential noise issues that can be associated with mixed signal PCB's.
- An example below shows a 4-layer host PCB. For GND layer, all available space should not be used for singal trace routing or component placement.

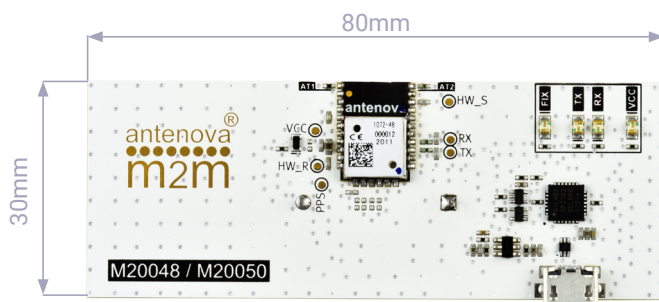


## 11. Evaluation kit

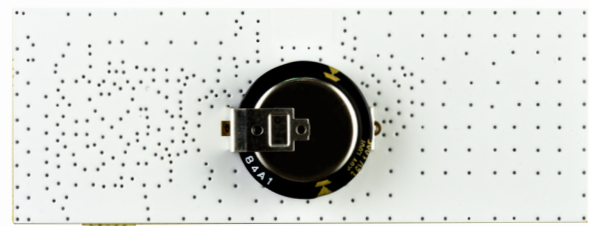
The EVK is a single PCB that contains the M20048-1 module and required components to run on a PC via a USB cable and Antenova software.

Evaluation kits are available on request. Please contact Antenova for more information. (Part number: M20048-EVB-1).

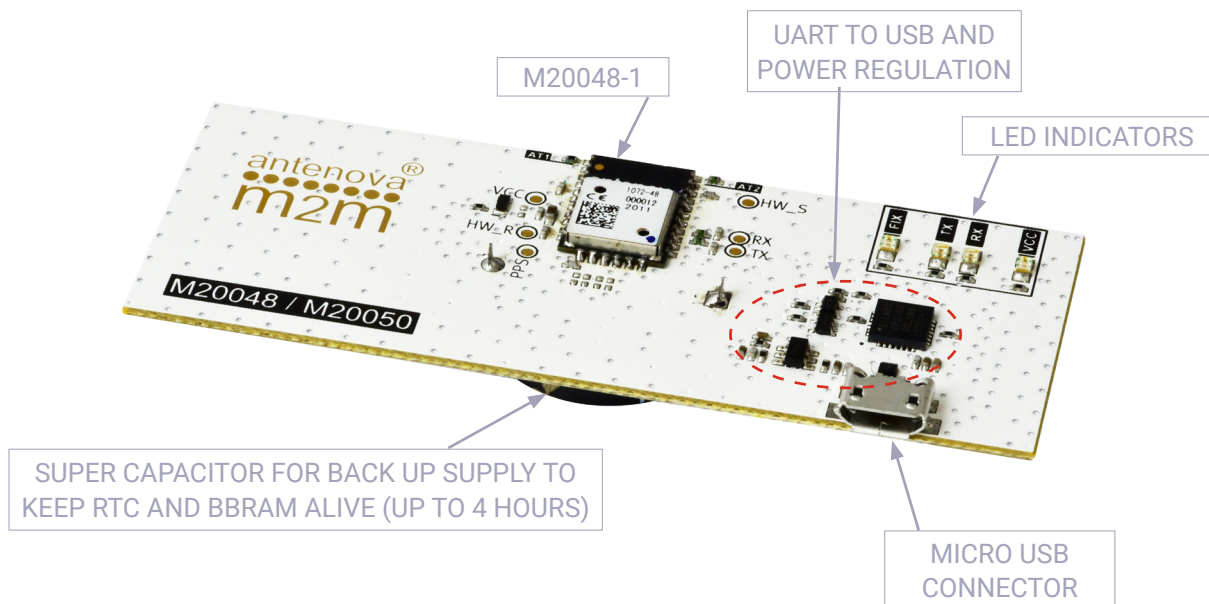
To order a reference board  
please see [antenova.com](http://antenova.com)



Front view



Bottom view





## 12. Reflow soldering

### 12.1. Placement

Typical placement systems used for any BGA/LGA package are acceptable.  
Recommended nozzle diameter for placement: 5mm

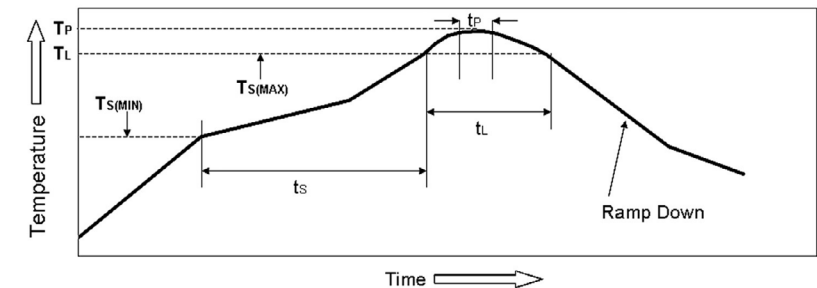
### 12.2. Soldering paste

Use of “No Clean” soldering paste is strongly recommended, as it does not require cleaning after the soldering process has taken place. An example of suitable soldering paste is Alpha OM350.

### 12.3. Soldering

The recommended soldering profile for M20048-1 is shown below. However, it is the responsibility of the Contract Manufacturer to determine the exact reflow profile used, taking into consideration the parameters of the host PCB, solder paste used, etc.

| PROFILE FEATURE                                       |                                  | PB-FREE SOLDER |
|---|----------------------------------|----------------|
| Pre-Heat  | Temperature ( $T_s$ ) Min        | 130°C          |
|   | Temperature ( $T_s$ ) Max        | 220°C          |
|   | Time ( $t_s$ )                   | <150s          |
| Reflow  | Liquidus Temperature - ( $T_l$ ) | 220°C          |
|   | Time ( $t_l$ )                   | 45-90s         |
| Peak Package Body Temperature ( $T_p$ )               |                                  | 245°C          |
| Time within 5°C of peak temp ( $t_p$ )                |                                  | 30s            |
| Average Ramp up rate - $T_s(\text{max})$ to ( $T_p$ ) |                                  | 3°C/s          |
| Ramp Down Rate  |                                  | 6°C/s max      |



The Pb Free Process-Package Peak Reflow Temperature is 260°C.  
Exceeding the maximum soldering temperature could permanently damage the module.

#### 12.4. Multiple soldering

The M20048-1 module can be submitted up to 2 reflow soldering processes.

Upside-down soldering is acceptable but it is recommended that the Contract Manufacturer qualify the process before mass production. The second reflow must take place within the recommended floor life limit (MSL3). Please contact Antenova for further information.

#### 12.5. Hand soldering

Hand-soldering and rework of the M20048-1 module is acceptable, however care must be taken to avoid short circuits due to the small size of the module pads.

### 13. Quality and environmental specifications

| TEST                        | STANDARD  | PARAMETERS   |
|-----------------------------|---|--|
| PCB inspection              | IPC-6012B, Class 2. Qualification and Performance Specification for Rigid Printed Boards - Jan 2007 |  |
| Assembly inspection         | IPC-A-610-D, Class 2 "Acceptability of electronic assemblies"                                       |  |
| Temperature range           | ETSI EN 300 019-2-7 specification T 7.3   | -30 °C, +25 °C, +85 °C, operating  |
| Moisture/Reflow sensitivity | IPC/JEDEC J-STD-020D.1  | MSL3   |
| Storage (Dry Pack)          | IPC/JEDEC J-STD-033C  | MSL3   |
| Solderability               | EN/IEC 60068-2-58 Test Td   | More than 90% of the electrode should be covered by solder. Solder temperature 245 °C ± 5 °C |

Moisture sensitivity:

Antenova ships all devices dry packed in tape on reel with desiccant and moisture level indicator sealed in an airtight package. If on receiving the goods the moisture indicator is pink in colour or a puncture of the airtight seal packaging is observed, then follow J-STD-033 "Handling and Use of Moisture/Reflow Sensitive Surface Mount Devices".

## 14. Hazardous material regulation conformance

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The RF antenna module has been tested to conform to RoHS and REACH requirements. A certificate of conformance is available from Antenova's website.

## 15. Packaging

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### 15.1. Optimal storage conditions

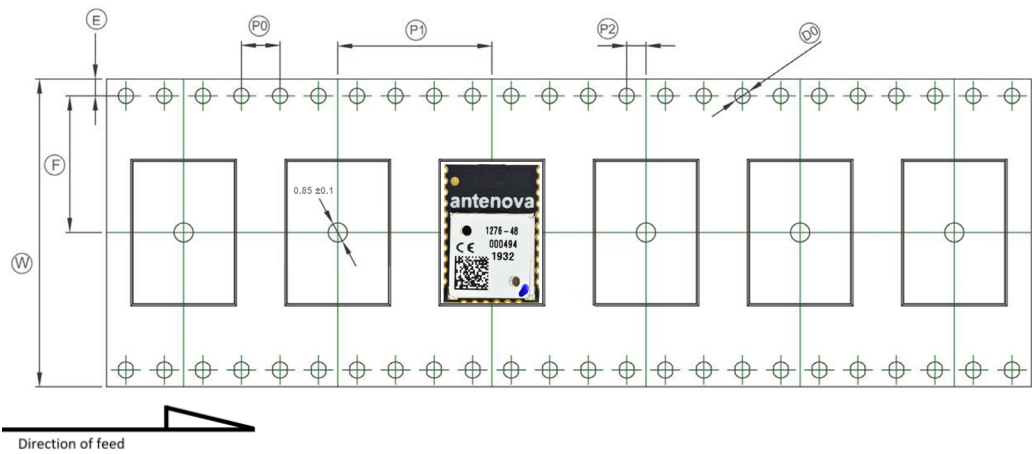
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|               |   |
|---------------|---|
| TEMPERATURE   | -10°C to 40°C   |
| HUMIDITY      | Less than 75% RH  |
| SHELF LIFE    | 24 Months   |
| STORAGE PLACE | Away from corrosive gas and direct sunlight                                 |
| PACKAGING     | Reels should be stored in unopened sealed manufacturer's plastic packaging. |
| MSL LEVEL     | 3   |

Note: The M20048-1 modules meet MSL Level 3 of the JEDEC specification J-STD-020D - 168 hours Floor Life (out of bag)  $\leq 30^{\circ}\text{C}/60\% \text{ RH}$ . If the stated floor life expires prior to reflow process then follow J-STD-033 "Handling and Use of Moisture/Reflow Sensitive Surface Mount Devices".

The shelf life of the antenna is 2 years provided the factory seal on the package has not been broken.

15.2. Tape characteristics



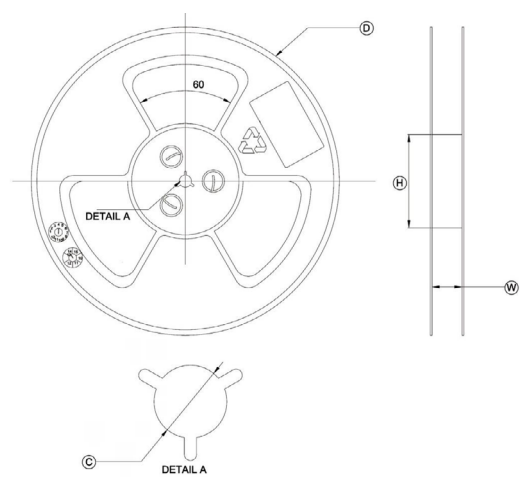
| P0         | P1          | P2         | D0         |
|------------|-------------|------------|------------|
| 4.00 ± 0.1 | 16.00 ± 0.1 | 2.00 ± 0.1 | 1.55 ± 0.1 |

| E          | F           | W           |
|------------|-------------|-------------|
| 1.75 ± 0.1 | 14.20 ± 0.1 | 32.00 ± 0.3 |

All dimensions in (mm)

| QUANTITY        | LEADING SPACE           | TRAILING SPACE          |
|-----------------|-------------------------|-------------------------|
| 1000 pcs / reel | 50 blank module holders | 50 blank module holders |

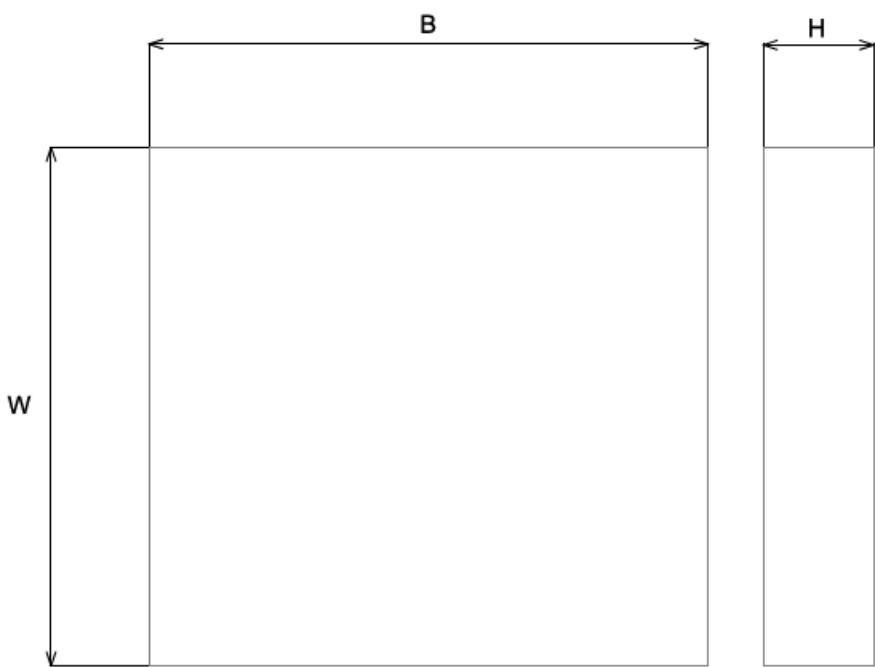
15.3. Reel dimensions



| D           | C          | H           | W          |
|-------------|------------|-------------|------------|
| 330.0 ± 2.0 | 13.3 ± 0.3 | 100.0 ± 3.0 | 32.4 ± 0.2 |

All dimensions in (mm)

15.4. Box dimensions




| WIDTH (W) | BREADTH (B) | HEIGHT (H) |
|-----------|-------------|------------|
| 340mm     | 339mm       | 53mm       |

### 15.5. Bag properties

Reels are supplied in protective plastic packaging.

### 15.6. Reel label information

|  |   |
|--|---|
| <p><b>antenova® Antenova Limited</b></p> <p>Description: GPS Module</p> <p>Part Number: M20048-1</p> <p>Qty: 1000pcs</p> <p>Date Code: YYWW</p> <p>Package Date: YYWW</p> <p><b>CE</b></p> <p>Antenova Limited, 2nd Floor, Titan Court,<br/>3 Bishop Square, Hatfield, Herts, UK</p> <p>sales@antenova.com / www.antenova.com</p> <p><b>RADIONOVA®</b></p> | <div style="display: flex; justify-content: space-between; align-items: flex-start;"> <div style="text-align: center;">  <p><b>Caution</b></p> <p>This Bag Contains<br/>MOISTURE SENSITIVE DEVICES</p> </div> <div style="border: 1px solid black; padding: 5px; text-align: center;"> <p>LEVEL</p> <p><b>3</b></p> </div> </div> <ol style="list-style-type: none"> <li>1. Calculated shelf life in sealed bag: 12months at &lt;40°C and &lt;90% relative humidity (RH).</li> <li>2. After bag is opened, devices that will be subjected to reflow solder or other high temperature process must:             <ol style="list-style-type: none"> <li>a) Mounted within: 168 hours of factory conditions &lt;30°C/60% RH, or</li> <li>b) Stored per J-STD-033</li> </ol> </li> <li>3. Devices required bake, before mounting if:             <ol style="list-style-type: none"> <li>a) Humidity indicator card reads &gt;10% when read at 23±5°C</li> <li>b) 2a or 2b not met</li> </ol> </li> <li>4. If baking is required, refer to IPC/JEDEC J-STD-033 for bake procedure.<br/>Note: Level and body temperature defined by IPC/JEDEC J-STD-020</li> </ol> |
|--|---|

### Quality statements

Antenova's products conform to REACH and RoHS legislation. For our statements regarding these and other quality standards, please see [antenova.com](http://antenova.com).



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### Datasheet version

2.01 released Aug 17th 2021

2.02 released May 10th 2022

# Antenna design, integration and test resources

Product designers – the details contained in this datasheet will help you to complete your embedded antenna design. Please follow our technical advice carefully to obtain optimum antenna performance.

We aim to support our customers to create high performance wireless products. You will find a wealth of design resources, calculators and case studies to aid your design on our website.

Antenova's design laboratories are equipped with the latest antenna design tools and test chambers. We provide antenna design, test and technical integration services to help you complete your design and obtain the required certifications.

If you cannot find the antenna you require in our product range, please contact us to discuss creating a custom antenna to meet your exact requirements.

Share knowledge with RF experts around the world.

**ask.antenova** is a global forum for designers and engineers working with wireless technology.

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