

8 to 12 GHz 20dB Directional Coupler

FPC06302



DESCRIPTION

DLI brand directional couplers utilize DLI's high dielectric ceramic materials to deliver small size and minimal performance variation over temperature. The components are well matched for monitoring incident and reflected power.

FEATURES

- Small Size
- Solder Surface Mount Package
- Moisture Sensitivity Level: MSL1
- Frequency Stable over Temperature
- Operating & Storage Temp: -55°C to +125°C
- Characteristic Impedance: 50Ω

Packaging and Ordering Information: To request Tape and Reel packaging, please order part number FPC06302-T, see additional data on page 5.



SPECIFICATIONS*

Parameter	Frequency (GHz)	Min	Max		
Passband Insertion Loss* (dB)			0.3		
Passband Return Loss (dB)		15			
Coupling (dB)	8 - 12	19	22		
Directivity (dB)		15			
CW Input Power** (W)			25		
$\theta_{JC} \left(\frac{^{\circ}C}{W}\right)$	3				
Size (L x W x H)	0.120 x 0.080 x 0.015 in 3.048 x 2.032 x 0.381mm				

*Electrical specifications based on typical mounted performance at room temperature. Insertion loss shall vary ±0.5dB over temperature.

** Power rating assumes the component will be mounted to a PCB with good thermally conducting ground vias as outlined in the recommended PCB layout that are connected to an adequate heat sink. Max power is based on 125°C base temperature.

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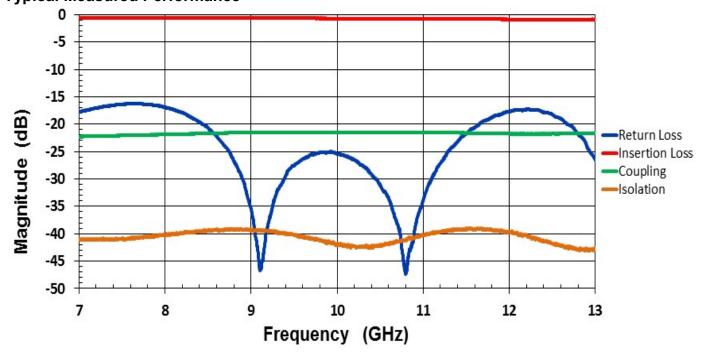




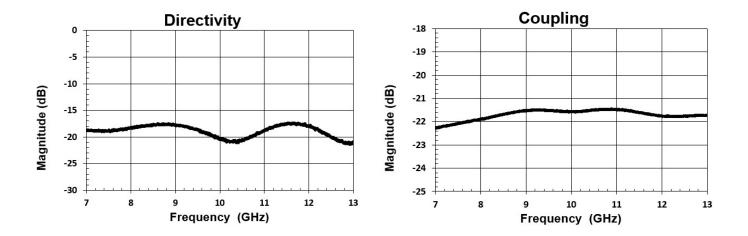
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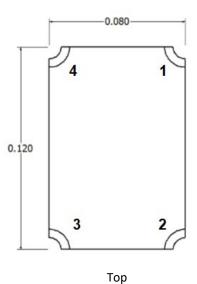


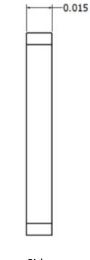
*Typical de-embedded measured performance mounted on a connectorized test fixture. DEB is 0.010in RO4350B with 50.00hm CPW ground traces going into the ports at room temperature.

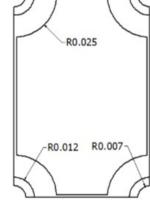


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Bottom

- Notes :
- 1. Termination Finish:

ENIG: 3 - 6 µinch Au over 50 µinch Ni

2. Maximum Assembly Process Temperature: 250°C

Tolerances:

For values with 3 decimal places ±0.001

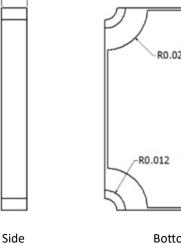
For values with 4 decimal places ±0.0005

FPC06302 Coupler Port Configuration						
	Port 1	Port 2	Port 2 Port 3			
Configuration 1	Input	Output	Isolated	Coupled		
Configuration 2	Output	Input	Coupled	Isolated		
Configuration 3	Coupled	Isolated	Output	Input		
Configuration 4	Isolated	Coupled	Input	Output		

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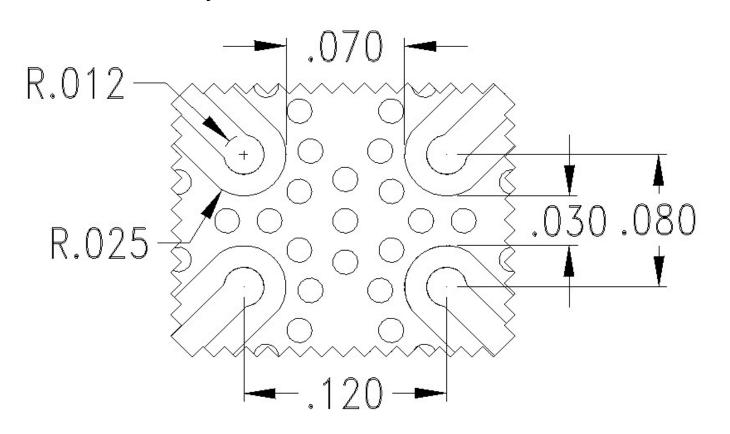
Units = Inches





Recommended PCB Layout

FPC06302



Note:

- 50Ω trace dimensions are application specific.
- Ensure adequate grounding beneath the part.
- Trace feed locations can be horizontal, vertical or angled.

For further details and best practices, reference the **Microwave Products Guide**, available at: https://www.knowlescapacitors.com/Support/Catalogs

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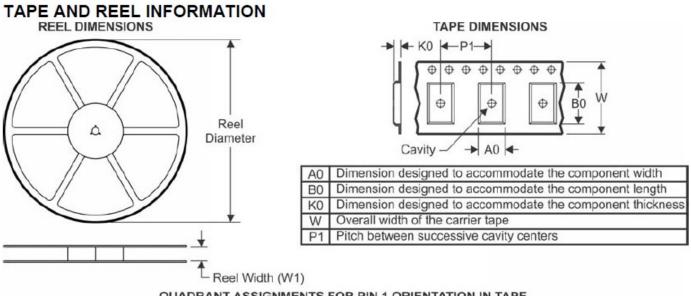


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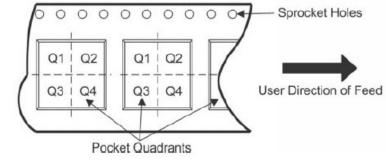
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QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



*All dimensions are nominal

Device	Package Type	Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
FPC06302-T	SMD	180	8.4	1.3	1.76	0.5	4	8	Q1

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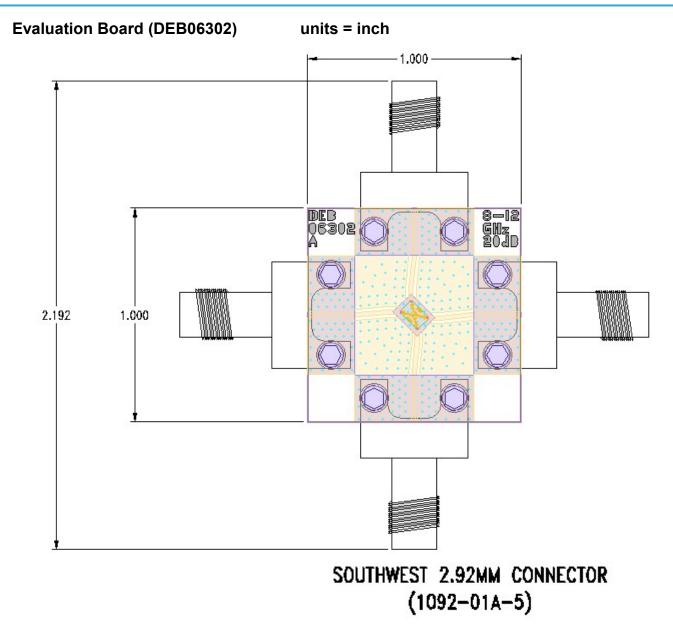




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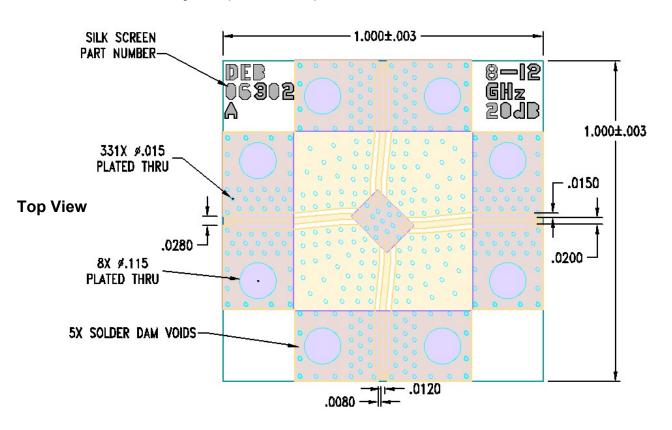


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Evaluation Board Layout (DEB06302)

units = inch



The CPWG traces going into the ports have been tapered at the board edge to accommodate capacitive loading of the Southwest connector's center pin



NOTES:

 MATERIAL: R04350B THICKNESS: .0100±.001 0.5 0Z/0.5 0Z COPPER, PLATE UP
METALLIZATION: (TOP AND BOTTOM) 3-6 µINCHES AU OVER 100 µINCHES MIN. Ni

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