WP7113P3C

Phototransistor

DESCRIPTION



PACKAGE DIMENSIONS

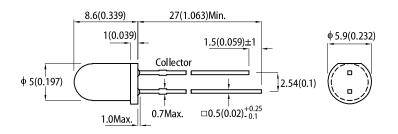
· Made with NPN silicon phototransistor chips

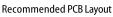
FEATURES

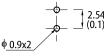
- · Mechanically and spectrally matched to the infrared emitting LED lamp
- Package matched with IR emitter WP7113F3C
- · Water clear lens
- RoHS compliant

APPLICATIONS

- · Infrared applied systems
- Optoelectronic switches
- · Photodetector control circuits
- Sensor technology







Notes

- Notes: 1. All dimensions are in millimeters (inches). 2. Tolerance is $\pm 0.25(0.01'')$ unless otherwise noted. 3. Lead spacing is measured where the leads emerge from the package. 4. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

ABSOLUTE MAXIMUM RATINGS at T_A=25°C

Parameter	Max.Ratings	Units
Collector-to-Emitter Voltage	30	V
Emitter-to-Collector Voltage	5	V
Power Dissipation at (or below) 25°C Free Air Temperature	100	mW
Operating Temperature	-40 to +85	°C
Storage Temperature	-40 to +85	°C
Lead Soldering Temperature(>5mm for 5sec)	260	°C

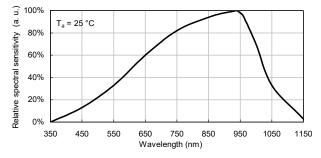
Note: 1. Relative humidity levels maintained between 40% and 60% in production area are recommended to avoid the build-up of static electricity – Ref JEDEC/JESD625-A and JEDEC/J-STD-033.

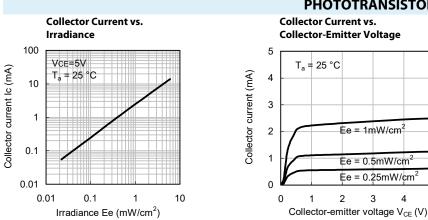
ELECTRICAL / OPTICAL CHARACTERISTICS at T_A=25°C

Parameter	Symbol	Min.	Тур.	Max.	Units	Test Conditions
Collector-to-Emitter Breakdown Voltage	V _{BR CEO}	30	-	-	V	$I_{\rm C}$ = 100µA E _e = 0mW/cm ²
Emitter-to-Collector Breakdown Voltage	V _{BR ECO}	5	-	-	V	l _E = 100μA E _e = 0mW/cm ²
Collector-to-Emitter Saturation Voltage	V _{CE (SAT)}	-	-	0.8	V	$I_{C} = 2mA$ $E_{e} = 20mW/cm^{2}$
Collector Dark Current	I _{CEO}	-	-	100	nA	V_{CE} = 10V E _e = 0mW/cm ²
Rise Time(10% to 90%)	tr	-	15	-	μS	V _{CE} = 5V I _C = 1mA R _L = 1000Ω
Fall Time(90% to 10%)	t _f	-	15	-	μS	
On State Collector Current	I _(ON)	0.5	2.5	-	mA	$V_{CE} = 5V$ $E_{e} = 1mW/cm^{2}$ $\lambda = 940nm$
Range of Spectral Bandwidth	λ _{0.1}	420	-	1120	nm	-
Wavelength of Peak Sensitivity	λ_{p}	-	940	-	nm	-

TECHNICAL DATA

RELATIVE SPECTRAL SENSITIVITY vs. WAVELENGTH

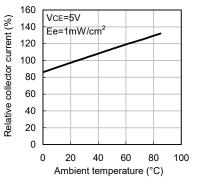




PHOTOTRANSISTOR

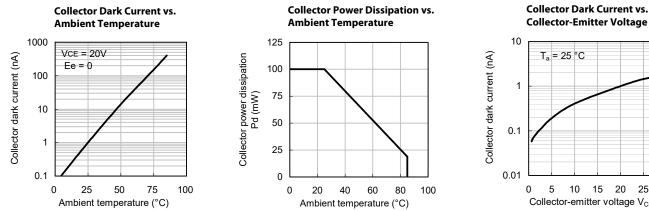
5

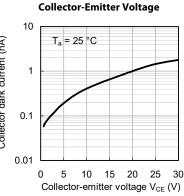
Relative Collector Current vs. Ambient Temperature



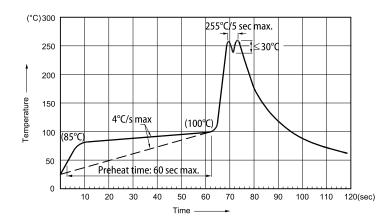
TECHNICAL DATA

PHOTOTRANSISTOR





RECOMMENDED WAVE SOLDERING PROFILE



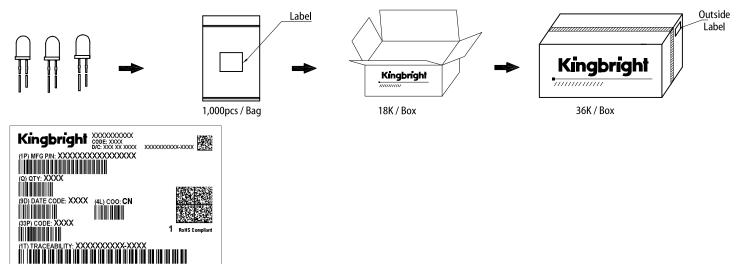
Notes:

- 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C.

- competature or 200 C
 2. Peak wave soldering temperature between 245°C ~ 255°C for 3 sec (5 sec max).
 3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
 4. Fixtures should not incur stress on the component when mounting and during soldering process.
 5. SAC 305 solder alloy is recommended.
 6. No more than one wave soldering pass.

PACKING & LABEL SPECIFICATIONS

(SP) XXXXXXXXXX



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PCF

PRECAUTIONS

Storage Conditions

- 1. Avoid continued exposure to the condensing moisture environment and keep the product away from rapid transitions in ambient temperature.
- 2. The LEDs should be stored at temperature <30°C and relative humidity <70%. If the packaging is opened but not used within three months, the unused LEDs should be stored in a sealed container with nitrogen atmosphere and moisture absorbent material.

LEC

LED Mounting Method

1. The lead pitch of the LED must match the pitch of the mounting holes on the PCB during component placement. Lead-forming may be required to insure the lead pitch matches the hole pitch. Refer to the figure below for proper lead forming procedures. Note 1-3: Do not route PCB trace in the contact area between the leadframe and the PCB to prevent short-circuits.

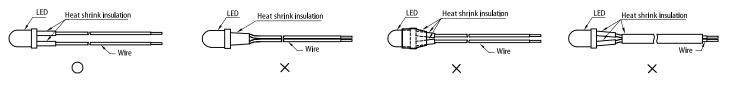
Note.1 Note.2 Note.3 45°Max. 45 \bigcirc \cap LED LED LED LED PCB X × X Х X \times

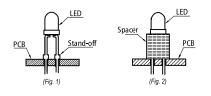
LED

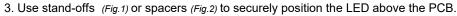
 \bigcirc " Correct mounting method " X " Incorrect mounting method

LED

2. When soldering wires to the LED, each wire joint should be separately insulated with heat-shrink tube to prevent short-circuit contact. Do not bundle both wires in one heat shrink tube to avoid pinching the LED leads. Pinching stress on the LED leads may damage the internal structures and cause failure.



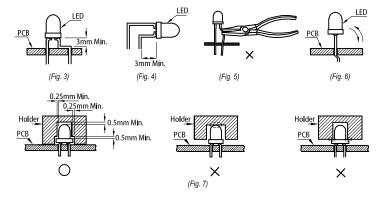




- Maintain a minimum of 3mm clearance between the base of the LED lens and the first lead bend (Fig. 3 ,Fig. 4).
- 5. During lead forming, use tools or jigs to hold the leads securely so that the bending force will not be transmitted to the LED lens and its internal structures. Do not perform lead forming once the component has been mounted onto the PCB. (Fig. 5)

Lead Forming Procedures

- 1. Do not bend the leads more than twice. (Fig. 6)
- 2. During soldering, component covers and holders should leave clearance to avoid placing damaging stress on the LED during soldering. (Fig. 7)
- 3. The tip of the soldering iron should never touch the lens epoxy.
- 4. Through-hole LEDs are incompatible with reflow soldering.
- 5. If the LED will undergo multiple soldering passes or face other processes where the part may be subjected to intense heat, please check with Kingbright for compatibility.



PRECAUTIONARY NOTES

- The information included in this document reflects representative usage scenarios and is intended for technical reference only.
- The part number, type, and specifications mentioned in this document are subject to future change and improvement without notice. Before production usage customer should refer 2 to the latest datasheet for the updated specifications.
- 3 When using the products referenced in this document, please make sure the product is being operated within the environmental and electrical limits specified in the datasheet. If customer usage exceeds the specified limits, Kingbright will not be responsible for any subsequent issues.
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