

Selection Guide

Part No.	Dice	Lens Type	Iv (ucd) [1] @ 10mA		Description
			Min.	Typ.	
PSC23-11YWA	Yellow (GaAsP/GaP)	White Diffused	3600	7000	Common Cathode, Rt. Hand Decimal.
			*900	*2200	

Note:

1.Luminous intensity/ luminous Flux: +/-15%.

*Luminous intensity value is traceable to the CIE127-2007 compliant national standards.

Electrical / Optical Characteristics at TA=25°C

Symbol	Parameter		Device	Typ.	Max.	Units	Test Conditions
λ peak	Peak Wavelength		Yellow	590		nm	If=20mA
λ D[1]	Dominant Wavelength		Yellow	588		nm	If=20mA
$\Delta\lambda$ 1/2	Spectral Line Half-width		Yellow	35		nm	If=20mA
C	Capacitance		Yellow	20		pF	Vf=0V;f=1MHz
Vf[2]	Forward Voltage	A1,A2,D1,D2,P,K	Yellow	4.2	5.0	V	If=20mA
		B,C,E,F,G,H,J,L,M,N					
		DP		2.1	2.5		
IR	Reverse Current (Per chip)	A1,A2,D1,D2,P,K	Yellow		10	uA	VR = 5V VR = 5V VR = 5V
		B,C,E,F,G,H,J,L,M,N			20		
		DP			10		

Notes:

1.Wavelength: +/-1nm.

2. Forward Voltage: +/-0.1V.

3. Wavelength value is traceable to the CIE127-2007 compliant national standards.

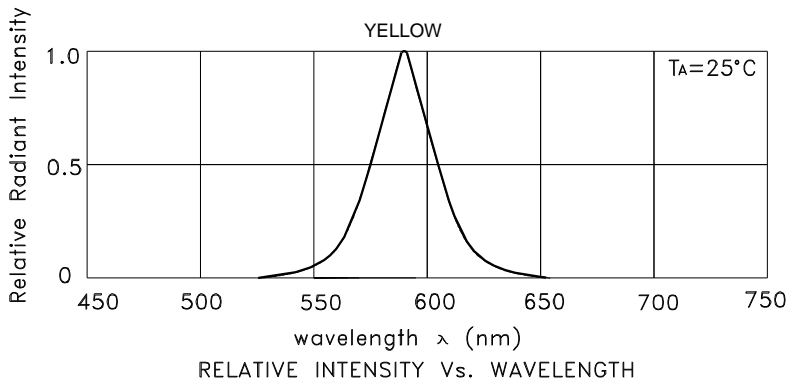
Absolute Maximum Ratings at TA=25°C

Parameter		Yellow	Units
Power dissipation	A1,A2,D1,D2,P,K	150 300 75	mW
	B,C,E,F,G,H,J,L,M,N		
	DP		
DC Forward Current	A1,A2,D1,D2,P,K	30 60 30	mA
	B,C,E,F,G,H,J,L,M,N		
	DP		
Peak Forward Current [1]	A1,A2,D1,D2,P,K	140 280 140	mA
	B,C,E,F,G,H,J,L,M,N		
	DP		
Reverse Voltage (Per chip)	A1,A2,D1,D2,P,K	5 5 5	V
	B,C,E,F,G,H,J,L,M,N		
	DP		
Operating / Storage Temperature		-40°C To +85°C	
Lead Solder Temperature [2]		260°C For 3-5 Seconds	

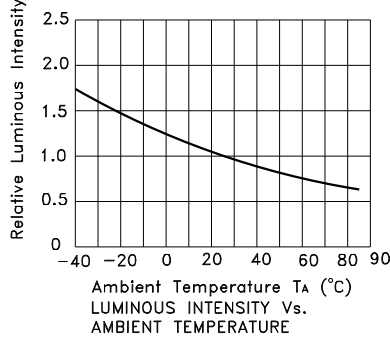
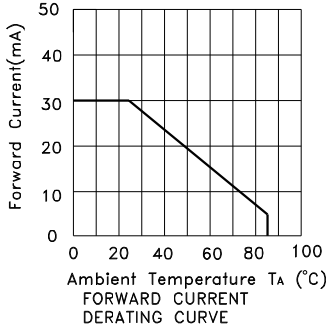
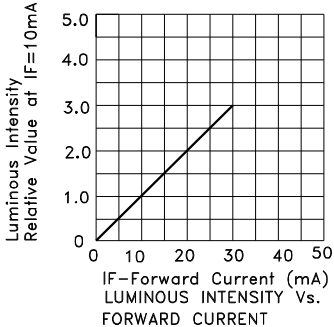
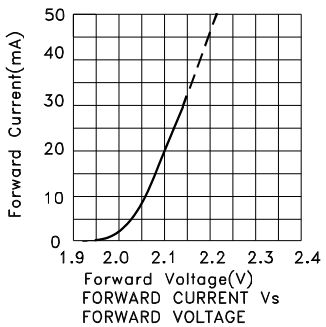
Notes:

1. 1/10 Duty Cycle, 0.1ms Pulse Width.

2. 2mm below package base.

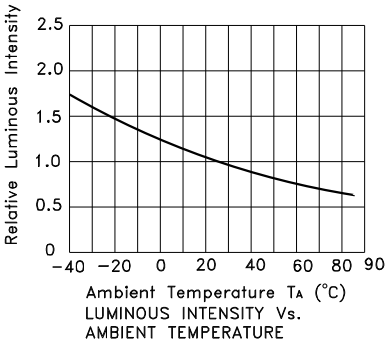
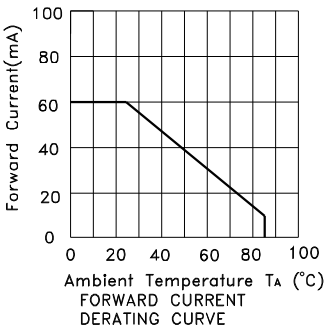
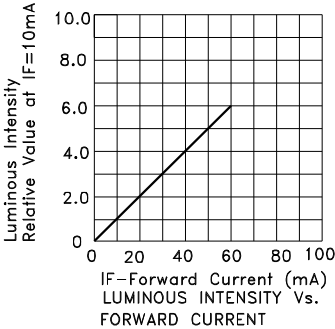
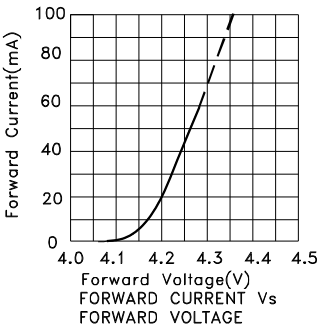


Yellow PSC23-11YWA



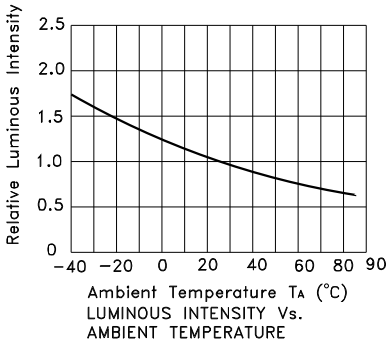
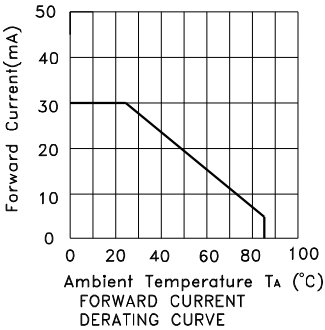
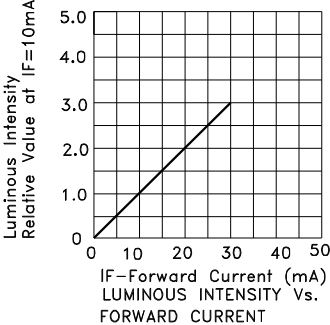
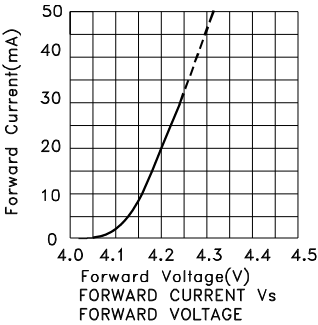
Note:the curves are on the DP.

Yellow



Note:the curves are on the segment b,c,e,f,g,h,j,l,m,n.

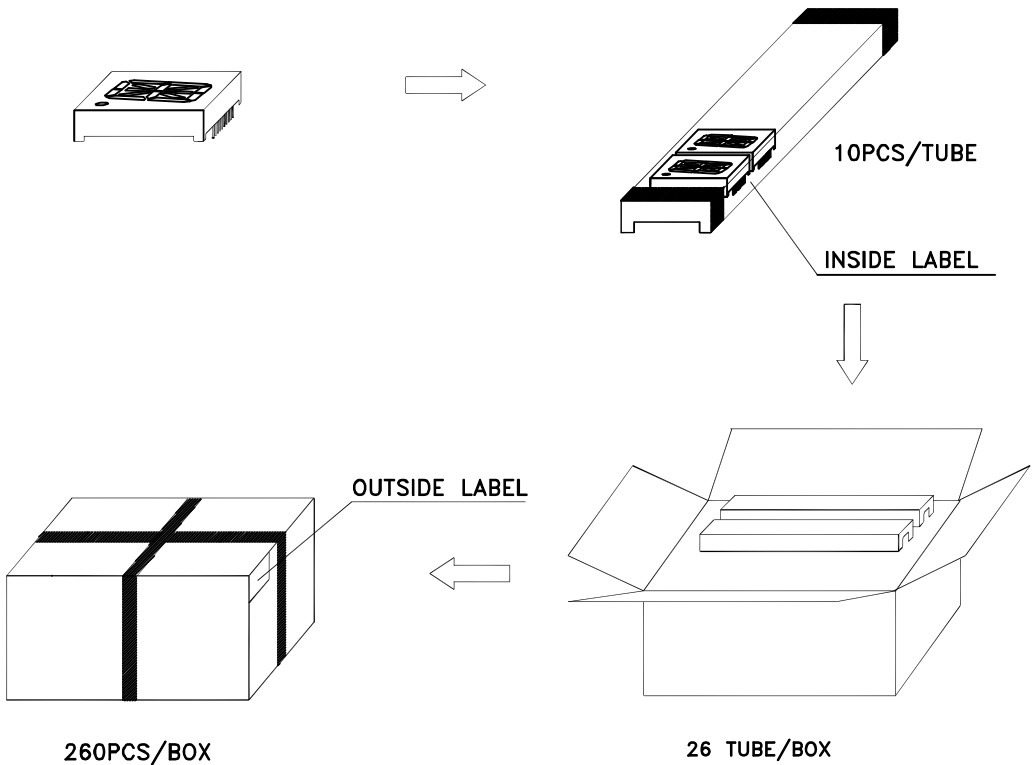
Yellow



Note:the curves are on the segment a1,a2,d1,d2,p,k.

PACKING & LABEL SPECIFICATIONS

PSC23-11YWA



Inside Label On IC-tube

Kingbright	TYPE: PSx23-11xxx	PASSED xx xx xx FQCX	Date
	QTY: 10 PCS CODE: xx		
XXXXXXXXXX-XXXX		Number OF FQC	
LOT NO.		RoHS Compliant	

Outside Label On Box

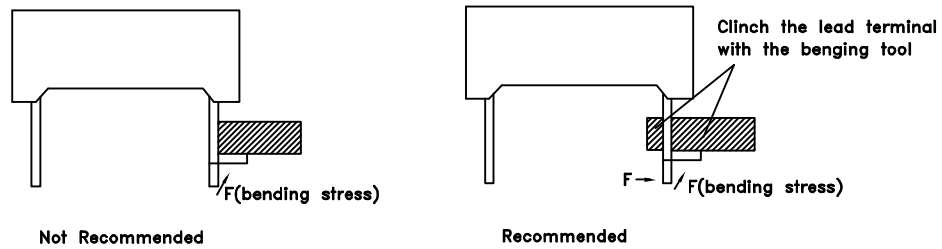
XXXXXX			
PSx23-11xxx	Bin Code	Number OF QA	
260 PCS	XX	QAx xx xx xx PASSED	Date
RoHS Compliant			

THROUGH HOLE DISPLAY MOUNTING METHOD

Lead Forming

Do not bend the component leads by hand without proper tools.

The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

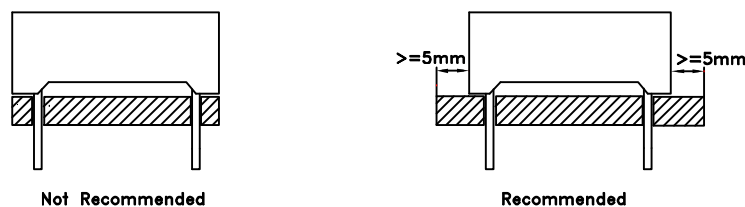


Installation

- 1.The installation process should not apply stress to the lead terminals.
- 2.When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.

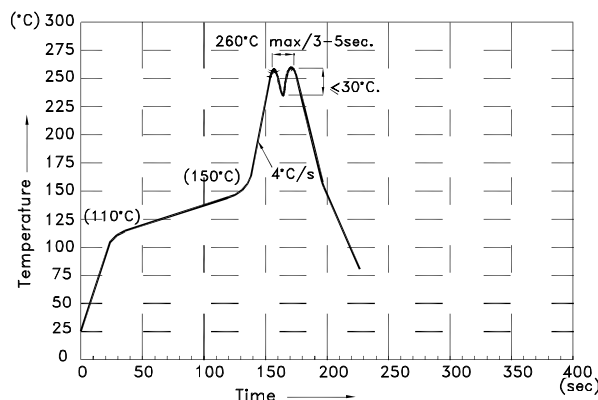


- 3.The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.



DISPLAY SOLDERING CONDITIONS

Wave Soldering Profile For Lead-free Through-hole LED.



NOTES:

1. Recommend the wave temperature 245°C~260°C. The maximum soldering temperature should be less than 260°C.
2. Do not apply stress on epoxy resins when temperature is over 85°C.
3. The soldering profile apply to the lead free soldering (Sn/Cu/Ag alloy).
4. During wave soldering, the PCB top-surface temperature should be kept below 105°C.
5. No more than once.

Soldering General Notes:

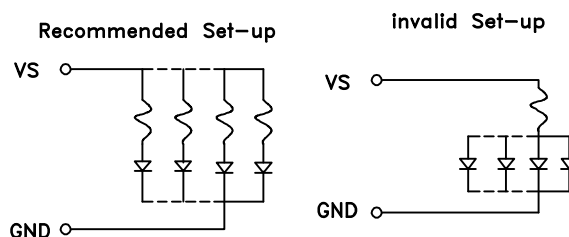
1. Through-hole displays are incompatible with reflow soldering.
2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with Kingbright for compatibility.

CLEANING

1. Mild "no-clean" fluxes are recommended for use in soldering.
2. If cleaning is required, Kingbright recommends to wash components with water only. Do not use harsh organic solvents for cleaning, because they may damage the plastic parts. And the devices should not be washed for more than one minute.

CIRCUIT DESIGN NOTES

1. Protective current-limiting resistors may be necessary to operate the Displays.
2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.



All design applications should refer to Kingbright application notes available at <http://www.KingbrightUSA.com/ApplicationNotes>

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