

Overview

KEMET's 900 encapsulated radial leaded ceramic disc capacitors are specifically designed for interference-suppression AC line filtering applications. Having internationally recognized safety certifications, these capacitors are well-suited for applications that require keeping potentially disruptive or damaging line transients and EMI out of susceptible equipment. They are also an ideal solution when needing to suppress line disturbances at the source.

Safety Certified Capacitors are classified as either X and/ or Y capacitors. Class X capacitors are primarily used in line-to-line (across-the-line) applications. In this application, there is no danger of electric shock to humans should the capacitor fail, but could result in a risk of fire. The class Y capacitor is primarily used in line-to-ground (line by-pass) applications. In this application, failure of the capacitor could lead to danger of electric shock.

With a working voltage of 440 VAC in line-to-line (Class X) and 300 VAC in line-to-ground (Class Y) applications, these safety capacitors meet the impulse test criteria outlined in IEC Standard 60384. Meeting subclass X1 and Y2 requirements, these devices are certified to withstand impulses up to 4 KV (X1) and 5 KV (Y2) respectively. These encapsulated devices also meet the flame test requirements outlined in UL Standard 94 V-0.

Benefits

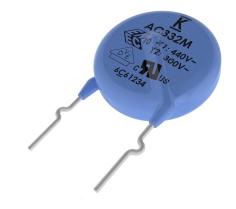
- Safety standard recognized (IEC 60384-14)
- Reliable operation up to 125°C
- Class X1/Y2
- 7.5 and 10 mm lead spacing
- Lead (Pb)-free and RoHS Compliant
- Halogen-free
- Capacitance offerings ranging from 10 pF up to 10 nF
- Available capacitance tolerances of ±5%, ±10%, and ±20%
 High reliability
- Preformed (crimped) or straight lead configurations
- Non-polar device, minimizing installation concerns
- Encapsulation meets flammability standard UL 94 V-0

Applications

Typical applications include:

- Line-to-line (Class X) filtering
- · Line-to-ground (Class Y) filtering
- Antenna coupling
- · Primary and secondary coupling (switching power supplies)
- · Line disturbances suppression (motors and motor controls, relays, switching power supplies, and invertors)

One world. One KEMET





Ordering Information

C 9	7	1	U	472	М	Z	W	D	Α	Α	7317
Ceramic Series	Body Diameter	Lead Spacing ^{1,3}	Spec.	Capacitance Code (pF)	Capacitance Tolerance	Rated Voltage	Dielectric/ Temp. Char.	Design	Lead Configuration ^{2,3}	Failure Rate	Packaging (C-Spec)
C9 = Ceramic 900 Series	0 = 7.0 mm 1 = 8.0 mm 2 = 9.0 mm 3 = 10.0 mm 4 = 11.0 mm 6 = 13.0 mm 8 = 15.0 mm	7 = 7.5 mm 1 = 10.0 mm	U = Safety	Two significant digits and number of zeroes	J = ±5% K = ±10% M = ±20%	Z = X1 440 VAC /Y2 300 VAC	S = SL Y = Y5P W = Y5U V = Y5V	D = Disc	A = Straight B = Vertical Kink C = Outside Kink	A = N/A	See "Packaging C-Spec Ordering Options Table"

¹ Capacitor body diameter will limit available lead spacing and packaging options. See "Dimensions" and "Product Ordering Codes and Ratings" sections of this document to determine availability.

² "Vertical Kink" and "Outside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

³ Bulk packaging lead length availability is dependent upon "Lead Configuration" and "Lead Spacing." See "Dimensions" section of this document to verify availability of a specific lead length option. For nonstandard lead length inquiries, please contact KEMET.

Packaging C-Spec Ordering Options Table

Packaging Type	Lead Length (mm) ^{2,3}	Packaging Ordering Code (C-Spec)
Ammo Pack	See Note 5	7317
	3.0±1.0	WL30
	3.5±1.0	WL35
Dulk Dog	4.0±1.0	WL40
Bulk Bag	4.5±1.0	WL45
	5.0±1.0	WL50
	20.0 minimum ⁴	WL20

¹ Preformed (crimped) lead configurations include "Vertical Kink" and "Outside Kink." See "Lead Configurations" and "Ordering Information" sections of this document for further details.

² "Vertical Kink" and "Outside Kink" lead configurations cannot be combined with the bulk/20 mm lead length option (WL20). 20 mm lead length is only available on capacitors with straight leads (lead configuration ordering code "A"). For nonstandard lead length inquiries, please contact KEMET.

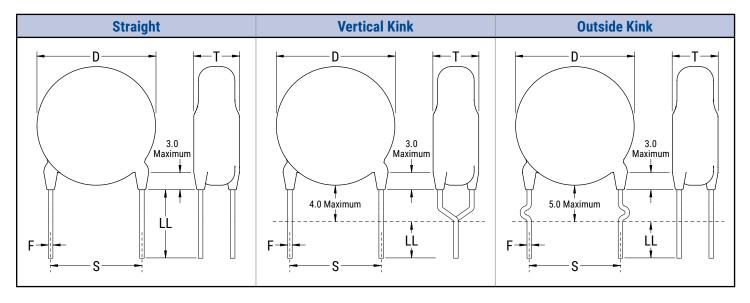
³ For nonstandard lead length inquiries, please contact KEMET.

 $^{\rm 4}$ Lead length of 20.0 mm minimum only available for straight leads.

⁵ Lead length for ammo pack packaging is defined by the H and H_a dimensions in Table 3.



Lead Configurations



Dimensions – Millimeters

	Lead	S	Lead	D	D T		ØF
Lead Configuration	Configuration Ordering Code ¹			Lead Meniscus	Lead Diameter		
Ctroight	٨	7.5	±1.0				
Straight	A	10.0	±1.0			3.0 maximum	0.55±0.1
Vertical Kink	В	7.5	±1.0	See Table 1 - "Product Ordering Codes and Ratings"			
(Preformed)	В	10.0	±1.0				
Outside Kink	ide Kink 7.5 ±1.0						
(Preformed)	С	10.0	±1.0				

¹Lead Configuration is identified in the 13th character of the ordering code. See "Lead Configuration" and "Ordering Information" sections of this document for further details.

² Body diameter of capacitor will limit available lead spacing and packaging options. See "Product Ordering Codes and Ratings" sections of this document for further details.



Approval Standard and Certification No.

Safety Standard	Standard No.	Subclass	Working Voltage	Certificate No.	
VDE		X1	440 VAC	40026415	
(ENEC)	IEC 60384-14	Y2	300 VAC	<u>40036415</u>	
UL	UL 60384-14 and	X1	440 VAC	F2F6290	
CAN/CSA	E60384-14	Y2	300 VAC	<u>E356389</u>	

These devices are VDE/ENEC and UL recognized for antenna coupling and AC line-to-line (Class X) and line-to-ground (Class Y) applications per IEC60384–14 and UL 60384–14.

Environmental Compliance

These devices are Halogen-free and RoHS Compliant. They meet all requirements set forth by both EU and China RoHS directives.



General Specifications/Performance Characteristics

Dielectric/Temperature Characteristic	SL	Y5P	Y5U	Y5V
Operating Temperature Range:	-40°C to +125°C			
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC):	-1,000 ~ +350 ppm/ °C ±10% +20%/-55% ~		~ +30%/-80%	
Dielectric Withstanding Voltage (7.5 and 10 mm Lead Spacing)		2,600 VAC (60 ±5 seconds at 25°C)		
Quality Factor (Q)	30 pF and above: ≥ 1,000 Below 30 pF: ≥ 400 +(20xC)*	See "Dissipation Factor"		
Dissipation Factor (tanδ) at +25°C1	See "Quality Factor"	2.50% 2.50% 5.0%		
Insulation Resistance (IR) Limit at +25°C	10,000 MΩ Minimum (500 VDC applied for 60 ±5 seconds at 25°C)			

*C = Nominal capacitance

¹ Capacitance and Dissipation Factor (DF) measured under the following conditions:

SL: 1 MHz ±100 kHz and 1.0 ±0.2 Vrms

X5P, Y5U and Y5V: 1 kHz ±50 Hz and 1.0 ±0.2 Vrms

Note: When measuring capacitance, it is important to ensure the set voltage level is held constant. The HP4284 and Agilent E4980 have a feature known as Automatic Level Control (ALC). The ALC feature should be switched to "ON."



Table 1 – Product Ordering Codes and Ratings

				Dimensions (mm)			Lead Spacing	
Dielectric/ Temp. Char.	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Bulk Packaging	Ammo Packaging
	C90(1)U100JZSD(2)A(3)	10 pF						
	C90(1)U120JZSD(2)A(3) C90(1)U150JZSD(2)A(3)	12 pF 15 pF						
	C90(1)U180JZSD(2)A(3)	18 pF						
	C90(1)U200JZSD(2)A(3)	20 pF						
	C90(1)U220JZSD(2)A(3)	22 pF						
	C90(1)U240JZSD(2)A(3)	24 pF						
	C90(1)U270JZSD(2)A(3)	27 pF		7.0				
	C90(1)U300JZSD(2)A(3)	30 pF						
	C90(1)U330JZSD(2)A(3)	33 pF						mm
SL	C90(1)U360JZSD(2)A(3)	36 pF	±5%		5.0	0.55±0.1)r
	C90(1)U390JZSD(2)A(3)	39 pF					10	mm
	C90(1)U470JZSD(2)A(3) C90(1)U500JZSD(2)A(3)	47 pF 50 pF						
	C90(1)U510JZSD(2)A(3)	50 pF 51 pF						
	C90(1)U560JZSD(2)A(3)	56 pF						
	C91(1)U620JZSD(2)A(3)	62 pF						
	C91(1)U680JZSD(2)A(3)	68 pF		8.0				
	C91(1)U750JZSD(2)A(3)	75 pF						
	C92(1)U820JZSD(2)A(3)	82 pF		9.0				
	C93(1)U101JZSD(2)A(3)	100 pF		10.0				
	C90(1)U101KZYD(2)A(3)	100 pF			[l I		
	C90(1)U151KZYD(2)A(3)	150 pF						
	C90(1)U221KZYD(2)A(3)	220 pF		7.0			7.5 mm or	
	C90(1)U331KZYD(2)A(3)	330 pF						mm
Y5P	C90(1)U471KZYD(2)A(3)	470 pF	±10%		5.0	0.55±0.1		
	C91(1)U561KZYD(2)A(3)	560 pF		8.0			10) mm
	C91(1)U681KZYD(2)A(3)	680 pF						
	C92(1)U821KZYD(2)A(3)	820 pF		9.0				
	C92(1)U102KZYD(2)A(3)	1,000 pF						
	C90(1)U102MZWD(2)A(3)	1,000 pF		7.0	[1		
	C92(1)U152MZWD(2)A(3)	1,500 pF		9.0			7 5	mm
Y5U	C92(1)U222MZWD(2)A(3)	2,200 pF	±20%		5.0	0.55±0.1	-	n in
	C94(1)U332MZWD(2)A(3)	3,300 pF		11.0	0.0	01002011		mm
	C96(1)U392MZWD(2)A(3) C96(1)U472MZWD(2)A(3)	3,900 pF 4,700 pF		13.0				
	0,20(1)0472WIZWD(2)A(3)	π ,700 μr			L			
	C90(1)U102MZVD(2)A(3)	1,000 pF						
	C90(1)U152MZVD(2)A(3)	1,500 pF		7.0				
	C90(1)U222MZVD(2)A(3)	2,200 pF					75	mm
Y5V	C92(1)U332MZVD(2)A(3)	3,300 pF	±20%	9.0	5.0	0.55±0.1		r
	C94(1)U392MZVD(2)A(3)	3,900 pF 4,700 pF		11.0			10	mm
	C94(1)U472MZVD(2)A(3) C96(1)U682MZVD(2)A(3)	4,700 pF 6,800 pF		13.0				
	C98(1)U103MZVD(2)A(3)	10,000 pF		15.0				
	· · · · · · · · · · · · · · · · · · ·	· •			[·		
	KEMET Part Number	Capacitance	Capacitance Tolerance	Body Diameter (Maximum)	Body Thickness (Maximum)	Lead Diameter	Lead S	pacing

(1) To properly complete ordering code, insert the one-digit numeric code to reflect required lead spacing: (Note that select capacitance values and packaging options may limit lead spacing availability. See table above to verify availability.) 7 = 7.5 mm

1 = 10.0 mm

(2) To properly complete ordering code, insert the one-digit character code to reflect the required lead configuration: (See "Lead Configuration" section of this document, page 2, for further details.)

A = Straight

B = Vertical Kink

C = Outside Kink

(3) To properly complete ordering code, enter the four-digit numeric or alphanumeric "Packaging C-Spec Ordering Code." See "Dimensions" section of this document, page 2, for available options.



Table 2 – Performance & Reliability: Test Methods and Conditions

lt	em	Speci	fication	Test Method				
Operating Tem	perature Range			-40°C to +125°C				
	Between lead wires	No f	ailures	The capacitor shall not be damaged when 2,600 VAC (rms) is applied between the lead wires for 60 seconds.				
Dielectric Strength			No failures		ads) of the capacito gether. A metal foil i the body of the cap about 3 to 4 mm froi pacitor is then inser filled with metal ball mm in diameter. 2,6 or 60 seconds betw ires and metal balls	is tightly acitor m each ted S 00 VAC veen the		
Insulation R	esistance (IR)	10,000 N	IΩ minimum	The insulation re		easured with 500 ±50 VDC		
Capacitance		Within spec	ified tolerance			5		
		Temperature Characteristics	Specification					
		Y5P, Y5U	DF ≤ 2.5%	Y5P, Y5U, and Y5V: Capacitance is measured at 1 kHz $\pm 20\%$ and				
D		Y5V	DF ≤ 5.0%	5 V _{rms} or less. (20±2°C) SL: Capacitance is measured at 1 MHz ±20% and				
Dissipation F	actor (DF) or Q	SL	\ge 30 pF: Q \ge 1,000 < 30 pF: Q \ge 400 +(20 x C) C = Nominal capacitance	1.0 ±0.2 V _{rms} (25°C)				
				A capacitance m	easurement is made	e at each step specified:		
				Step	Temperature			
		Temperature Characteristics	Capacitance Change	1	+20±2°C			
				2	-25±2°C			
Temperature	Characteristics	Y5P	Within ±10%	3	+20±2°C			
		Y5U	Within +20%/-55%	4	+85±2°C			
		Y5V	Within ~+30%/-80%	5	+20±2°C			
		SL	-1,000 ~+350 ppm°C (+20°C ~+85°C)		ed at 85±2°C for 1 h ±2 hours before me	nour and then placed at room asurement.		
	Tensile		acitor body shall not reak.	 With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a tensile force of 10 N is applied to the termination in the direction of its axis and acting in a direction away from the body of the specimen. With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass force of 5 N is then suspended from the end of the termination. The body of the specimen is then inclined within a period of 2 to 3 seconds, through an angle of approximately 90° in the vertical plane and then resumed to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction. 				
Terminal Strength	Bending		acitor body shall not reak.					

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

lte	m	Speci	fication	Test M	lethod		
Solder	ability	of solder in the ax	ave a uniform coating ial direction and over ircumference.	The lead wire of the capacitor is 5±0.5 seconds. The depth of imn (+5/-0 mm) from the root of lead Solder Temperature: Lead free s 245°C ±5°C.	nersion is up to 1.5 mm wires.		
	Appearance	No vis	ual defect	As shown in the figure below, the lead wires are immersed in molten solder up to 1.5 mm (+5/-0 mm) from the end of the			
	IR	1,0	00 MΩ	epoxy meniscus (root of lead wire). Duration/Solder Temperature: 3.5±0.5 seconds/350°C ±10°C or			
	Dielectric Strength	Per	item 1	Duration/Solder Temperature: 3. 10±1 seconds/260°C ±5°C	5±0.5 seconds/350°C ±10°C or		
Soldering Effect (Non-Preheat)	Capacitance	Y5P, Y5U, and Y5V: Within ±10% SL: Within ±2.5% or ±0.25 pF, whichever is larger		Thermal Screen 1.5 to 2.0 mm Nolten Solder Pre-treatment: Capacitor is store then placed at room condition ¹ for measurements. Post-treatment: Capacitor is store condition ¹ .	or 24 ±2 hours before initial red for 1 to 2 hours at room		
	Appearance	No vis	ual defect	Capacitor is stored at 120°C +0/- Then, as shown in the figure belo	-5°C for 60 +0/-5 seconds.		
	IR	1,000 ΜΩ		in molten solder up to 1.5 mm (+	5/-0mm) from the end of the		
	Dielectric Strength	Per item 1		epoxy meniscus (root of lead wire). Duration/Solder Temperature: 7.5 +0/-1 seconds/260°C ±5°C			
Soldering Effect (Preheat)	Capacitance	Y5P, Y5U, and Y5V: Within ±10% SL: Within ±2.5% or ±0.25 pF, whichever is larger		Thermal Capacitor Screen 1.5 to 2.0 mm Molten Solder Pre-treatment: Capacitor is stored at 85°C ±2°C for 1 hour and then placed at room condition' for 24 ±2 hours before initial measurements. Post-treatment: Capacitor is stored for 1 to 2 hours at room condition ¹ .			
	Appearance	No vis	ual defect	Steady State Humidity:	Load Humidity:		
	Capacitance	Temperature Characteristics Y5P Y5U	Capacitance Change Within ±10% Within ±20%				
	Capacitance	Y5V	Within ±30%				
Biased Humidity		SL	Within ±2.5% or ±0.25 pF, whichever is larger	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours. Post Treatment:	90 to 95% humidity at 40°C ±2°C for 500 ±12 hours with full rated voltage applied.		
	DF		: 5.0% maximum	Capacitor is stored for 1 to 2	Post Treatment: Capacitor is stored for 1 to 2		
	Q	SL: Less Q ≥ 100 More than C = Nomina	% maximum than 30 pF: + 10 × C/3 30 pF: Q ≥ 200 al capacitance	hours at room condition ¹ .	hours at room condition ¹ .		
	IR		: 3,000 MΩ minimum MΩ minimum				
	Dielectric Strength		failures				

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

lte	em	Specification	Test Method
High	Appearance Capacitance Change IR	No visual defect Y5P, Y5V, and Y5U: Within ±20% SL: Within ±3 or ±0.3 pF, whichever is larger. 3,000 MΩ minimum SL: 1,000 MΩ minimum	Impulse Voltage: Each individual capacitor is subjected to three 5 kv impulses prior to life testing.
Temperature Life	Dielectric Strength	No failures	Capacitors are placed in a circulating air oven for a period of 1,000 hours. The air in the oven is maintained at a temperature of 125°C ±2 throughout the test. The capacitors are subjected to AC 510 V_{rms} . Each hour the voltage is increased to 1,000 V_{rms} for 0.1 seconds.
Flame	e Test	The capacitor flame extinguishes as follows:CycleTime1 ~ 430 seconds maximum560 seconds maximum	The capacitor is exposed to a flame for 15 seconds and then removed for 15 seconds. This test is repeated for 5 cycles.
Active Flammability		The cheesecloth should not ignite.	The capacitors are individually wrapped in at least one, but not more than two, complete layers of cheesecloth. They are then subjected to 20 discharges. The interval between successive discharges is 5 seconds. The VAC is maintained for 2 minutes after the last discharge. $I_1 + \underbrace{I_2 + \underbrace{I$

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



Table 2 – Performance & Reliability: Test Methods and Conditions cont'd

Item		Specif	cation		Test Me	thod		
Passive Fla	ammability	The burning time sh seco The tissue paper	nds.	which best flame one t the second sec	About 10mm Thick Board f exposure to flame: Length of flame: Gas burner length: Inside diameter: Outside diameter:	specimen is e	m	
	Appearance	No visua	Il defect					
		Temperature Characteristics	Capacitance Change	The capacitor is subjected to 5 temperature cycles. Temperature Cycle			S.	
	Capacitance	SL	Within ±5%			Dwell	Transition	
		Y5P Y5U, Y5V	Within ±10% Within ±20%	Step	Temperature (°C)	Time (minutes)	Time (minutes)	
Temperature		SL	≥ 30 pF: Q ≥ 350	1	-40+0/-3	30		
Cycle		<u>SE</u>	< 30 pF: Q ≥ 275	2	Room temperature	3		
			+5/2C C = Nominal	3	125+3/-0	30	3	
	DF/Q		capacitance	4	Room temperature	3		
		Y5P	DF ≤ 5%					
		Y5U, Y5V	DF ≤ 7.5%	Pre-treatment: Capacitor shall be stored at 85 ±2 for 1 hour the placed at room condition ¹ for 24 ±2 hours.				
	IR	3,000 MΩ	minimum	condition ¹ .	ment: Capacitor is stored	1 10f 1 to 2 hou	rs at room	
	Dielectric Strength	No fa	ilures					

¹ "Room Condition" is defined as follows: Temperature: 15 ~ 35°C/Humidity: 45 ~ 75%/Atmospheric Pressure: 86 ~ 106 kPa.



Soldering and Mounting Information

Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could reflow the solder joint between the lead and ceramic element and/or may result in thermal shocks that can crack the ceramic element.

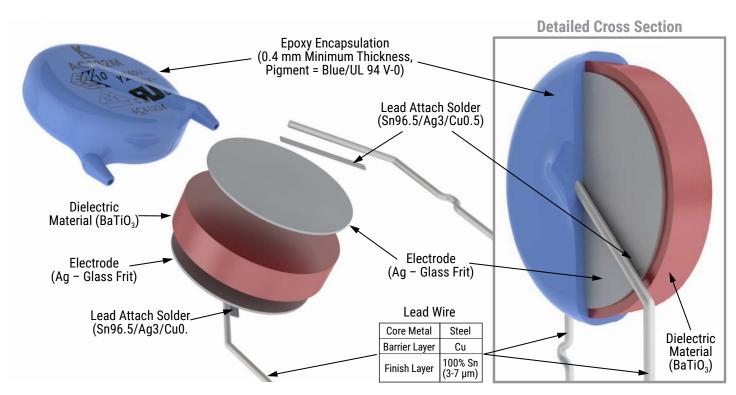
When soldering these capacitors with a soldering iron, it should be performed under the following conditions:

- Temperature of iron-tip: 400°C maximum
- Soldering iron wattage: 50 W maximum
- · Soldering time: 3.5 seconds maximum

Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions:

- · Rinse bath capacity: Output of 20 watts per liter or less
- Rinsing time: 5 minute maximum
- Do not vibrate the PCB/PWB directly
- · Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires



Construction



Marking

These capacitors shall be stamped or laser marked with KEMET's trademark, type designation, capacitor class, rated voltage, rated capacitance, and capacitance tolerance codes. In addition, all devices are marked with the recognized approval mark and a date/lot code for traceability. Marking will be supplied either on one side or both sides of the encapsulated capacitor body. All marking shall be legible to allow for clear identification of the component. Marking appears in legible contrast. Illustrated below is an example of the marking format and content. (Two-sided marking is limited to capacitors with body diameters ≤ 8.0 mm)

Single Sided **Rated Capacitance** (3 Numeric Characters) KEMET ID Type Designation Capacitance Tolerance Code (2 Characters) (1 Character) X1: 440V-**VDE & ENEC Capacitor Class** and Rated Voltage approval marks 300 IEC 60384-14 3rd (2005)UL approval mark Date/Lot Code 6C61234 (See Table Below) **Double Sided** Front Back UL approval mark **Rated Capacitance** KEMET ID (3 Numeric Characters) Capacitance Type Designation VDE & ENEC Tolerance Code (2 Characters) approval marks (1 Character) IEC 60384-14 3rd <1: 440V~ (2005) Capacitor Class 6C61234 and Rated Voltage Y2: 300V~ Date/Lot Code (See Table Below) Date/Lot Code Explanation

Location Code A = October lot number	6	<u>C</u>	6	1234
D = December	e.g.,	-	Month: 1-9 = Jan - Sept A = October N = November	Last 4 digits of lot number

© KEMET Electronics Corporation • KEMET Tower • One East Broward Boulevard Fort Lauderdale, FL 33301 USA • 954-766-2800 • www.kemet.com



Packaging Quantities

Capacitor			Ar	oe)			
Body Diameter	Body Diameter Code ¹	Bulk Bag (Loose)	Component pitch on carrier12.7 mm15 mm				r tape²
(mm)	out	(20000)			25.4 mm		
7.0	0						
8.0	1				1,000 pieces/box		
9.0	2						
10.0	3						
11.0	4	500 pieces/bag	1,000 pie	eces/box			
12.0	5						
13.0	6						
14.0	7				500 pieces/box		
15.0	8						

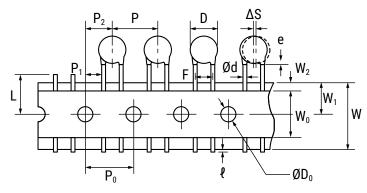
¹ The "Body Diameter Code" is located in the third character position of the ordering code. This code identifies the maximum diameter of the capacitor body in millimeters. For more information regarding the ordering code, see "Ordering Information" section of this document.

² For details regarding component pitch on carrier tape, see "Ammo Pack Taping Format" and "Ammo Pack Taping Specifications" sections of this document.

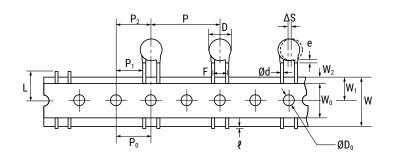


Figure 1 - Ammo Pack Taping Format

5 mm and 7.5 mm Lead Spacing:



10 mm Lead Spacing:



For All Lead Spacing:

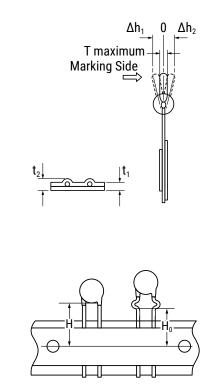


Table 3 – Ammo Pack Taping Specifications

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style		Straight	Preformed ¹	Straight	Preformed ¹	Straight	Preformed ¹
Item	Symbol	Dimensions (mm)					
Lead Spacing	F	5.0+0.8/-0.2		7.5±1.0		10.0±1.0	
Component Pitch	Р	12.7		15.0		25.4±2	
Sprocket Hole Pitch	P ₀	12.7±0.3		15.0±0.3		12.7±0.3	
Sprocket Hole Center to Component Center	P ₂	6.35±1.5		7.5±1.5		12.7±1.5	
Sprocket Hole Center to Lead Center	P ₁	3.75±1.0		3.75±1.0		7.7±1.5	
Body Diameter	D	See "Product Ordering Codes and Ratings" section of this document.					
Component Alignment (side/side)	ΔS	0±2.0					
Carrier Tape Width	W	18.0+1.0/-0.5					

¹ Prefromed (crimped) lead configurations include vertical kink and outside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

² Also referred to as "lead length" in this document.



Table 3 – Ammo Pack Taping Specifications cont'd

Lead Spacing		5 mm		7.5 mm		10 mm	
Lead Style		Straight	Preformed ¹	Straight	Preformed ¹	Straight	Preformed ¹
Item	Symbol	Dimensions (mm)					
Sprocket Hole Position	W ₁	9.0±0.5					
Height to Seating Plane ² (preformed leads ¹)	H _o	N/A	18.0+2.0/-0	N/A	18.0+2.0/-0	N/A	18.0+2.0/-0
Height to Seating Plane ² (straight leads)	Н	20.0+1.5/-1.0	N/A	20.0+1.5/-1.0	N/A	20.0+1.5/-1.0	N/A
Lead Protrusion	ł	2.0 maximum					
Diameter of Sprocket Hole	D ₀	4.0±0.2					
Lead Diameter	φd	0.55±0.1					
Carrier Tape Thickness	t ₁	0.6±0.3					
Total Thickness (Carrier Tape, Hold-Down Tape and Lead)	t ₂	1.5 maximum					
Component Alignment (front/ back)	Δh_1 Δh_2	2.0 maximum					
Cut Out Length	L	11.0 maximum					
Hold-Down Tape Width	W ₀	11.0 minimum		11.5 minimum			
Hold-Down Tape Position	W ₂	3.0 maximum		1.5±1.5			

¹ Prefromed (crimped) lead configurations include vertical kink and outside kink. See "Lead Configurations" and "Ordering Information" sections of this document for further details.

² Also referred to as "lead length" in this document.

Application Notes:

Storage and Operating Conditions:

The insulating coating of these devices does not form an air and moisture-tight seal. Avoid exposure to moisture and do not use or store these devices in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt, or the like are present. Before cleaning, bonding or molding these devices, it is important to verify that your process does not affect product quality and performance. KEMET recommends testing and evaluating the performance of a cleaned, bonded or molded product prior to implementing and/or qualifying any of these processes. Store the capacitors where the temperature and relative humidity do not exceed 40 degrees Centigrade and 70% respectively. For optimum solderability, capacitor stock should be used promptly, preferably within 6 months of receipt.

Working Voltage:

Application voltage (Vp-p or Vo-p) must not exceed the voltage rating of the capacitor. Irregular voltages can be generated for a transient period of time when voltage is initially applied and/or removed from a circuit. It is important to choose a capacitor with a voltage rating greater than or equal to these irregular voltages.



Voltage	DC Voltage	DC+AC Voltage	AC Voltage	Pulse Voltage (1)	Pulse Voltage (2)
Positional Measurement	Vo-p	vo-p	vp-p	Vp-p	Vp-p

Operating Temperature and Self-Generating Heat:

The surface temperature of a capacitor should be kept below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or similar current, it may self-generate heat due to dielectric loss. Temperature rise due to self-generated heating should not exceed 20°C (while operated at an atmosphere temperature of 25°C).

Handling - Vibration and Impact:

Do not expose these devices or their leads to excessive shock or vibration during use.

FAILURE TO FOLLOW THE ABOVE CAUTIONS MAY RESULT, WORST CASE, IN A SHORT CIRCUIT AND CAUSE FUMING OR PARTIAL DISPERSION WHEN THE PRODUCT IS USED.



KEMET Electronics Corporation Sales Offices

For a complete list of our global sales offices, please visit www.kemet.com/sales.

Disclaimer

All product specifications, statements, information and data (collectively, the "Information") in this datasheet are subject to change. The customer is responsible for checking and verifying the extent to which the Information contained in this publication is applicable to an order at the time the order is placed. All Information given herein is believed to be accurate and reliable, but it is presented without guarantee, warranty, or responsibility of any kind, expressed or implied.

Statements of suitability for certain applications are based on KEMET Electronics Corporation's ("KEMET") knowledge of typical operating conditions for such applications, but are not intended to constitute – and KEMET specifically disclaims – any warranty concerning suitability for a specific customer application or use. The Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by KEMET with reference to the use of KEMET's products is given gratis, and KEMET assumes no obligation or liability for the advice given or results obtained.

Although KEMET designs and manufactures its products to the most stringent quality and safety standards, given the current state of the art, isolated component failures may still occur. Accordingly, customer applications which require a high degree of reliability or safety should employ suitable designs or other safeguards (such as installation of protective circuitry or redundancies) in order to ensure that the failure of an electrical component does not result in a risk of personal injury or property damage.

Although all product-related warnings, cautions and notes must be observed, the customer should not assume that all safety measures are indicted or that other measures may not be required.

KEMET is a registered trademark of KEMET Electronics Corporation.

Mouser Electronics

Authorized Distributor

Click to View Pricing, Inventory, Delivery & Lifecycle Information:

KEMET: C937U101JZSDDA7317