No.: SPC-K-HTS-0001 /5
Date: 2017. 4. 21

Data sheet

Title: ESD SUPPRESSOR; RECTANGULAR TYPE

Style: SPC06, 10

AEC-Q200 qualified

RoHS COMPLIANCE ITEM Halogen and Antimony Free

Note: • Stock conditions

Temperature: $+5^{\circ}\text{C} \sim +35^{\circ}\text{C}$ Relative humidity: $25\% \sim 75\%$

The period of guarantee: Within 2 year from shipmen t by the company.

Solderability shall be satisfied.

- Product specification contained in this data sheet are subject to change at any time without notice
- •If you have any questions or a Purchasing Specification for any quality Agreement is necessary, please contact our sales staff.



Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya

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1. Scope

1.1 This data sheet covers the detail requirements for ESD suppressor; rectangular type, style of SPC06, 10.

2. Classification

Type designation shall be the following form.

1 ESD suppressor; rectangular type — Style

2 Size

3 Peak voltage

Symbol	Peak voltage
501	50×10 ¹ V

4 Rated voltage

Symbol	Rated voltage
Α	30V max
С	50V max

5 Optional code

•	Symbol	Optional code
	01	Capacitance: 0.1 pF max.

6 Packaging form

В	Bulk (loose package)	
PA	Press pocket taping	
TH	Paper taping	

3. Rating

3.1 The ratings shall be in accordance with Table-1.

Table-1

	ESD capability *1			Rated voltage	Capacitance	Leakage current
Style	Peak voltage (V)	Clamping voltage (V)	ESD pulse withstand (pulses)	(V)	(pF) *2	(µА)
SPC06	500 max.	100 max.	50	30 max.	0.1 max.	1 max.
SPC10	500 max.	100 max.	100	30 max.	0.1 max.	1 max.
35010	500 max.	100 max. 100		50 max.	U. I IIIax.	i iliax.

Style	Category temperature range (°C)	
SPC06	-55 to +125	
SPC10		

^{*1} Peak voltage: IEC61000-4-2, 8kV, Contact discharge, The peak voltage shall be measured.

Clamping voltage: IEC61000-4-2, 8kV, Contact discharge, The voltage value shall be measured after 30ns from the peak voltage.

ESD pulse withstand: IEC61000-4-2, 8kV, Contact discharge, The pulse withstand.

*2 Capacitance: 25°C, 1MHz, 1Vrms

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4. Packaging form

The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	Packaging form		Standard packaging quantity / units	Application
В	Bulk (loose package)		1,000 pcs.	SPC06, 10
PA	Press pocket taping (paper taping)	8mm width, 2mm pitches	15,000 pcs.	SPC06
TH	Paper taping	8mm width, 2mm pitches	10,000 pcs.	SPC10

5. Dimensions

5.1 The suppressor shall be of the design and physical dimensions in accordance with Figure-1 and Table-3.

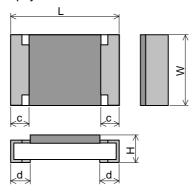


Figure-1

Unit : ı	mm
	Unit : ı

Style	L	W	Н	С	d
SPC06	0.6±0.03	0.3±0.03	0.23±0.03	0.15±0.10	0.15±0.10
SPC10	1.0±0.05	0.5±0.05	0.35±0.05	0.2±0.10	0.25±0.10

5.2 Equivalent circuits





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6. Performance

6.1 Unless otherwise specified, the standard range of atmospheric conditions for tests is as follows;

Ambient temperature: 5 °C to 35 °C, Relative humidity: 45 % to 85 %, Air presser: 86 kPa to 106 kPa

If there is any doubt the results, measurements shall be made within the following:

Ambient temperature: 20 °C \pm 2 °C, Relative humidity: 60 % to 70 %, Air presser: 86 kPa to 106 kPa

6.2 The performance shall be satisfied in Table-4.

Table-4(1)

	,	14016 -(1)	,
No.	Test items	Condition of test	Performance requirements
1	ESD capability	IEC61000-4-2	500V max.
	Peak voltage	The suppressor shall be mounted on the test substrate	
		as shown in Figure–2.	
		Test condition: 8kV, Contact discharge	
		Measurement: The peak voltage shall be measured.	
2	ESD capability	IEC61000-4-2	100V max.
_	Clamp voltage	The suppressor shall be mounted on the test substrate	100 v max.
	Ciamp voltage	as shown in Figure—2.	
		Test condition: 8kV, Contact discharge	
		Measurement: The voltage value shall be measured	
		after 30ns from the peak voltage.	
3	CCD conshility	IEC61000-4-2	404
3	ESD capability		10μA max.
	ESD pulse withstand	The suppressor shall be mounted on the test substrate	
		as shown in Figure—2.	
		Test condition: 8kV, Contact discharge	
		Applied pulses: SPC06: 20 pulses, SPC10: 100 pulses	
		Measurement: After examination, the current value when	
		the rated voltage is applied is measured.	
4	Capacitance	Measurement condition:	0.1pF max.
		Frequency: 1MHz±10%	
		Voltage: 1 Vrms±0.2Vrms	
		Ambient temperature:25°C±2°C	
5	Leakage current	Measurement voltage: Rated voltage	1μA max.
		Measurement: The current value when the	·
		measurement voltage is applied is measured.	
6	Terminal bond strength of	JIS C 60068-2-21	Leakage current: 10µA max.
	the face plating	The suppressor shall be mounted on the test substrate	No evidence of mechanical
		as shown in Figure–2.	damage.
		Bending value: 3 mm (Among the fulcrums: 90 mm)	
		Duration: 10 s ± 1 s	
7	Resistance to soldering	JIS C 60068-2-58	Leakage current: 10μA max.
	heat	Test by a piece.	No evidence of appearance
		Temp. of solder bath: 260 °C ± 5 °C	damage
		Immersion time: $10 \text{ s} \pm 1 \text{ s}$	3
		After immersion into solder, leaving the room temp. for	
	48h or more, and then measure the leakage current.		
		•	
		Reflow soldering Description 450 00 400 00 400 0 400 0	
		Pre-heating: 150 °C ~ 180 °C, 120 s max.	
		Peak: 260 °C ± 5 °C, 10 s max.	
		Reflow cycle: 2 times	
		After immersion into solder, leaving the room temp. for	
		48h or more, and then measure the leakage current.	
8	Solderability	JIS C 60068-2-58	The surface of terminal immersed
		Test by a piece	shall be min. of 95 % covered with
		Flux: Rosin-Methanol	a new coating of solder.
		Temp. of solder: bath: 235 $^{\circ}$ C \pm 5 $^{\circ}$ C	
		Immersion time: $2 s \pm 0.5 s$	
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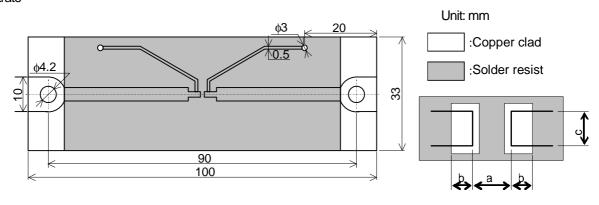
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Table 4(2)

_	Table-4(Z)				
9	Solvent	JIS C 60068-2-45	No evidence of appearance		
		The specimen shall be cleansed at normal temperature	damage		
		for 90s using Isopropyl alcohol.			
10	Rapid change temperature	JIS C 60068-2-14	Leakage current: 10µA max.		
		The suppressor shall be mounted on the test substrate	No evidence of appearance		
		as shown in Figure–2.	damage		
		Lower temperature: –55 °C			
		Upper temperature: +125 °C			
		Duration of exposure at each temperature: 30 min.			
		Number of cycles: 100 cycles			
		After examination, leaving the room temp. for 48h or			
		more, and then measure the leakage current.			
11	Humidity	JIS C 60068-2-78	Leakage current: 10μA max.		
	(Steady state)	The suppressor shall be mounted on the test substrate as	No evidence of appearance		
		shown in Figure—2.	damage		
		Test temp. & relative humidity: 60±2°C & 90~95% RH.			
		Test period: 1,000 ⁺⁴⁸ ₀ h			
		After examination, leaving the room temp. for 48h or			
		more, and then measure the leakage current.			
12	Load life in humidity	The suppressor shall be mounted on the test substrate as	Leakage current: 10μA max.		
		shown in Figure-2.	No evidence of appearance		
		Test temp. & relative humidity: 60±2°C & 90~95% R.H.	damage		
		Test voltage: Rated voltage shall be applied continuously.			
		Test period: 1,000 ⁺⁴⁸ ₀ h			
		After examination, leaving the room temp. for 48h or			
		more, and then measure the leakage current.			
13	Endurance at 85 °C	The suppressor shall be mounted on the test substrate as	Leakage current: 10µA max.		
		shown in Figure—2.	No evidence of appearance		
		Test temp.: 85±2°C	damage		
		Test voltage: Rated voltage shall be applied continuously.			
		Test period: 1,000 ⁺⁴⁸ ₀ h			
		After examination, leaving the room temp. for 48h or			
		more, and then measure the leakage current.			

7. Test substrate



	Style	а	b	С
5	SPC06	0.3	0.35	0.35
S	SPC10	0.6	0.65	0.7

Figure-2 SPC TEST SUBSTRATE

Remark 1). Material: Epoxide woven glass

Thickness: 1. 6mm Thickness of copper clad: 0. 035mm

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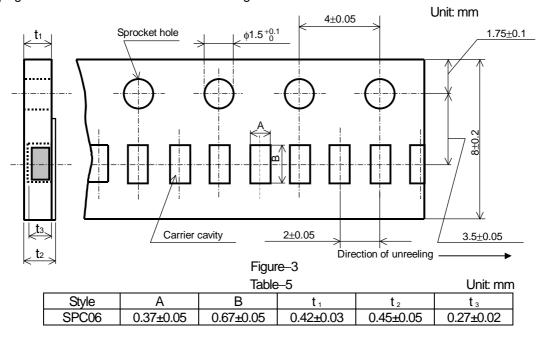
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8. Taping

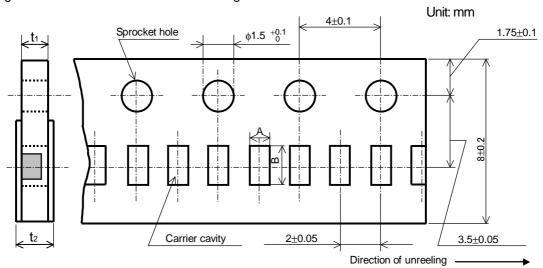
- 8.1 Applicable documents JIS C 0806-3: 2014, EIAJ ET-7200C: 2010.
- 8.2 Taping dimensions
- 8.2.1 Press pocket taping (Paper taping, 8mm width, 2mm pitches)

Taping dimensions shall be in accordance with Figure-3 and Table-5.



8.2.2 Paper taping (8mm width, 2mm pitches)

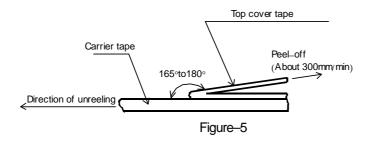
Taping dimensions shall be in accordance with Figure-4 and Table-6.

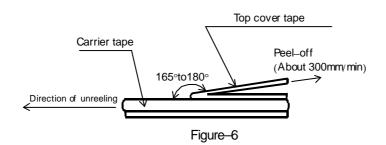


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- 1). The cover tapes shall not cover the sprocket holes.
- 2). Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following SPC06: Figure–5, SPC10: Figure–6.
- 6). When the tape is bent with the minimum radius for 25 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- In no case shall there be two or more consecutive components missing.
 The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The suppressors shall be faced to upward at the over coating side in the carrier cavity.



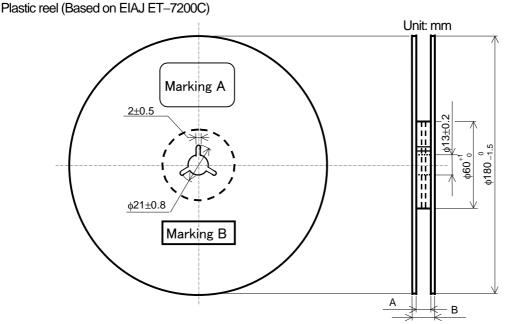


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8.3 Reel dimension

Reel dimensions shall be in accordance with the following Figure–7 and Table–7.



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

8.4 Leader and trailer tape.

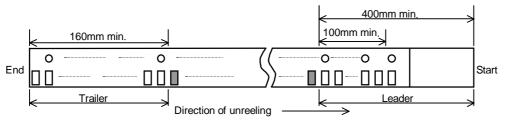


Figure-8

9. Marking on package

The label of a minimum package shall be legibly marked with follows.

9.1 Marking A

- (1) Classification (Style, Peak voltage, Rated voltage, Optional code, Packaging form) (2) Quantity (3) Lot number
- (4) Manufacturer's name or trade mark (5) Others
- 9.2 Marking B (KAMAYA Control label)

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