



# **WW25N**

±1%, ±5%, 2W

Ultra low ohm power chip resistors

Size 2512 (6432)

Metal Plate Current Sensing Type RoHS Exemption free and Lead free products Halogen free

Rishiechnology Corport

\*Contents in this sheet are subject to change without prior notice.

Oct- 2024



## FEATURE

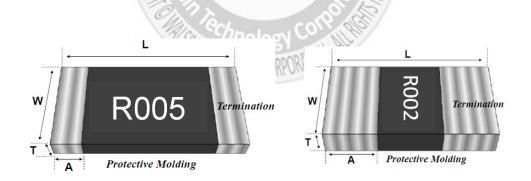
- 1. Ultra low and stable TCR performance
- 2. High power rating and compact size
- 3. High reliability and stability
- 4. Reduced size of final equipment
- 5. RoHS Exemption free and Lead free products
- 6. Inductance below 5nH

## APPLICATION

- Power supply
- PDA
- Digital meter
- Computer
- Automotives
- Battery charger
- DC-DC power converter

## DESCRIPTION

The resistors are constructed in a high grade low resistive metal body. The resistive layer is covered with a protective coat and printed a resistance marking code over it. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a tin (lead-free) alloy.



Item	Protective Molding	<b>Resistive Element</b>	Internal Terminal	External Terminal
Material	Resin	Alloy Metal	Copper	Solder

Fig 1. Construction of Chip-R

## QUICK REFERENCE DATA

ltem	Gene	ral Specification		
Series No.		WW25N		
Size code	2	512 ( 6432 )		
Resistance Tolerance		±5%, ±1%		
Resistance Value	0.001Ω, 0.002Ω	$\begin{array}{c} 0.003\Omega, 0.004\Omega, 0.005\Omega, 0.006\Omega,\\ 0.007\Omega, 0.008\Omega, 0.009\Omega, 0.010\Omega,\\ 0.012\Omega, 0.015\Omega, 0.018\Omega, 0.020\Omega,\\ 0.022\Omega, 0.025\Omega, 0.030\Omega, 0.033\Omega,\\ 0.035\Omega, 0.040\Omega, 0.050\Omega, 0.060\Omega,\\ 0.070\Omega, 0.075\Omega, 0.080\Omega, 0.100\Omega \end{array}$		
TCR (ppm/°C)	≤±70 ppm/°C	≤±50 ppm/°C		
Max. dissipation at T <sub>amb</sub> =70°C	2 W			
Max. Operation current (DC or RMS)	SQRT ( F	Power / Resistance)		
Operation temperature	-	-55 ~+170°C		

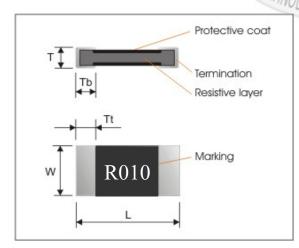
Note :

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

 $RCWV = \sqrt{Rated Power \times Resistance Value}$  or Max. RCWV listed above, whichever is lower.

- 3. Please keep the surface temperature do not exceed 105°C when operating.
- 4. \*\* : TCR Hot (+25~+155°C)
- 5. R-value might be variance depend on soldering conditions and please consider this influence before use milli-ohm resistors, and strongly suggest use the recommend solder pad to design your circuits
- 6. \*Max. working &. Max. overload current details please refer Annex. 1

#### **MECHANICAL DATA**



Symbol	R001, R002	R003 ~ R100
L	6.40±0.20	6.20±0.20
W	3.25±0.20	3.25±0.20
т	0.75±0.20	0.60±0.20
Tt	2.00±0.20	0.80±0.20
Tb	2.00±0.20	0.80±0.20



## CATALOGUE NUMBERS

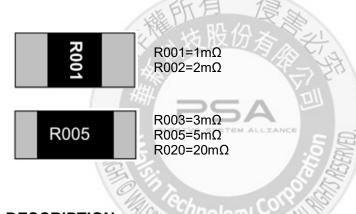
The resistors have a catalogue number starting with .

WW25	N	R005	J	т	L
Size code	Type code	Resistance code	Tolerance	Packaging code	Termination code
WW25 : 2512	N : 2W Sensing type	R is first digit followed by 3 significant digits. $0.010\Omega = R010$ $0.005\Omega = R005$	J :±5% F :±1%	T:7" reeled in tape Q:10" reel in tape	L = Sn base (lead free)

Reeled tape packaging : 12mm width plastic emboss taping 4,000pcs per 7" reel. 8,000pcs per 10" reel.

#### MARKING

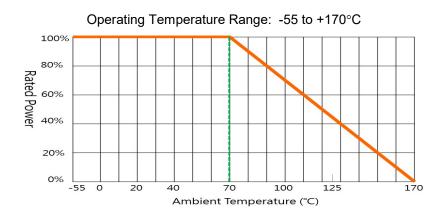
Each resistor is marked with a four-digit code on the protective coating to designate the nominal resistance value.



## FUNCTIONAL DESCRIPTION

#### Derating curve

The power that the resistor can dissipate depends on the operating temperature; see Fig.2





#### MOUNTING

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

The end terminations guarantee a reliable contact.

#### **Storage and Handling Conditions:**

- 1. Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.
- 2. To store products with following condition:

Temperature :5 to 40°C

Humidity :20 to 70% relative humidity

- 3. Caution:
  - a. Don't store products in a corrosive environment such as sulfide, chloride gas, or acid.

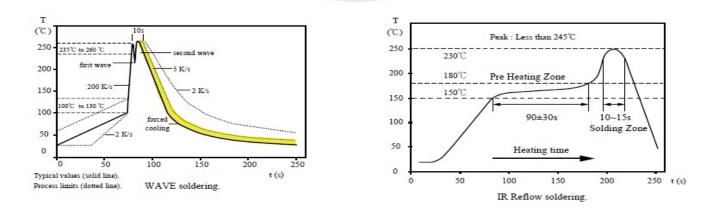
It may cause oxdization of electrode, which easily be resulted in poor soldering.

- b. To store products on the shelf and avoid exposure to moisture.
- c. Don't expose products to excessive shock, vibration, direct sunlight and so on.

#### SOLDERING CONDITION

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds. Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.





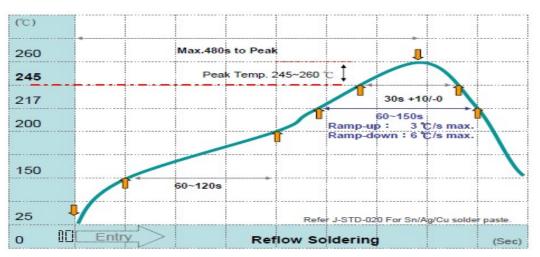
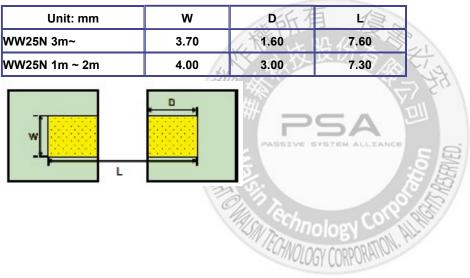


Fig 3. Infrared soldering profile for Chip Resistors WW25



## FOOT PRINT



## TEST AND REQUIREMENTS(JIS C 5201-1 : 1998)

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range : Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied :

Temperature: 15°C to 35°C.

Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar).

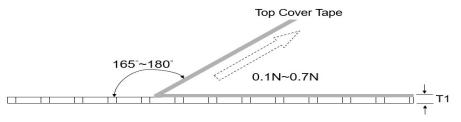
All soldering tests are performed with midly activated flux.

TEST	PROCEDURE	REQUIREMENT
Temperature Coefficient of Resistance(T.C.R) <b>Clause 4.8</b>	Natural resistance change per change in degree centigrade. $\frac{R_2 - R_1}{R_1(t_2 - t_1)} \times 10^6 \text{ (ppm/°C)}  t_1 : 20^\circ\text{C}+5^\circ\text{C}-1^\circ\text{C}$ R <sub>1</sub> : Resistance at reference temperature R <sub>2</sub> : Resistance at test temperature	Refer to "QUICK REFERENCE DATA"
Short time overload (S.T.O.L) <b>Clause 4.13</b>	Permanent resistance change after a 5second application of 5 times rated power	no visible damage $\Delta$ R/R max. ±(1%+0.0001 $\Omega$ )
Resistance to soldering heat(R.S.H) Clause 4.18	Un-mounted chips completely immersed for $10\pm1$ second in a SAC solder bath at $260^\circC\pm5^\circC$	no visible damage $\Delta$ R/R max. ±(1%+0.0001 $\Omega$ )
Solderability Clause 4.17	Un-mounted chips completely immersed for 5±0.5second in a SAC solder bath at 235 $^\circ\!C\pm\!5^\circ\!C$	good tinning (>95% covered) no visible damage
Temperature cycling Clause 4.19	30 minutes at -55°C±3°C, 2~3 minutes at 20°C+5°C-1°C, 30 minutes at +155°C±3°C, 2~3 minutes at 20°C+5°C-1°C, total 5 continuous cycles	no visible damage $\Delta R/R$ max. $\pm(1\%+0.0001\Omega)$
Load life (endurance) Clause 4.25	1000 +48/-0 hours, loaded with RCWV or Vmax in chamber controller $70\pm2^{\circ}$ C, 1.5 hours on and 0.5 hours off	no visible damage $\Delta$ R/R max. ±(2%+0.0001 $\Omega$ )
Load life in Humidity Clause 4.24	1000 +48/-0 hours, loaded with RCWV or Vmax in humidity chamber controller at 40°C±2°C and 90~95% relative humidity, 1.5hours on and 0.5 hours off	no visible damage $\Delta$ R/R max. ±(2%+0.0001 $\Omega$ )
Adhesion Clause 4.32	Pressurizing force: 5N, Test time: 10±1sec.	No remarkable damage or removal of the terminations

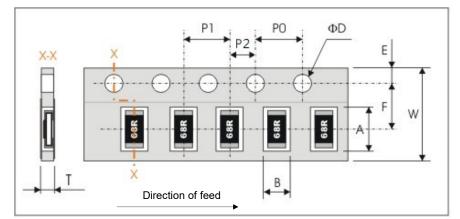
## PACKAGING

#### Peel Strength of Top Cover Tape

The peel speed shall be about 300 mm/min The peel force of top cover tape shall between 0.1 to 0.7N



#### Plastic Tape specifications (unit :mm)

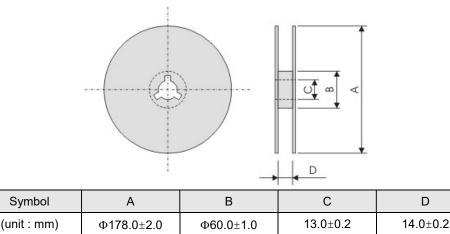


Symbol	А	В	W	F	E
Dimensions	6.75±0.20	3.50±0.20	12.00±0.30	5.50±0.10	1.75±0.10
-	25	2.	10 6	4	

Symbol	P1	P0	P2	ΦD	Т
Dimensions 4.0	00±0.10	4.00±0.10	2.00±0.10	$\Phi 1.50^{+0.1}_{-0.0}$	1.00±0.20

WULUGY LUKPUNN

#### **Reel dimensions**



#### **Taping quantity**

- Chip resistors 4,000 pcs per reel.

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Approval sheet



2512 Rating Power 1.0W			2512 Rating Power 2.0W		
R_Value	Max.	Max.	R_Value	Max.	Max.
(mΩ)	Working (A)	Overload (A)	(mΩ)	Working (A)	Overload (A)
1	31.6	70.7	1	44.7	100.0
2	22.4	50.0	2	31.6	70.7
2.5	20.0	44.7	2.5	28.3	63.2
3	18.3	40.8	3	25.8	57.7
4	15.8	35.4	4	22.4	50.0
5	14.1	31.6	5	20.0	44.7
6	12.9	28.9	6	18.3	40.8
7	12.0	26.7	7	16.9	37.8
8	11.2	25.0	8	15.8	35.4
9	10.5	23.6	9	14.9	33.3
10	10.0	22.4	10	14.1	31.6
12	9.1	20.4	12	12.9	28.9
15	8.2	18.3	15	11.5	25.8
18	7.5	16.7	18	10.5	23.6
20	7.1	15.8	20	10.0	22.4
22	6.7	15.1	22	9.5	21.3
25	6.3	14.1	25	8.9	20.0
30	5.8	12.9	30	8.2	18.3
33	5.5	12.3	33.	7.8	17.4
35	5.3	12.0	35	7.6	16.9
40	5.0	11.2	40	7.1	15.8
50	4.5	10.0	50	6.3	14.1
60	4.1	9.1	60	5.8	12.9
70	3.8	8.5	70	5.3	12.0
75	3.7	8.2	75	5.2	11.5
80	3.5	7.9	80	5.0	11.2
100	3.2	7.1	100	4.5	10.0
		PASSIV	150	LITANCE 3.7	8.2

## Annex. 1 Max. working &. Max. overload current



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