

# APPROVAL SHEET

WR10\_J, WR12\_J, WR08\_J, WR06\_J, WR04\_J

±1%, ±5%, Jumper

Thick Film General Purpose Chip Resistors Size 1210, 1206, 0805, 0603, 0402 RoHS 2 Compliant with exemption 7C-I

Halogen free

#### **FEATURES**

- 1. Automotive grade AEC Q-200 qualified
- 2. 100% CCD inspection
- 3. RoHS 2 compliant with exemption 7C-I and Halogen free products
- 4. Flammability against UL94-V0

#### **APPLICATIONS**

- Consumer electrical equipment
- EDP, Computer application
- Telecom application

#### DESCRIPTION

The resistors are constructed in a high grade ceramic body (aluminum oxide). Internal metal electrodes are added at each end and connected by a resistive paste that is applied to the top surface of the substrate. The composition of the paste is adjusted to give the approximate resistance required and the value is trimmed to within tolerance by laser cutting of this resistive layer.

The resistive layer is covered with a protective coat. Finally, the two external end terminations are added. For ease of soldering the outer layer of these end terminations is a Tin (lead free) alloy.

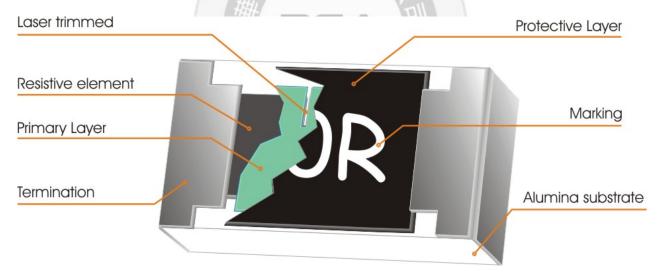


Fig 1. Construction of Chip-R

#### QUICK REFERENCE DATA

Item		G	eneral Specification	on			
Series No.	WR10	WR12	WR08	WR06	WR04		
Size code	1210(3225)	1206(3216)	0805(2012)	0603(1608)	0402(1005)		
Decistores Dange		1Ω~	$\cdot$ 10Μ $\Omega$ (±5% toleran	ice),			
Resistance Range		1Ω~	·10MΩ (±1% toleran	ice),			
Resistance Tolerance			±1%, E96/E24				
Resistance Tolerance	±5%, E24						
TCR (ppm/°C)							
$10M\Omega\!\ge\!\!R\!>\!10\Omega$			$\leq$ $\pm$ 100				
R≤10Ω			-200~+400				
Max. dissipation @ T <sub>amb</sub> =70°C	1/2 W	1/4 W	1/8 W	1/10 W	1/16 W		
Max. Operation Voltage	200V	200V	150V	75V	50V		
Max. Overload Voltage	400V 400V 300V 150V 100						
Operation temperature	-55 ~ +155°C						

#### Note:

- 1. This is the maximum voltage that may be continuously supplied to the resistor element, see "IEC publication 60115-8"
- 2. Max. Operation Voltage : So called RCWV (Rated Continuous Working Voltage) is determined by

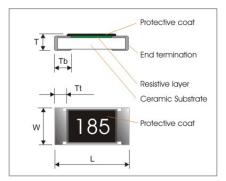
 $RCWV = \sqrt{Rated Power \times Resistance Value}$  or Max. RCWV listed above, whichever is lower.

## For Jumper $(0\Omega)$ :

	Item		General Specification						
Series No.		WR10	WR12	WR08	WR06	WR04			
Size code		1210(3225) 1206(3216) 0805(2012) 0603(1608) 0402(							
Resistance	e Range	≤50mΩ for P tolerance ≤30mΩ for F tolerance							
TCR (ppm/°C)  TCR is not applicable for Jumper product									
Max. dissip @ T <sub>amb</sub> =70		1/2 W	1/4 W o g \	1/8 W	1/10 W	1/16 W			
Р	Rated Current	3A	2AV MRP(	1.6A	1A	1A			
tolerance	Peak Current	7.5A	5A	3.2A	3A	2A			
F	Rated Current	4A	2.9A	2A	1.8A	1A			
tolerance	Peak Current	10A	5.8A	4A	3.6A	2A			
Operation temperature -55 ~ +155°C				·					

## **DIMENSIONS** (unit: mm)

	WR10	WR12	WR08	WR06	WR04
L	$3.10 \pm 0.10$	$3.10 \pm 0.10$	$2.00 \pm 0.10$	1.60 ± 0.10	$1.00 \pm 0.05$
W	$2.60 \pm 0.10$	$1.60 \pm 0.10$	$1.25 \pm 0.10$	$0.80 \pm 0.10$	$0.50 \pm 0.05$
Т	$0.55 \pm 0.10$	$0.55 \pm 0.10$	$0.50 \pm 0.15$	$0.45 \pm 0.15$	$0.35\pm0.05$
Tb	$0.50 \pm 0.20$	$0.45\pm0.20$	$0.40 \pm 0.20$	$0.30 \pm 0.15$	$0.25 \pm 0.10$
Tt	$0.50\pm0.20$	$0.50\pm0.20$	$0.40\pm0.20$	$0.30\pm0.10$	$0.20 \pm 0.10$





## **CATALOGUE NUMBERS**

The resistors have a catalogue number starting with

WR12	х	472_	J	Т	L	L
Size code	Type code	Resistance code	Tolerance code	Packaging code	Termination code	Special code
WR10: 1210 WR12: 1206 WR08: 0805 WR06: 0603 WR04: 0402	$\mathbf{X}$ : Jumper $\pm 5\% \ 1\Omega \sim 10 \mathrm{M}\Omega$ $\pm 1\% \ 10\Omega \sim 1 \mathrm{M}\Omega$ $\mathbf{W}$ : $\pm 1\% \ 1\Omega < R < 10\Omega$ $\pm 1\% .1 \mathrm{M}\Omega < R < 10 \mathrm{M}\Omega$	$\pm 5\%$ ,E24: 2 significant digits followed by no. of zeros and a blank $4.7\Omega$ =4R7_ $10\Omega$ =100_ $220\Omega$ =221_ Remark: "" means a blank $\pm 1\%$ ,E24+E96: 3 significant digits followed by no. of zeros $102\Omega$ =1020 $37.4K\Omega$ =3742 $82\Omega$ =82R0  For Jumper (0Ω) P tol. ≤ $50m\Omega$ =000_ F tol. ≤ $30m\Omega$ =0000 Remark: "_" means a blank	F: ±1% J: ±5% For Jumper (0Ω) P: ≤ $50mΩ$ F: ≤ $30mΩ$	T :7" Reeled taping  Q :10" Reeled taping  G :13" Reeled taping  H :13" reel 50Kpcs only for 0402  B :Bulk  D :7" reel 20Kpcs only for 0402  A :7" reel 15Kpcs only for 0402	L:Sn base (lead-free)	J = Automotive grade AEC Q200 compliant

#### Remark:

1210, 1206, 0805, 0603:

- 1. 8mm width paper taping 5000pcs per 7" reel, 10kpcs per 10" reel, 20kpcs per 13" reel.
- 2. Bulk packaging: 5000pcs per poly-bag

#### 0402:

- 1. 8mm width paper taping 10,000pcs per 7" reel, 70kpcs per 13" reel.
- 2. Bulk packaging: 5000pcs per poly-bag

#### **MARKING**

Size \ Nr. Of digit of code\tolerance	±5% & Jumper (0Ω)	±1%
1210 (3225)	3 digits marking	4 digits marking
1206 (3216)	3 digits marking	4 digits marking
0805 (2012)	3 digits marking	4 digits marking
0603 (1608)	3 digits marking	3 digits marking
0402 (1005)	NO MA	RKING

#### 3 digits marking [ $\pm 5\%$ : 1210,1206, 0805, 0603 & Jumper (0 $\Omega$ )]

Each resistor is marked with a three digits code on the protective coating to designate the nominal resistance value.

## 3 digits marking $(\pm 1\%:0603)$

Nomina	ıl resistar	nce	Description												
1.E24 s	eries	2 si	gnificant o	digits fol	lowed by	No. of ze	eros .As 0	603 WR	206X ±5%	)					
			Exam												
				SISTAN		4.7Ω	47Ω		70Ω	4K70	47K0			M70	
			3 di	gits mar	king	4R7	470	4	171	472	473	47	4 4	475	
2.E96 s	eries	The	1st two d	igit code	es are ref	erring to	the COD	E on the	table, the	e 3rd co	de is the	ndex of	resistand	e value.	
		Rep	oeat value	s betwe	en E24 aı	nd E96 s	eries, wh	ose mar	king are l	pased or	the E96	CODE t	table.		
			Code	Z	10/1	V. F	xi发	10 ×	B	10	D	Е		=	G
		-	Multiplier	10	3 10	-2 1	0 <sup>-1</sup>	A 10 <sup>0</sup>	101	10 <sup>2</sup>	10 <sup>3</sup>	104	<u> </u>	) <sup>5</sup> -	10 <sup>6</sup>
		'	-		77X/77		U	10	2F_	7.91	10	10	11	,	10
			Exam		IOF	4 700	47.00		700	41670	471/0	470	214	M70	
				SISTAN		1.78Ω	17.8Ω		78Ω	1K78	17K8			M78	
			3 di	gits mar	king	25Y	25X		25A	25B	25C	25	D 2	25E	
3. Rem	ark	The	ere is no m	narking f	or the ite	ms not u	nder E24	and E96	series.		5				
E96 CC	DE table:	I		18	2 2					3/3	57				
CODE	R value	CODE	R-value	CODE	R-Value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value	CODE	R-value
01	100	13	133	25	178	37	237	49	316	61	422	73	562	85	750
02	102	14	137	26	182	38	243	50	324	62	432	74	576	86	768
03	105	15	140	27	187	39	249	51	332	63	442	75	590	87	787
04	107	16	143	28	191	40	255	52	340	64	453	76	604	88	806
05	110	17	147	29	196	41//	261	53	348	65	464	77	619	89	825
06	113	18	150	30	200	42	267	54	357	66	475	78	634	90	845
07	115	19	154	31	205	43	274	55	365	67	487	79	649	91	866
80	118	20	158	32	210	44	280	56	374	68	499	80	665	92	887
09	121	21	162	33	215	45	287	57	383	69	511	81	681	93	909
10	124	22	165	34	221	46	294	58	392	70	523	82	698	94	931
11	127	23	169	35	226	47	301	59	402	71	536	83	715	95	953
12	130	24	174	36	232	48	309	60	412	72	549	84	732	96	976

## **4 digits marking** ( $\pm 1\%$ : 1210,1206,0805 )

Each resistor is marked with a four digits code on the protective coating to designate the nominal resistance value. For values below  $97\Omega6$  the R is used as a digit. For values of  $100\Omega$  or greater, the first 3 digits are significant, and the fourth digit indicates the number of multiple to follow.

#### Example

RESISTANCE	Jumper (0Ω)	4.7Ω	10Ω	12Ω	100Ω	6800Ω	47000Ω	470000Ω
3 digits marking (1210, 1206, 0805, 0603 ±5% & Jumper)	000	4R7	100	120	101	682	473	474
4 digits marking	-	4R70	10R0	12R0	1000	6801	4702	4703

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#### **FUNCTIONAL DESCRIPTION**

#### Product characterization

Standard values of nominal resistance are taken from the E24 series for resistors with a tolerance of  $\pm 5\%$ , and E96 series for resistors with a tolerance of  $\pm 1\%$ . The values of the E24/E96 series are in accordance with "IEC publication 60063"

#### **Derating**

The power that the resistor can dissipate depends on the operating temperature; see Fig.2

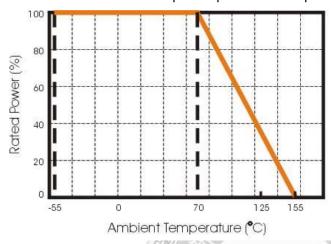


Figure 2 Maximum dissipation in percentage of rated power as a function of the ambient temperature

#### **MOUNTING**

Due to their rectangular shapes and small tolerances, Surface Mountable Resistors are suitable for handling by automatic placement systems.

SYSTEM ALLIANCE

Chip placement can be on ceramic substrates and printed-circuit boards (PCBs).

Electrical connection to the circuit is by individual soldering condition.

## **Storage and Handling Conditions:**

- 1. Products are recommended to be used up within two years since operation date as ensured shelf life. Check solderability in case shelf life extension is needed.
- 2. To store products with following condition:

Temperature :5 to 40°C

Humidity :20 to 70% relative humidity

- 3. Caution:
  - a. Don't store products in a corrosive environment such as sulfide, chloride gas, or acid.

It may cause oxdization of electrode, which easily be resulted in poor soldering.

- b. To store products on the shelf and avoid exposure to moisture.
- c. Don't expose products to excessive shock, vibration, direct sunlight and so on

## **SOLDERING CONDITION follows J-STD-020D**

The robust construction of chip resistors allows them to be completely immersed in a solder bath of 260°C for 10 seconds. Therefore, it is possible to mount Surface Mount Resistors on one side of a PCB and other discrete components on the reverse (mixed PCBs).

Surface Mount Resistors are tested for solderability at 235°C during 2 seconds. The test condition for no leaching is 260°C for 30 seconds.

Typical examples of soldering processes that provide reliable joints without any damage are given in Fig 3.

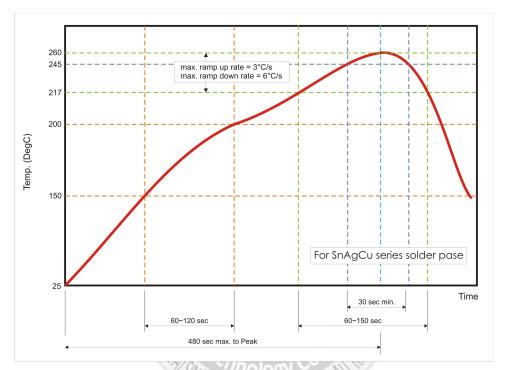


Fig 3. Recommended IR reflow soldering profile for SMT process with SnAgCu series solder paste

#### **TESTS AND REQUIREMENTS**

Essentially all tests are carried out according to the schedule of IEC publication 115-8, category LCT/UCT/56(rated temperature range: Lower Category Temperature, Upper Category Temperature; damp heat, long term, 56 days). The testing also meets the requirements specified by EIA, EIAJ and JIS.

The tests are carried out in accordance with IEC publication 68, "Recommended basic climatic and mechanical robustness testing procedure for electronic components" and under standard atmospheric conditions according to IEC 60068-1, subclause 5.3. Unless otherwise specified, the following value supplied:

Temperature: 15°C to 35°C. Relative humidity: 45% to 75%.

Air pressure: 86kPa to 106 kPa (860 mbar to 1060 mbar). All soldering tests are performed with midly activated flux.

TEST	PROCEDURE / TEST METHOD	REQUIREMENT	
		Resistor	0Ω
Electrical Characteristics  JISC5201-1: 1998	- DC resistance values measurement - Temperature Coefficient of Resistance (T.C.R) Natural resistance change per change in degree	Within the specified tolerance Refer to "QUICK REFERENCE DATA"	<50mΩ
Clause 4.8 $\frac{R_2-R_1}{R_1(t_2-t_1)}\times 10^6 \text{ (ppm/°C)}  t_1:20^\circ\text{C+5°C-1°C}$ $R_1: \text{Resistance at reference temperature}$ $R_2: \text{Resistance at test temperature}$ Resistance to soldering Un-mounted chips completely immersed for			
Resistance to soldering heat(R.S.H) MIL-STD-202 method 210	Un-mounted chips completely immersed for 10±1second in a SAC solder bath at 270°C ±5°C	$\Delta$ R/R max. $\pm$ (1.0%+0.05 $\Omega$ ) no visible damage	<50mΩ
Solderability J-STD-002	<ul> <li>a) Bake the sample for 155°C dwell time 4hrs/ solder dipping 235°C / 5sec.</li> <li>b) Steam the sample dwell time 1 hour/ solder dipping 260°C / 5sec.</li> </ul>	95% coverage min., good tini no visible damage	ning and
Temperature cycling JESD22 Method JA-104	1000 cycles, -55°C $\sim$ +155°C, dwell time 30min. / 1min. max. transition time	$\Delta$ R/R max. $\pm$ (1.0%+0.05 $\Omega$ ) No visible damage	<50mΩ
Moisture Resistance MIL-STD-202 method 106	65±2°C, 80~100% RH, 10 cycles, 24 hours/ cycle	$\Delta$ R/R max. $\pm$ (1.0%+0.05 $\Omega$ ) No visible damage	<50mΩ
Bias Humidity MIL-STD-202 method 103	1000+48/-0 hours; 85°C, 85% RH, 10% of operation power	$\Delta$ R/R max. $\pm$ (2.0%+0.10 $\Omega$ ) No visible damage	<50mΩ
Operational Life MIL-STD-202 method 108	1000+48/-0 hours; 35% of operation power, 125±2°C	$\Delta$ R/R max. $\pm$ (2.0%+0.1 $\Omega$ ) No visible damage	<50mΩ

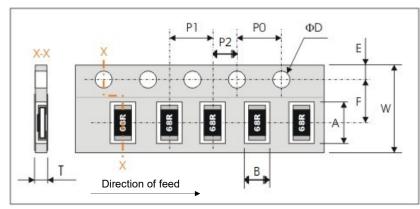


## **Approval sheet**

TEST	PROCEDURE / TEST METHOR	REQUIREMENT		
TEST	PROCEDURE / TEST METHOD	Resistor	0Ω	
High Temperature	1000+48/-0 hours; without load in a temperature	$\Delta$ R/R max. $\pm$ (2.0%+0.10 $\Omega$ )		
Exposure MIL-STD-202	chamber controlled 155±3°C	No visible damage	<50mΩ	
Method 108				
External Visual	Electrical test not required. Inspect device construction, marking and workmanship	No visible damage		
Physical Dimension	The chip dimension(L, W, T, Tb, Tt) prescribed in the detail specification shall be checked by Protech 2.5D.	Within the specified tolerance	e for WTC	
Board Flex	Resistors mounted on a 90mm glass epoxy resin	$\Delta$ R/R max. $\pm$ (1.0%+0.05 $\Omega$ )		
AEC-Q200-005	PCB(FR4),	No visible damage <50mΩ		
	bending once 2mm for 10sec.			
Terminal strength	Force: 1.8Kg, ( only 1Kg for 0402 )Test time:	No remarkable damage or removal of		
AEC-Q200-006	60±1sec.	the terminations		
Thermal shock	Test –55 to 155°C / dwell time 15min/ Max transfer	$\Delta$ R/R max. $\pm$ (1.0%+0.05 $\Omega$ )		
MIL-STD-202	time 20sec 300cycles	No visible damage	<50mΩ	
method 107	XX HB 份X SUL			
ESD AEC-Q200-002	Test contact 1.0KV (0.5KV for 0402 only)	$\Delta$ R/R max. $\pm$ (1.0%+0.05 $\Omega$ ) No visible damage	<50mΩ	
Short Time Overload	2.5 times RCWV or max. overload voltage, for	$\Delta$ R/R max. $\pm$ (2.0%+0.10 $\Omega$ )		
JISC5201-1: 1998	5seconds	No visible damage	<50mΩ	
Clause 4.13	8 PASSIVE SYSTEM ALLIANCE	9		
Load life in Humidity	1000 +48/-0 hours, loaded with RCWV or Vmax in	$\Delta$ R/R max. $\pm$ (2%+0.10 $\Omega$ )		
humidity chamber controller at 40°C±2°C and 9 relative humidity, 1.5hours on and 0.5 hours of		No visible damage	<50mΩ	
Clause 4.24	relative Hulliluity, 1.5 nours off affu 0.5 flours off			
Load life (endurance)	1000 +48/-0 hours, loaded with RCWV or Vmax in	$\Delta$ R/R max. $\pm$ (2%+0.10 $\Omega$ )		
JISC5201-1: 1998	chamber controller 70±2°C, 1.5 hours on and 0.5 hours off	ours on and 0.5 No visible damage <5		
Clause 4.25	2001 00111 2.			

## **PACKAGING**

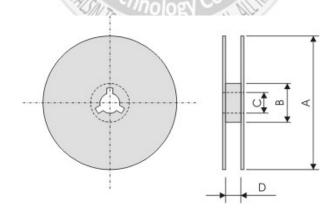
## Paper Tape specifications (unit :mm)



Series No.	Α	В	W	F	E
WR10	3.60±0.20	3.00±0.20			
WR12	3.60±0.20	2.00±0.20	13		
WR08	2.40±0.20	1.65±0.20	8.00±0.30	3.50±0.20	1.75±0.10
WR06	1.90±0.20	1.10±0.20	1 会 1 天上		
WR04	1.20±0.10	0.70±0.10	1/2 VO		

Series No.	P1	P0	P2	ΦD	Т
WR10/12/08	4.00.0.40	7	A		0.80±0.1
WR06	4.00±0.10	4.00±0.10	2.00±0.10	Φ1.50 <sup>+0.1</sup> <sub>-0.0</sub>	0.70±0.05
WR04	2.00±0.10			25	0.50±0.05

#### **Reel dimensions**



Symbol	A	В	С	D
7" reel	Φ178.0±2.0	Φ60.0±1.0	13.0±0.2	9.0±0.5
10" reel	Φ254.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5
13" reel	Ф330.0±2.0	Φ100.0±1.0	13.0±0.2	9.0±0.5

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WR04X1303FTLJ WR04W\_FTLJ WR04X\_FTLJ WR04X\_JTLJ WR06W\_FTLJ WR06X\_FTLJ WR06X\_JTLJ WR08W\_FTLJ WR08X\_FTLJ WR08X\_JTLJ WR12W\_FTLJ WR12X\_FTLJ WR12X\_JTLJ WR04W\_FGLJ WR04X\_FGLJ WR04X\_JGLJ WR04X\_PGLJ WR06X\_PGLJ WR06X\_FGLJ WR06X\_PGLJ WR06X\_PGLJ WR08X\_PGLJ WR08X\_PGLJ WR08X\_PGLJ WR08X\_PTLJ WR12W\_FGLJ WR12X\_FGLJ WR12X\_PGLJ WR12X\_PGLJ WR12X\_PGLJ WR04X1963FTLJ